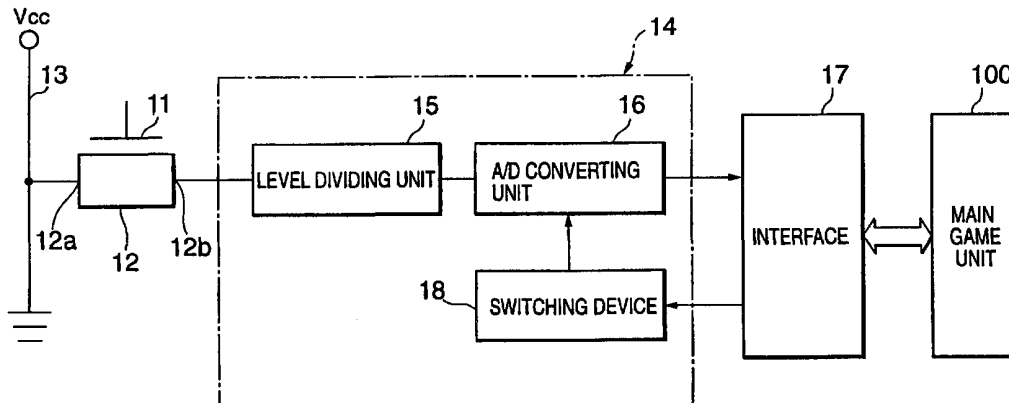




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<p>(21) International Application Number: PCT/JP00/02625</p> <p>(22) International Filing Date: 21 April 2000 (21.04.00)</p> <p>(30) Priority Data:</p> <table border="0"> <tr> <td>11/117101</td> <td>23 April 1999 (23.04.99)</td> <td>JP</td> </tr> <tr> <td>11/297203</td> <td>11 September 1999 (11.09.99)</td> <td>JP</td> </tr> <tr> <td>11/367067</td> <td>24 December 1999 (24.12.99)</td> <td>JP</td> </tr> </table> <p>(71) Applicant: SONY COMPUTER ENTERTAINMENT INC. [JP/JP]; 1-1, Akasaka 7-chome, Minato-ku, Tokyo 107-0052 (JP).</p> <p>(72) Inventors: OGATA, Hiroki; Sony Computer Entertainment Inc., 1-1, Akasaka 7-chome, Minato-ku, Tokyo 107-0052 (JP). TAGAWA, Kazusato; Sony Computer Entertainment Inc., 1-1, Akasaka 7-chome, Minato-ku, Tokyo 107-0052 (JP). NAKAZAWA, Hiroyuki; Sony Computer Entertainment Inc., 1-1, Akasaka 7-chome, Minato-ku, Tokyo 107-0052 (JP). SAKAKURA, Youtaro; Alps Electric Co., Ltd., 1-7, Yukigaya-otsuka-cho, Ota-ku, Tokyo 145-8501 (JP). MITA, Koji; Alps Electric Co., Ltd. 1-7, Yuki-gaya-otsuka-cho, Ota-ku, Tokyo 145-8501 (JP).</p>		11/117101	23 April 1999 (23.04.99)	JP	11/297203	11 September 1999 (11.09.99)	JP	11/367067	24 December 1999 (24.12.99)	JP	<p>(74) Agent: YAMAMOTO, Toshitake; 301, Ogikubo Sunny Garden, 28-9, Ogikubo 4-chome, Suginami-ku, Tokyo 167-0051 (JP).</p> <p>(81) Designated States: AU, BR, CA, CN, JP, KR, MX, NZ, RU, SG, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Published With international search report.</p>
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(54) Title: OPERATING DEVICE



(57) Abstract

It is an object of the present invention to allow both a digital operation and an analog operation by using pressing operation continuously. An operating device has detecting elements (e.g., pressure-sensitive elements (12)) for outputting analog signals according to pressing operations of operators (11), and is configured such that analog signals output from the detecting elements are subjected to level dividing by a level dividing unit (15) and converted into multi-bit digital signals corresponding to the output levels thereof by an A/D converting unit (16), and is also configured so as to output single-bit digital signals according to change in analog signals output from the detecting element. A switching unit (18) output one or the other of the multi-bit digital signals and single-bit digital signals.

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DESCRIPTION

OPERATING DEVICE

5 BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to an operating device (also called a "controller") used as peripheral equipment for entertainment apparatuses such as video game apparatuses and the like.

10 Description of the Related Art

Generally, with entertainment apparatuses such as video game apparatuses and the like, various operations are executed using an operating device. Accordingly, multiple operating buttons are provided on the operating device, and the user controls the entertainment apparatus by operating these buttons in various ways, thereby moving
15 a character displayed on a television set, for example.

Conventionally, many of such operating devices have been configured with a cross-shaped or round direction indicating operating button positioned on the left side of the front thereof, and multiple multi-purpose buttons on the right side of the front thereof.

20 These indicating operating buttons and multi-purpose buttons are configured of tact switches of rubber switches, and characters are digitally moved or the states of characters are digitally changed by performing on/off operation of these switches.

As described above, with conventional entertainment apparatuses, the direction indicating operating buttons and the multi-purpose buttons only have functions for
25 digitally changing characters displayed on a television set, so the movement and state change of characters become non-continuous, and thus there has been the problem of being jerky when viewed.

In order to solve this problem, with the game apparatus operating device disclosed in Japanese Unexamined Patent Application Publication No. H7-88252 adds analog input devices configured of a trackball, joystick, etc., so that the character displayed on the display screen of the television set can be controlled in an analog manner. Another example of conventional art with such an analog input device added is disclosed in Japanese Unexamined Patent Application Publication No. 11-90042.

However, the operability of analog input devices configured of a trackball, joystick, etc., differs greatly from that of the above described direction indicating operating buttons and multi-purpose buttons, so it takes time for users who are accustomed to using the direction indicating operating buttons and multi-purpose buttons to thoroughly learn how to operate such analog input devices, which could greatly diminish the enjoyment of the entertainment apparatus for which was intended.

SUMMARY OF THE INVENTION

The present invention has been made in light of such aspects, and accordingly, it is an object thereof to enable digital operating and analog operating, using pressure-operated operators used in conventional digital operating.

In order to achieve the above object, the operating device according to the present invention comprises: an operator operable by pressing; a detecting element for outputting analog signals corresponding to the pressing operation of the operator; first digital signal output means for converting analog signals output from the detecting element corresponding to the pressing operation of the operator into digital signals comprising multiple bits according to the output level thereof; second digital signal output means for outputting digital signals comprising single bit according to change in the analog signals output from the detecting element; and switching means for switching output between digital signals output from the first digital signal output means and digital signals output from the second digital signal output means.

According to the configuration of the present invention, with a single operator pressing operation, a digital signal comprising multiple bits enabling analog control is output from the first digital signal output means, and on the other hand a digital signal comprising a single bit enabling digital control is output from the second digital signal output means. Accordingly, arranging such that these digital signals are selected and output by switching means realizes digital operation and analog operation with a single operator.

Now, the detecting element may be a pressure-sensitive element placed at a position where pressing force acting upon the operator is transferred. Examples of such pressure-sensitive elements include resistor pressure-sensitive elements formed of pressure-sensitive electroconductive rubber. Hall devices or electrostatic device which output analog signals according to the pressing stroke of the operator may be applied, as well.

Also, the detecting element may comprise an electroconductive member which moves in conjunction with the operator and has elasticity, and a resistor placed at the position where the electroconductive member makes and breaks contact, wherein the resistor outputs analog signals according to the area of contact of the electroconductive member. Incidentally, the positional relationship of the electroconductive member and the resistor may be interchanged.

The electroconductive member preferably is of a configuration which deforms at the surface thereof facing the resistor according to the contact pressure applied thereupon with the resistor, such that the area of contact with the resistor changes, and may have forms such as the following:

- (1) A mountain-shaped longitudinal-sectional form.
- (2) A trapezoidal longitudinal-sectional form.
- (3) A shape wherein the cross-sectional area becomes smaller in steps toward the apex facing the resistor.

(4) The surface facing the resistor has a spherical shape.

On the other hand, the resistor may be formed so as to have a shape wherein the cross-sectional area thereof becomes smaller toward the apex facing the electroconductive member. Also, the resistor may be configured so as to divide the contact area of the electroconductive member into a plurality of contact areas by gaps, and to increase in steps the contact area of the electroconductive member in conjunction with the deformation thereof.

On the other hand, according to a second aspect of the present invention, an operating device comprises: an operator operable by pressing; a detecting element for outputting analog signals corresponding to the pressing operation of the operator; first digital signal output means for converting analog signals output from the detecting element corresponding to the pressing operation of the operator into digital signals comprising multiple bits according to the output level thereof; a digital switch for turning on and off according to the pressing operation of the operator; second digital signal output means for detecting the on/off state of the digital switch and outputting digital signals comprising single bits; and switching means for switching output between digital signals output from the first digital signal output means and digital signals output from the second digital signal output means.

With the configuration of this aspect of the present invention as well, with a single operator pressing operation, a digital signal comprising multiple bits enabling analog control is output from the first digital signal output means, and on the other hand a digital signal comprising a single bit enabling digital control is output from the second digital signal output means. Accordingly, arranging such that these digital signals are selected and output by switching means realizes digital operation and analog operation with a single operator.

Now, though according to one aspect of the present invention both multiple-bit digital signals and single-bit digital signals are generated from analog signals output

from the detecting element, according to another aspect of the present invention the multiple-bit digital signals are generated from analog signals output from the detecting element while the single-bit digital signals are output by detecting the on/off state of a digital switch.

5 Also, with the operating device according to the present invention, the digital switch may be configured containing first and second fixed terminals, and an electroconductive movable member which makes and breaks contact with the first and second fixed terminals in conjunction with movement of the operator.

 Also, in the above aspects, the first digital signal output means may comprise:
10 level dividing means for dividing into a plurality of levels the output level of analog signals output from the detecting element in conjunction with the pressing operation of the operator, and A/D converting means for converting the analog signals into digital signals, according to each output level divided by the level dividing means. Thus, multiple-bit digital signals can easily be output based on the output level of analog
15 signals output from the detecting element.

 Now, the level dividing means preferably uniformly divides into a plurality of levels the output level of the analog signals output from the detecting element in conjunction with the pressing operation of the operator. Uniformly dividing the output level of the analog signals output from the detecting element can provide natural and
20 smooth operability to be obtained which corresponds to the pressing force applied by the operator.

BRIEF DESCRIPTION OF THE DRAWINGS

 Fig. 1 is a plan view illustrating the overview of a video game apparatus with
25 which the operating device according to an embodiment of the present invention is used;

 Fig. 2 is an enlarged plan view of the operating device shown in Fig. 1;

Fig. 3 is a block diagram illustrating the principal portions of the operating device according to a first embodiment of the present invention;

Fig. 4 is a diagram illustrating the properties of the pressure-sensitive element shown in Fig. 3;

5 Fig. 5 is a block diagram illustrating an overall configuration example of the operating device according to the first embodiment of the present invention;

Fig. 6 is a block diagram illustrating a first configuration example for calibrating the level dividing unit;

10 Fig. 7 is a block diagram illustrating a second configuration example for calibrating the level dividing unit;

Fig. 8 is a flowchart illustrating an example of a setting program for the calibration, applied to the second configuration example shown in Fig. 7;

Fig. 9 is a flowchart illustrating another example of a setting program for the calibration, applied to the second configuration example shown in Fig. 7;

15 Fig. 10 is a block diagram illustrating a third configuration example for calibrating the level dividing unit;

Fig. 11 is a diagram for describing the calibration action with the third configuration example shown in Fig. 10;

20 Fig. 12 is a block diagram illustrating a fourth configuration example for calibrating the level dividing unit;

Fig. 13 is an exploded, perspective view of operating buttons (operators) provided in the second operating unit according to the first embodiment;

Fig. 14 is an exploded, perspective view of a first configuration example of the second operating unit according to the first embodiment;

25 Fig. 15 is a frontal cross-sectional view of the first configuration example of the second operating unit according to the first embodiment;

Fig. 16 is an exploded, perspective view of a second configuration example of

the second operating unit according to the first embodiment;

Fig. 17 is a frontal cross-sectional view of the second configuration example of the second operating unit according to the first embodiment;

Fig. 18 is an exploded, perspective view of a third configuration example of the
5 second operating unit according to the first embodiment;

Fig. 19 is a frontal cross-sectional view of the third configuration example of the second operating unit according to the first embodiment;

Fig. 20 is an exploded, perspective view illustrating a configuration example of a first operating unit according to the first embodiment;

10 Fig. 21 is a frontal cross-sectional view illustrating a configuration example of the first operating unit according to the first embodiment;

Fig. 22 is an exploded, perspective view illustrating a configuration example of a third operating unit according to the first embodiment;

15 Fig. 23 is a frontal cross-sectional view illustrating a configuration example of the third operating unit according to the first embodiment;

Fig. 24 is a block diagram illustrating the principal portions of the operating device according to a second embodiment of the present invention;

Fig. 25 is a block diagram illustrating an overall configuration example of the operating device according to the second embodiment of the present invention;

20 Fig. 26 is a block diagram illustrating another overall configuration example of the operating device according to the second embodiment of the present invention;

Fig. 27 is an exploded, perspective view of a first configuration example of the second operating unit according to the second embodiment;

25 Fig. 28 is a frontal cross-sectional view of the first configuration example of the second operating unit according to the second embodiment;

Fig. 29 is an exploded, perspective view of a second configuration example of the second operating unit according to the second embodiment;

Fig. 30 is a frontal cross-sectional view of the second configuration example of the second operating unit according to the second embodiment;

Fig. 31A is a plan view of a third configuration example of the second operating unit according to the second embodiment;

5 Fig. 31B is a bottom view of the third configuration example of the second operating unit according to the second embodiment;

Fig. 32 is a frontal cross-sectional view of the third configuration example of the second operating unit according to the second embodiment;

10 Fig. 33 is a frontal cross-sectional view of a fourth configuration example of the second operating unit according to the second embodiment;

Figs. 34A-34C are frontal cross-sectional views of a configuration example of the second operating unit according to a third embodiment of the present invention;

Fig. 35 is a diagram illustrating the circuit configuration of the resistor shown in Fig. 34;

15 Fig. 36 is a diagram illustrating the properties of analog signals output from the output terminal of the resistor shown in Fig. 35;

Fig. 37 is a block diagram illustrating the primary portions relating to the second operating unit according to the third embodiment;

20 Fig. 38 is a diagram for describing the functions of the division range setting unit for the second operating unit according to the third embodiment;

Fig. 39 is a frontal cross-sectional view of a configuration example of the first operating unit according to the third embodiment of the present invention;

Fig. 40 is a diagram illustrating the circuit configuration of the resistor shown in Fig. 39;

25 Fig. 41 is a diagram illustrating the properties of analog signals output from the output terminal of the resistor shown in Fig. 40;

Fig. 42 is a block diagram illustrating the primary portions relating to the first

operating unit according to the third embodiment;

Fig. 43 is a diagram for describing the functions of the division range setting unit for the first operating unit according to the third embodiment;

5 Figs. 44A-44D are diagrams illustrating a variation example of the detecting element, wherein

Fig. 44A is a frontal cross-sectional view of the operating unit including the detecting element;

Fig. 44B is a frontal view of the electroconductive member;

Fig. 44C is a view of the electroconductive member from below; and

10 Fig. 44D is a characteristic diagram for analog signals output from the output terminal of the resistor;

Figs. 45A-45D are diagrams illustrating another variation example of the detecting element, wherein

15 Fig. 45A is a frontal cross-sectional view of the operating unit including the detecting element;

Fig. 45B is a frontal view of the electroconductive member;

Fig. 45C is a view of the electroconductive member from below; and

Fig. 45D is a characteristic diagram for analog signals output from the output terminal of the resistor;

20 Figs. 46A-46D are diagrams illustrating another variation example of the detecting element, wherein

Fig. 46A is a frontal cross-sectional view of the operating unit including the detecting element;

Fig. 46B is a frontal view of the electroconductive member;

25 Fig. 46C is a view of the electroconductive member from below; and

Fig. 46D is a characteristic diagram for analog signals output from the output terminal of the resistor;

Figs. 47A-47D are diagrams illustrating another variation example of the detecting element, wherein

Fig. 47A is a frontal cross-sectional view of the operating unit including the detecting element;

5 Fig. 47B is a frontal view of the electroconductive member;

Fig. 47C is a view of the electroconductive member from below; and

Fig. 47D is a characteristic diagram for analog signals output from the output terminal of the resistor;

10 Figs. 48A-48D are diagrams illustrating another variation example of the detecting element, wherein

Fig. 48A is a frontal cross-sectional view of the operating unit including the detecting element;

Fig. 48B is a frontal view of the electroconductive member;

Fig. 48C is a view of the electroconductive member from above; and

15 Fig. 48D is a characteristic diagram for analog signals output from the output terminal of the resistor;

Figs. 49A-49D are diagrams illustrating another variation example of the detecting element, wherein

20 Fig. 49A is a frontal cross-sectional view of the operating unit including the detecting element;

Fig. 49B is a frontal view of the electroconductive member;

Fig. 49C is a view of the electroconductive member from above; and

Fig. 49D is a characteristic diagram for analog signals output from the output terminal of the resistor;

25 Figs. 50A-50D are diagrams illustrating another variation example of the detecting element, wherein

Fig. 50A is a frontal cross-sectional view of the operating unit including

the detecting element;

Fig. 50B is a frontal view of the electroconductive member;

Fig. 50C is a view of the electroconductive member from above; and

Fig. 50D is a characteristic diagram for analog signals output from the

5 output terminal of the resistor;

Figs. 51A-51D are diagrams illustrating another variation example of the detecting element, wherein

Fig. 51A is a frontal cross-sectional view of the operating unit including the detecting element;

10 Fig. 51B is a frontal view of the electroconductive member;

Fig. 51C is a view of the electroconductive member from above; and

Fig. 51D is a characteristic diagram for analog signals output from the

output terminal of the resistor;

Figs. 52A-52D are diagrams illustrating another variation example of the detecting element, wherein

15

Fig. 52A is a frontal cross-sectional view of the operating unit including the detecting element;

Fig. 52B is a plan view of the resistor;

Fig. 52C is a plan view of another example of the form of the resistor;

20 and

Fig. 52D is a plan view of yet another example of the form of the resistor; and

Fig. 53 is a frontal cross-sectional view of another variation example of the detecting element.

25

DESCRIPTION OF THE PREFERRED EMBODIMENTS

The embodiments of the present invention will hereinafter be described with

reference to the drawings.

The operating device according to the embodiment of the present invention is connected to a video game apparatus serving as an entertainment apparatus, and is capable of digital or analog control of characters displayed on the display screen of a television set.

(Overview of the Device)

Fig. 1 is a plan view illustrating the overview of a video game apparatus with which the operating device according to an embodiment of the present invention is used. As shown in the Fig. 1, the video game apparatus has a main game unit 100 connected to a television set to be used as a display, and an operating device 200 connected to the main game unit 100.

Built into the main game unit 100 are a disk driving unit 101 for reproducing optical disks upon which game programs are recorded, an image processing device for displaying characters along with background screens on the screen of the television set according to the game programs recorded on the optical disks, and so forth. Also provided in the main game unit 100 are a reset switch 102 for resetting a game in play, a power source switch 103, and a lid opening operation button 105 for opening the lid 104 which opens and closes the disk mounting unit for the disk driving unit 101.

Connection of the operating device 200 to the main game unit 100 is made through a connecting cord 202 extended from the main device unit 201. A connector 203 is provided in the tip of the connecting cord 202, and the operating device 200 is connected to the main game unit 100 by connecting this connector 203 into a jack 106 provided in one side of the main game unit 100.

Fig. 2 is a plan view illustrating the exterior of the operating device. Provided in the main device unit 201 of the operating device 200 are first and second operating units 210 and 220 on the upper plane thereof, and third and fourth operating units 230 and 240 on the side thereof.

The first operating unit 210 has a cross-shaped operating member 211 for operating by pressing, with operating keys 211a forming operators extending in the four directions from this operating member 211. This first operating unit 210 is for providing motion to characters displayed on the screen of the television set, and has
5 functions of moving the character vertically and horizontally by pressing the operating keys 211a of the operating member 211.

The second operating unit 220 has four cylindrical operating buttons 221 (operators) for operating by pressing. Each operating button 221 has identification marks such as "○", "△", "□", and "×" inscribed on the top portion thereof, so that each
10 operating button 221 can be easily identified. The functions of the second operating unit 220 are set by the game program recorded in the optical disk, e.g., functions for changing the state of the game character are appropriated to each operating button 221. For example, functions for moving the character's left arm, right arm, left leg, and right leg, are appropriated to the operating buttons 221.

15 The third and fourth operating units 230 and 240 have generally the same structure, and each has two vertically arrayed operating buttons 231 and 241 (operators) for operating by pressing. The functions of the third and fourth operating units 230 and 240 are also set by the game program recorded in the optical disk, e.g., functions are appropriated for special motions for the game character.

20 Further, a joystick 251 for performing analog operations is provided in the main device unit 201 shown in Fig. 2. The user switches between the joystick 251 and the first and second operating units 210 and 220 to enable use of one or the other. This switching is performed by an analog selection switch 252 provided in the main device unit 201. Once the joystick 251 is selected, the display unit 253 provided on the main
25 device unit 201 is lit, indicating the state of the joystick 251 having been selected.

Also provided in the main device unit 201 are a start switch 254 for instructing the start of the game, a selection switch 255 for selecting the difficulty and the like of

the game at the time of starting the game, and so forth.

First Embodiment

Next, the configuration of the first embodiment of the present invention will be described in detail.

5 Fig. 3 is a block diagram illustrating the principal components of the operating device according to the first embodiment of the present invention.

The operating units 210, 220, 230, and 240 on the operating device 200 described above are provided with the operating keys 221a of the operating member 211, operators 11 made up of the operating buttons 221, 231, and 241, and pressure-
10 sensitive elements 12 (detecting elements).

The pressure-sensitive element 12 may be formed of pressure-sensitive electroconductive rubber, and is of a configuration wherein electrodes 12a and 12b are formed at the two edge portions at symmetrical positions. One electrode 12a is connected to the power supply line 13, and a predetermined voltages is applied thereto
15 from the electric power source (Vcc). The electrical resistance value between the electrodes 12a and 12b changes according to the magnitude of pressure applied on the pressure-sensitive element 12.

Generally, the pressure-sensitive element 12 formed of pressure-sensitive electroconductive rubber exhibits lowest resistance values in a state where there is no
20 pressure force applied thereupon, as indicated by the broken line in Fig. 4, and the resistance values increase as the pressure force applied thereto increases. Accordingly, as illustrated by the solid line in Fig. 4, the other electrode 12b outputs analog signals (voltage) which are greatest in a state where there is no pressure force applied, and the analog signals (voltage) output decrease as the pressure force increases.

25 The pressure-sensitive element 12 is positioned on the depressing path of the operator 11, and as the user performs a pressing operation with the operator 11, the resistance value changes under the pressure force, and analog signals corresponding to

the pressure force are output to the electrode 12b side.

Also, a microprocessor unit (hereafter abbreviated as "MPU") 14 for governing the operating device 200 is provided in a board within. This MPU 14 comprises the functions of a level dividing unit (LS) 15 for dividing the output level of the analog
5 signal output from the pressure-sensitive element 12 into multiple levels, an A/D converting unit 16 for converting analog signals output from the pressure-sensitive element 12 into digital signals according to the output levels divided by the level dividing unit 15, and a later-described switching device 18, with the electrode 12b of the pressure-sensitive element 12 connected to the input side of the level dividing unit
10 15.

As shown in Fig. 4, the level dividing unit 15 has the basic functions of dividing the level range of analog signals (voltage) that have been present, in uniform widths. The number of divisions can be set arbitrarily, and in the examples shown in Fig. 4, the level range of the analog signals (voltage) is uniformly divided into eight. Each of the
15 individual output levels L1 through L8 thus uniformly divided are transferred to the A/D converting unit 16. Incidentally, the level range of analog signals which are uniformly divided by the level dividing unit 15 can be arbitrarily changed.

The A/D converting unit 16 converts the analog signals into digital signals and outputs, according to the output level of the analog signals subjected to level dividing
20 by the level dividing unit 15. That is, multi-bit digital signals are output from the A/D converting unit 16, according to the above output levels L1 through L8. Now, the functions of the level dividing unit 15 and the A/D converting unit 16 will be described with a specific example. For example, let us say that the operating device 200 being driven with power source voltage of 3.5 V, and that analog signals output from the
25 pressure-sensitive element 12 change within a range from 0 V to 2.4 V. In the event that the level dividing unit 15 divides the output level range 0 V to 2.4 V uniformly into eight steps, the level width for each step is 0.3 V.

Accordingly, the level dividing unit 15 performs level dividing so that output level of analog signals output from the pressure-sensitive element 12 of 2.4 V to 2.1 V is level 1 (L1), output level of 2.1 V to 1.8 V is level 2 (L2), output level of 1.8 V to 1.5 V is level 3 (L3), and so on through output level of 0.3 V to 0 V being level 8 (L8).

5 The A/D converting unit 16 appropriates appropriate multi-value digital signals to the output levels thus level-divided, and output these. Multiple-bit, e.g., 16-bit digital signals are appropriated to the above output levels, such that level 1 is "1f" in hexadecimal notation, level 2 is "3f", and so on through "ff" for level 8.

10 The multi-bit digital signals output from the A/D converting unit 16 are sent to the main game unit 100 via an interface 17 provided in the internal board of the operating device 200, and action and the like of the game character is executed based on these digital signals.

15 The level change of the analog signals output from the pressure-sensitive element 12 corresponds to the change in pressure force received from the operator 11 as described above. Accordingly, the multi-bit digital signals output from the A/D converting unit 16 correspond to the pressure force applied to the operator 11 by the user. Controlling the action and the like of the game character by such multi-bit digital signals related to the pressing operation by the user enable realization of movements smoother in analog fashion as compared to the control by single-bit digital signals of "1" or "0".

20 Also, with the present embodiment, the A/D converting unit 16 is configured to function also as single-bit digital signal output means for outputting single-bit (i.e., 1-bit) digital signals (that is, "1" or "0"), according to the change in analog signals output from the pressure-sensitive element 12, and outputs one or the other of the multi-bit digital signals and single-bit digital signals from the A/D converting unit 16 by a switching operation of the switching device 18.

25 With the present embodiment, the switching device 18 is controlled by control

signals sent from the main game unit 100 based on game programs recorded in the optical disk. That is, control signals for instructing whether to cause the A/D converting unit 16 to function as means for outputting multi-bit digital signals or to function as means for outputting single-bit digital signals are output according to the contents of a game program being executed from an optical disk mounted in the main game unit 100. Based on these control signals, the switching device 18 selects and switches the functions of the A/D converting unit 16.

The A/D converting unit 16 follows the functions selected by the switching device 18, and converts the analog signals output from the pressure-sensitive element 12 into one or other of multi-bit digital signals or single-bit digital signals, and outputs the same. In the event that functioning as means for outputting multi-bit digital signals is selected, the output levels uniformly divided by the level dividing unit 15 as described above are converted into corresponding digital signals, and output to the main game unit 100. On the other hand, in the event that functioning as means for outputting single-bit digital signals is selected, single-bit digital signals of "1" or "0" output to the main game unit 100, according to the change in analog signals output from the pressure-sensitive element 12.

Incidentally, the switching device 18 may be configured so as to be switched by manual operations by the user. For example, an arrangement may be made wherein functions for switching the switching device 18 are appropriated to the analog selecting switch 252 provided in the operating device 200, so as to work the switching device 18 with manual operation of the switch 252, thereby switching the functions of the A/D converting unit 16.

As shown in Fig. 5, in the present embodiment, the first through fourth operating units 210, 220, 230, and 240 have the configuration shown in Fig. 3. Accordingly, these operating units 210, 220, 230, and 240 have a configuration which can be used for digital operations and for analog operations. Incidentally, a

configuration wherein only an arbitrarily selected operating unit or arbitrarily selected operating units of the first through fourth operating units 210, 220, 230, and 240 has or have the configuration shown in Fig. 3.

Now, as described above, the level dividing unit 15 uniformly divides the output level of analog signals output from the pressure-sensitive element 12 in preset ranges, but in the event that there is an offset between the level range of the analog signals (voltage) actually output from the pressure-sensitive element 12 and this set range, there may arise a situation wherein digital signals matching the operating state of the operator 11 cannot be output.

However, there are individual differences in the pressure-sensitive elements 12, and there is also irregularity in the power source voltage, so the output ranges of the analog signals output from the pressure-sensitive elements 12 provided in the operating units 210, 220, 230, and 240 of each operating device 200 also differ.

Accordingly, the operating device 200 according to the present embodiment is provided with a calibration function (division range setting means) for individually setting the output level ranges of analog signals divided by the level dividing unit 15.

Fig. 6 is a block diagram illustrating a first configuration example for performing calibration of the level dividing unit. With the configuration shown in the Figure, the MPU 14 is provided with memory 20, and the configuration of the memory 20 is such that the output level range of analog signals divided by the level dividing unit 15 is stored in this memory 20.

For example, at the manufacturing line of the operating device 200, a constant load such that the resistance value of the pressure-sensitive element 12 is maximal is applied to the operating device 200, and the output level of the analog signal output from the pressure-sensitive element 12 at that time is stored in the memory 20.

Making description based on the above-described specific example, it is assumed that the voltage level range 0 V through 2.4 V was set so as to be uniformly

divided into eight steps, for the default value of the level dividing unit 15, and that an analog signal of 2.0 V was output from the pressure-sensitive element 12 at the time that the above constant load was applied, a digital signal "3f" corresponding to the level 2 is output from the A/D converting unit 16, as described above. This digital signal
5 "3f" is stored in the memory 20, and the output range of the analog signals to be level-divided is adjusted by the level dividing unit 15, based on the setting value.

Incidentally, the digital signal "3f" is equivalent to the analog signal output level 2.1 V through 1.8 V, and it is preferable to stipulate beforehand to which voltage value within this range the setting is to be made to. For example, stipulation may be made
10 beforehand so as to set the maximum voltage value of each output level (2.1 V in the above example) as the upper limit of the output level range of the analog signals to be divided by the level dividing unit 15.

Fig. 7 is a block diagram illustrating a second configuration example for performing calibration of the level dividing unit. With the configuration shown in the
15 Figure, memory is not provided in the operating device 200, and instead the configuration is such that the output level range of analog signals which the level dividing unit 15 divides is stored in built-in memory 111 in the main game unit 100 to which the operating device 200 is connected or in a detachable memory card 112.

In order to execute the calibration of the level dividing unit 15 using this
20 configuration, a setting program for executing calibration is preferably assembled into the control program stored in the ROM 110 of the main game unit 100.

Fig. 8 is a flowchart illustrating an example of such a setting program.

First, the power of the main game unit 100 is turned on (S1), and the sensitivity setting (calibration) of the operating unit is selected by menu selection by the user (S2),
25 whereby a setting screen is displayed on the television set 120 (S3). The setting screen displays a message prompting the user to firmly press the operator 11 provided in a certain operating unit, for example. When the user firmly presses the operator 11

according to this display, the output level of the analog signal from the pressure-sensitive element 12 detected at this time is output to the main game unit 100 (S4), and stored in the built-in memory 111 (S5). The above process is repeated for each level dividing unit 15 of the operating device 200 (S6), and the sensitivity setting for the
5 operating unit is thus completed.

Each level dividing unit 15 provided in the operating device 200 adjusts the output level range of the analog signals to be divided, based on the set value stored in the built-in memory 111 in the main game unit 100.

Also, a setting program for executing the calibration operation may be
10 assembled into the game program in the optical disk.

Fig. 9 is a flowchart for an example of such a setting program.

First, following mounting of the optical disk on the main game unit 100 (S10), confirmation is made whether or not a memory card 112 is mounted onto the main game unit 100 (S11), and in the event that a memory card 112 is not mounted, under the
15 condition that the user selects sensitivity setting (calibration) of the operating unit by menu selection (S12), the setting screen is displayed on the television set 120 (S13). The setting screen displays a message prompting the user to firmly press the operator 11 provided in a certain operating unit, for example. When the user firmly presses the operator 11 according to this display, the output level of the analog signal from the
20 pressure-sensitive element 12 detected at this time is output to the main game unit 100 (S14), and stored in the built-in memory 111 (S15). The above process is repeated for each level dividing unit 15 of the operating device 200 (S16), and the sensitivity setting for the operating unit is thus completed.

In the event that a memory card 112 is discovered to be mounted in step S11, it
25 is determined whether or not a setting value regarding calibration has already been stored in the memory card 112 (S17), and in the event that this is the case, the sensitivity setting of the operating unit is ended here. In the case, the level dividing

units 15 provided in the operating device 200 adjust the output level range of the analog signals to be divided based on the set values stored in the memory card 112.

On the other hand, in the event that a setting value regarding calibration has not been stored in the memory card 112, the flow proceeds to step S12, and the above-
5 described calibration operation is performed. Incidentally, the output level of analog signals from the pressure-sensitive element 12 detected in step S15 is stored in the memory card 112 (S16).

The level dividing units 15 provided in the operating device 200 adjust the output level range of the analog signals to be divided based on the set values stored in
10 the memory of the main game unit 100 or in the memory card 112.

Fig. 10 is a block diagram illustrating a third configuration example for calibrating the level dividing unit. In the configuration shown in the Figure, two volume elements 21 and 22 are serially inserted in the power source line to which the pressure-sensitive element 12 of the operating device 200 is connected, such that the
15 intermediate voltage of the power source line 13 can be adjusted by these volume elements 21 and 22.

Then, the configuration is such that the level dividing unit 15 sets the output level range of the analog signals to be divided based on the intermediate voltages V1 and V2 of the power source line 13 adjusted by these volume elements 21 and 22, as
20 shown in Fig. 11. That is, the level dividing unit 15 takes the intermediate voltage V1 detected at the volume element 21 closer to the power source Vcc as the maximum value of the output level range of the analog signals to be divided, takes the intermediate voltage V2 detected at the other volume element 22 as the minimum value of the output level range of the analog signals to be divided, and uniformly divides the
25 output level of the analog signals output from the pressure-sensitive element 12, within the range between these intermediate voltages V1 and V2. The adjustment of the volume elements 21 and 22 should be executed at the time of shipping the operating

device 200, for example.

Also, a configuration may be used wherein functions for monitoring the intermediate voltages V1 and V2 are added to the level dividing unit 15, so that in the event that these intermediate voltages V1 and V2 change over time or the like, the output level range of the analog signals to be divided can be adjusted according to the changed intermediate voltages V1 and V2. Adding such an automatic calibration function enables an optimal setting state to be constantly maintained even in the event that the intermediate voltages V1 and V2 change due to aging of the pressure-sensitive element 12 and volume elements or irregularities in the power source voltage, since the output level range of the analog signals to be divided is adjusted according to the changed intermediate voltages V1 and V2.

However, in the event that the level dividing unit 15 constantly performs such automatic calibration, output to the main game unit 100 may be delayed. In such cases, a configuration can be made wherein the level dividing unit 15 checks the intermediate voltages V1 and V2 of the power source line 13 only at the time of energization the operating device 200 and adjusts the output level range of the analog signals to be divided.

Fig. 12 is a block diagram illustrating a fourth configuration example for calibrating the level dividing unit. In the configuration shown in the Figure, two volume elements 21 and 22 are serially inserted in the power source line 13 to which the pressure-sensitive element 12 of the operating device 200 is connected, and further the MPU 14 includes a comparator 23 and memory 24.

The memory 24 stores beforehand the limit value range of the output level of analog signals to be divided by the level dividing unit 15. For example, the tolerance voltage of the MPU 14 is stored in the memory 24 as the limit value. The comparator 23 constantly monitors the intermediate voltages V1 and V2 detected by the volume elements 21 and 22, makes comparison of the intermediate voltages V1 and V2

(particularly V1) with the limit value stored in the memory 24, and functions to forcibly notify the level dividing unit 15 of the limit value in the event that the intermediate voltage has exceeded the limit value. In the event that the level dividing unit 15 receives the limit value from the comparator 23, the level dividing unit 15 adjusts the output level range of the analog signals to be divided based on this limit value.

With such a configuration, even in the event that analog signals with excessively large output levels exceeding the processing capabilities of the MPU 14 are output from the pressure-sensitive element 12, normal operation of the MPU 14 can be compensated.

Next, a detailed description will be made regarding a configuration example of the operating units provided in the operating device 200 according to the first embodiment of the present invention described above, with reference to the drawings.

Figs. 13 through 15 are diagrams illustrating a first configuration example of the second operating unit.

As shown in Fig. 14, the second operating unit 220 has four operating buttons 221 which are operators 11, an elastic member 222, and a sheet member 223 upon which are provided pressure-sensitive elements 12. The operating buttons 221 are mounted from the rear side of mounting holes 201a provided on the upper side of the main unit 201, as shown in Fig. 13. The operating buttons 221 mounted to the mounting holes 201a are movable in the axial direction.

The elastic member 222 is formed of insulating rubber or the like, has elastic portions 222a protruding upwards, and supports the lower edges of the operating buttons 221 with the upper walls of the elastic portions 222a. In the event that an operating button 221 is pressed, the slope portion of the elastic portion 222a flexes and the upper wall moves in conjunction with the operating button 221. On the other hand, in the event that the pressure force placed upon the operating button 221 is released, the slope portion of the elastic portion 222a which had flexed is restored by elastic force, thereby pushing the operating button 221 upwards. That is, the elastic member 222

functions as a pressing means for restoring an operating button 221 which has been pressed down by pressing force, to the original position.

The sheet member 223 is formed of a thin sheet material which is flexible and has insulating properties, such as a membrane or the like. Pressure-sensitive elements 5 12 are provided in appropriate positions on this sheet member 223, and as shown in Fig. 15, the pressure-sensitive elements 12 are positioned so as to be facing the operating buttons 221 across the elastic member 222.

Also, with the present example, protrusions 221a are formed on the bottom surface of the operating buttons 221 which are the operators 11, and also recessions 10 222b for supporting the protrusions 221a are formed to the elastic portions 222a of the elastic member 222. When an operating button 221 is pressed, the protrusion 221a presses the pressure-sensitive element 12 via the recession 222b of the elastic portion 222a.

As described above, the electrical resistance values of the pressure-sensitive 15 element 12 change according to the pressing force applied from the operating button 221. Providing a protrusion 221a on the bottom plane of the operating button 221 and pressing the pressure-sensitive element 12 with this protrusion 221a enables the pressing force to be transferred to the pressure-sensitive element 12 with high precision.

However, as a result of pressing the pressure-sensitive element 12 with the 20 protrusion 221a portion, it cannot be denied that the pressing force of the pressure-sensitive element 12 and the elastic member 222 acting on the recession 222b becomes excessive, thereby reducing the endurance of the pressure-sensitive element 12 and the elastic member 222.

Accordingly, with the second configuration example shown in Figs. 16 and 17, 25 the bottom plane of the operating button 221 which is an operator 11 is formed as a flat plane, such that the entirety of the flat bottom plane is used to press the pressure-sensitive element 12. Neither is a recession formed to the elastic portion 222a of the

elastic member 222, and the arrangement is made such that the bottom plate of the operating button 221 is supported with the flat plane. According to such a configuration, even though the sensitivity properties of transmitting the pressing force from the operating button 221 to the pressure-sensitive element 12 decreases, there is
5 the advantage that the endurance of the pressure-sensitive element 12 and the elastic member 222 is improved.

Figs. 18 and 19 are diagrams illustrating a third configuration example of the second operating unit.

With the third configuration example shown in these diagrams, the pressure-
10 sensitive elements 12 are directly provided on appropriate positions on the internal board 204 built into the operating device 200. Providing the pressure-sensitive elements 12 to the internal board 204 allows the sheet member to be omitted and the number of parts to be reduced. Of course, the pressure-sensitive elements 12 are to be provided at positions to which the pressing force from the operating buttons 221 can be
15 transmitted.

Figs. 20 and 21 are diagrams illustrating a configuration example of the first operating unit.

As shown in Fig. 20, the first operating unit 210 comprises a cross-shaped operating member 211, a spacer 212 for positioning the operating member 211, and an
20 elastic member 213 for elastically supporting the operating member 211, and further, as shown in Fig. 21, the pressure-sensitive elements 12 are provided at positions facing the operating keys 221a (operators 11) of the operating member 211 via the elastic member 213.

The overall construction of the first operating unit 210 is already known in
25 Japanese Unexamined Patent Application Publication No. 8-163672 and so forth, so detailed description thereof will be omitted, but the operating member 211 is assembled on a half-spherical protrusion 212a formed at the center of the spacer 212 which serves

as a fulcrum thereof, whereby the operating keys 221a (operators) can be pressed to the side of the pressure-sensitive elements 12 (see Fig. 21).

Then, in the event that operating keys 221a which are the operators 11 are pressed, the pressing force thereof acts on the pressure-sensitive elements 12 via the elastic member 213, such that the electrical resistance values of the pressure-sensitive elements 12 change according to the magnitude of the pressing force. In the configuration example shown in the Figure, a configuration wherein the pressure-sensitive elements 12 are directly provided at appropriate positions on the internal board 204 built into the operating device 200 is shown, but the pressure-sensitive elements 12 may be provided on a sheet material 223 as with the configuration example of the second operating unit 220 shown in Figs. 14 and 15.

Figs. 22 and 23 are diagrams illustrating a configuration example of the third operating unit.

The third operating unit 230 comprises two operating buttons 231, a spacer 232 for positioning these operating buttons 231 within the operating device 200, a holder 232 for supporting the operating buttons 231, an elastic member 234, and an internal substrate 235, with the pressure-sensitive elements 12 provided at appropriate positions on the internal substrate 235.

The overall construction of the third operating unit 230 is also known in Japanese Unexamined Patent Application Publication No. 8-163672 and so forth, so detailed description thereof will be omitted, but the operating buttons 231 are configured so as to be pressed in for operation, being guided by the spacer 232, and the pressing force at the time of pressing operates on the pressure-sensitive elements 12 via the elastic member 234. The electrical resistance values of the pressure-sensitive elements 12 change according to the magnitude of the pressing force applied from the operating buttons 231. In the configuration example shown in the Figure, a configuration wherein the pressure-sensitive elements 12 are directly provided at

appropriate positions on the internal board 235 built into the operating device 200 is shown, but the pressure-sensitive elements 12 may be provided on a sheet material 223 as with the configuration example of the second operating unit 220 shown in Figs. 14 and 15.

5 Note that the fourth operating unit 240 is configured in the same way as the above-described third operating unit 230.

The above has been a description of configuration examples of application of the first through fourth operating units 210, 220, 230, and 240 to the present invention, but it should be noted that the present invention is not restricted to an arrangement
10 wherein all operating units are applied to the present invention; rather, operating units to be applied to the present invention may be arbitrarily selected, with the remaining operating units being configured in a conventional manner.

Second Embodiment

Next, a configuration relating to the second embodiment of the present invention
15 will be described in detail. The components thereof which are the same as those in the above-described first embodiment will be denoted with the same reference numerals, and detailed description of such parts will be omitted.

With the above-described operating device 200 according to the first embodiment, the configuration involved analog signals output from the pressure-
20 sensitive elements 12 generating both multi-bit digital signals and single-bit digital signals, but in the following second embodiment, the configuration is such that multi-bit digital signals are generated from analog signals output from the pressure-sensitive element 12 and single-bit digital signals being output by detection of the on/off state of a digital switch.

25 Fig. 24 is a block diagram illustrating the principle portions of the operating device according to the second embodiment of the present invention.

With the present embodiment, the operating units 210, 220, 230, and 240 of the

operating device 200 comprise the operating keys 211a of the operating member 211 and the operating buttons 221, 231, and 241 making up the operators 11, the pressure-sensitive elements 12 (detecting elements), and a digital switch 30. Of these, the configuration of the operator 11 and pressure-sensitive element 12 is the same as those provided in the operating device 200 of the above-described first embodiment.

The digital switch 30 has a first and second fixed terminals 31 and 32, and a movable member 33 which makes contact or breaks contact with the fixed terminals 31 and 32 so as to make or break contact. The movable member 33 moves in accordance with the pressing operation of the operator 11, and makes or breaks contact between the first and second fixed terminals 31 and 32. Also, the first fixed terminal 31 of the digital switch 30 is connected to the electric power source line 13, and a predetermined voltage is applied from the electric power source (Vcc), as shown in Fig. 24.

The MPU 14 mounted to the internal board within the operating device 200 comprises, in addition to the functions of the level dividing unit (LS) 15 and A/D converting unit 16, the functions of a digital signal generating unit 35 for detecting the on/off state of the above digital switch 30 and outputting single-bit digital signals, a switching switch 18a for switching between the output of this digital signal generating unit 35 and the A/D converting unit 16 and outputting it externally, and a switching unit 18 for operating this switching switch 18a.

The A/D converting unit 16 in the present embodiment only has the function of converting analog signals output from the pressure-sensitive element 12 into multi-bit digital signals and outputting the same.

Also, the digital signal generating unit 35 is connected at the input side thereof to the second fixed terminal 32 of the digital switch 30, and monitors the voltage change occurring at the fixed terminal 32. That is, in the event that the digital switch 30 is in the on state, the potential of the second fixed terminal 32 is the same as that of the electric power source line 13, and on the other hand, in the event that the digital

switch 30 is in the off state, the voltage of the second fixed terminal 32 is zero. The digital signal generating unit 35 thus outputs single-bit digital signals "0" or "1" according to change in the voltage occurring at the second fixed terminal 32.

The switching unit 18 is configured so as to be controlled by control signals sent from the main game unit 100, based on game programs recorded in an optical disk, in this embodiment too. That is, at the time of executing the game program recorded in the optical disk, control signals are output from the main game unit 100 to instruct either connecting the switching switch 18a to the A/D converting unit 16 side, or connecting the switching switch 18a to the digital signal generating unit 35 side, according to the contents of the game program. The switching unit 18 operates the switching switch 18a based on these control signals.

Incidentally, a configuration may be made wherein the switching unit 18 is controlled by manual operation. For example, a configuration can be made wherein an analog selection switch 252 provided in the operating device 200 is appropriated with a function for switching the switching switch 18, so that manual operation of the analog selection switch 252 operates the switching switch 18a.

According to the operating device 200 according to the second embodiment of the above configuration, the movable member 33 of the digital switch 30 conducts between the first and second fixed terminals 31 and 32 in conjunction with the pressing operation of the operator 11, and analog signals are output from the pressure-sensitive element 12 according to pressing force applied from the operator 11. Then, the digital signal generating unit 35 outputs single-bit digital signals according to the state change of the digital switch 30, and multi-bit digital signals are output from the A/D converting unit 16 at an output level according to the pressing force applied to the pressure-sensitive element 12.

Accordingly, one or the other of single-bit digital signals and multi-bit digital signals can be output from the operating device 200 to the main game unit 100, by

selection made by the switching switch 18a.

As shown in Fig. 25, the present embodiment has a configuration wherein the first through fourth operating units 210, 220, 230, and 240 have the configuration shown in Fig. 24. Accordingly, these operating units are capable of being selectviely
5 used for digital operations and for analog operations. Incidentally, as shown in Fig. 26, an arrangement may be made wherein only operating units arbitrarily selected from the first through fourth operating units 210, 220, 230, and 240 are configured as shown in Fig. 24.

Also, the operating device 200 according to the present embodiment also has a
10 calibration function (division range setting means) for individually setting the output level ranges of analog signals divided by the level dividing unit 15, as shown in Figs. 6, 7, 10, and 12.

Next, a detailed description will be given regarding a configuration example of the second operating unit provided in the operating device 200 according to the second
15 embodiment of the present invention described above, with reference to the drawings.

Figs. 27 and 28 are diagram illustrating a first configuration example of the second operating unit of the present embodiment.

As shown in Fig. 27, the second operating unit 220 comprises four operating buttons 221 serving as operators 11, an elastic member 222, a sheet member 224
20 provided with pressure-sensitive elements 12, and a sheet member 225 provided with the first and second fixed terminals 31 and 32 of the digital switch 30. Each of the operating buttons 221 are mounted from the rear side to mounting holes 201a formed on the upper plane of the main operating unit 201 as with the case in the first embodiment described above (see Fig. 13). The operating buttons 221 mounted to the
25 mounting holes 301a are movable in the axial direction.

The elastic member 222 is formed of insulating rubber or the like, has elastic portions 222a protruding upwards, and supports the lower edges of the operating

buttons 221 with the upper walls of the elastic portions 222a. In the event that an operating button 221 is pressed, the slope portion of the elastic portion 222a flexes and the upper wall moves in conjunction with the operating button 221. On the other hand, in the event that the pressure force placed upon the operating button 221 is released, the slope portion of the elastic portion 222a which had flexed is restored by elastic force,
5 thereby pushing the operating button 221 upwards. That is, the elastic member 222 functions as a pressing means for restoring an operating button 221 which has been pressed down by the pressing operation, to the original position.

Also, the movable member 33 of the digital switch 30 is formed at the inner side
10 ceiling plane of the upper wall of the elastic portion 222a (see Fig. 28). This movable member 33 is formed of material having electroconductivity, and moves downwards by flexing deformation of the elastic portion 222a in conjunction with the pressing operation of the operating button 221.

The sheet member 225 is formed of a thin sheet material which is flexible and
15 has insulating properties, such as a membrane or the like. The first and second fixed terminals 31 and 32 are provided in appropriate positions on this sheet member 225, and as shown in Fig. 28, the first and second fixed terminals 31 and 32 are positioned so as to be facing the movable member 33. According to this configuration, the movable member 33 formed on the upper wall of the elastic portion 222a moves in conjunction
20 with the pressing operation of the operating button 221 which is an operator 11, comes into contact with the first and second fixed terminals 31 and 32, and makes electric conducting between the fixed terminals 31 and 32.

Also, the sheet member 224 is also formed of a thin sheet material which has insulating properties. Pressure-sensitive elements 12 are provided in appropriate
25 portions on this sheet member 224, and as shown in Fig. 28, the pressure-sensitive elements 12 are positioned facing the operating buttons 221 across the elastic material 222 and the sheet member 225.

As described above, the sheet member 225 is formed of a thin sheet which is flexible, so the pressing force of the operating button 221 transferred via the upper wall of the elastic portion 222a and the movable member 33 can be transferred to the pressure-sensitive elements 12 with almost no change.

5 Figs. 29 and 30 are diagrams illustrating a second configuration example of the second operating unit of the present embodiment.

The second embodiment shown in these drawings is a configuration wherein the pressure-sensitive elements 12 are directly provided on appropriate positions on the internal board 204 built into the operating device 200. Providing the pressure-
10 sensitive elements 12 to the internal board 204 allows the sheet member 224 to be omitted and the number of parts to be reduced. Of course, the pressure-sensitive elements 12 are to be provided at positions to which the pressing force from the operating buttons 221 can be transmitted.

15 Figs. 31 and 32 are diagrams illustrating a third configuration example of the second operating unit of the present embodiment.

With the third embodiment shown in these drawings, the first and second fixed terminals 31 and 32 of the digital switch 30 are provided on the front plane of the sheet member 225, and the pressure-sensitive element 12 is provided on the rear side of the same sheet member. Of course, the positions of the first and second fixed terminals 31
20 and 32 and the pressure-sensitive element 12 are arranged so as to face one another across the sheet member 225. Also, the sheet member 225 is positioned so that the pressure-sensitive element 12 is supported in a planar fashion by the inner wall 200a of the operating device 200 and the wiring circuit (see Fig. 32).

According to this configuration, one sheet member can be done away with.

25 Fig. 33 is a diagram illustrating a fourth configuration example of the second operating unit of the present embodiment.

With the fourth configuration example shown in the Figure, the movable

member 33 of the digital switch 30 is provided in the rear plane side of the sheet member 224 to which the pressure-sensitive element 12 has been provided on the front side. Then, the position of the sheet members 224 and 225 are changed, such that the above sheet member 224 is positioned between the sheet member 225 to which the first and second fixed terminals 31 and 32 have been provided and the elastic member 222. The sheet member 225 is positioned so as to be supported in a planar fashion by the inner wall 200a of the operating device 200 and the wiring circuit (see Fig. 33).

Note that though with the present embodiment various configuration examples have been described regarding the second operating unit 220, similar configurations can be made for the other operating units 210, 230, and 240, as well.

Third Embodiment

Next, a configuration relating to the third embodiment of the present invention will be described in detail. The components thereof which are the same as those in the above-described first embodiment will be denoted with the same reference numerals, and detailed description of such parts will be omitted.

With the above-described operating device 200 according to the first embodiment, the configuration involved using a pressure-sensitive element 12 as the detecting element thereof, but with the third embodiment described next, the detecting element is configured of a resistor 40 and electroconductive material 50.

Fig. 34 is a diagram illustrating a configuration example of the second operating unit according to the present invention. Incidentally, though the figure only shows one operating button 221 and the related configurations, but the various operating buttons 221 provided in the second operating unit 220 may be of the same configuration, and also operating buttons 221 may be arbitrarily selected and configured as shown in the Figure.

That is, the second operating unit 220 according to the present embodiment comprises operating buttons 221 serving as operators 11, an elastic member 222, an

electroconductive member 50, and a resistor 40. The electroconductive member 50 is formed of electroconductive rubber having elasticity for example, and in the configuration example shown in Fig. 34, is formed in a mountain-like shape with the apex thereof in the center. This electroconductive member 50 is applied by adhesion to the inner side ceiling plane of the elastic portion 222a formed in the elastic member 222.

Also, the resistor 40 is provided on the internal board 204 for example, facing the electroconductive member 50, and is configured such that the electroconductive member 50 comes into contact with the resistor 40 by pressing operation of the operating button 221. The electroconductive member 50 deforms according to pressing force of the operating button 221 (i.e., contact pressure with the resistor 40), and the contact area to the resistor 40 changes as shown in Figs. 34B and 34C. That is, in the event that the pressing force of the operating button 221 is weak, the area near the apex portion of the electroconductive member 50 shaped in a mountain-like form comes into contact as shown in Fig. 34B. In the event that the operator increases the pressing force on the operating button 221, the electroconductive member 50 gradually deforms from the apex and the contact area increases.

Fig. 35 is a diagram illustrating the circuit configuration of a resistor. As shown in the Figure, the resistor 40 is serially inserted into the electric power source line 13, and voltage is applied between electrodes 40a and 40b. A model representation of the internal resistance of this resistor 40 shows this being divided into a fixed resistor 41 and a variable resistor 42, as shown in the Figure. Of these, the portion of the variable resistor 42 is equivalent to the contact portion of the electroconductive member 50, and changes the resistance value according to the contact area of the electroconductive member 50. That is to say, when the electroconductive member 50 comes into contact with the resistor 40, the electroconductive member 50 serves as a bridge and the current flows through that portion, so the resistance value of

the contact portion becomes small. Accordingly, the greater the contact area of the electroconductive member 50 is, the more the resistance value of the resistor 40 is reduced.

With the present embodiment, an output terminal 40c is provided at the center portion of the resistor 40, and analog signals corresponding to the pressing force of the operating button 221 (operator 11) is output from this output terminal 40c.

Fig. 36 is a diagram illustrating the properties of analog signals (voltage) output from the output terminal of the resistor 40.

First, voltage is applied to the resistor 40 at the time of turning the power on, so even in the event that the operating button 221 is not pressed, a constant analog signal (voltage) V_{min} is output from the output terminal 40c (position "a" in the Figure). Next, even in the event that the operating button 221 is pressed, the resistance value of the resistor 40 does not change until the electroconductive member 50 comes into contact with the resistor 40, so the output from the resistor 40 remains unchanged at V_{min} . Further, in the event that the operating button 221 is pressed and the electroconductive member 50 comes into contact with the resistor 40 (the pressing position denoted by "b" in the Figure), the contact area of the electroconductive member 50 as to the resistor 40 subsequently increases according to the pressing force of the operating button 221, so the internal resistance of the resistor 40 decreases, and analog signals (voltage) output from the output terminal 40c of the resistor 40 increase. Then, at the point that the electroconductive member 50 has deformed as much as possible, the analog signals (voltage) output from the output terminal 40c of the resistor 40 reach a maximum V_{max} (the pressing position denoted by "c" in the Figure).

Fig. 37 is a block diagram illustrating the principle portions of the operating device according to the third embodiment of the present invention.

In the present embodiment as well, the MPU 14 mounted to the internal board within the operating device 200 comprises the functions of the level dividing unit 15,

A/D converting unit 16, and switching unit 18. With the present embodiment, analog signals (voltage) output from the output terminal 40c of the resistor 40 are input to the level dividing unit 15, the output level of the analog signals is divided into a plurality at the level dividing unit 15, and further, the A/D converting unit 16 converts the analog
5 signals output from the resistor 40 into digital signals, based on the divided output level.

The functions of the level dividing unit 15 and A/D converting unit 16 are the same as with the above-described first embodiment, and the level dividing unit 15 has the basic function of dividing the level range of analog signals (voltage) output from the resistor 40 into uniform widths, as shown in Fig. 36. The number of divisions can be
10 arbitrarily set, and with the example shown in Fig. 36, the level range of the analog signals (voltage) is uniformly divided into eight. Each uniformly-divided output level L1 through L8 is transferred to the A/D converting unit 16. Also, the level range of analog signals to be uniformly divided can be arbitrarily changed with the level
dividing unit 15.

15 The A/D converting unit 16 converts analog signals into digital signals and outputs the same, according to the output level of the analog signals subjected to level-division at the level dividing unit 15. That is, multiple-bit digital signals are output from the A/D converting unit 16, according to the above output levels L1 through L8.

20 Then, the A/D converting unit 16 appropriates appropriate multi-bit digital signals to each of the output levels subjected to level-division, and outputs. For example, 16-bit multi-bit digital signals are appropriated to the above output levels, such that level 1 is "1F", level 2 is "3F", and so on through "ff" for level 8.

The multi-bit digital signals output from the A/D converting unit 16 are sent to the main game unit 100 via an interface 17 provided in the internal board of the
25 operating device 200, and action and the like of the game character is executed by these digital signals.

The level change of the analog signals output from the output terminal 40c of

the resistor 40 corresponds to the change in pressure force received from the operating button 221 (operator 11) as described above. Accordingly, the multi-bit digital signals output from the A/D converting unit 16 correspond to the pressure force applied to the operating button 221 (operator 11) by the user. Controlling the action and the like of the game character by such multi-bit digital signals related to the pressing operation by the user enable realization of movements smoother in analog fashion as compared to the control by single-bit digital signals of "1" or "0".

Also, with the present embodiment, the A/D converting unit 16 is configured to function also as binary digital signal output means for outputting single-bit digital signals (i.e., "1" or "0"), according to the change in analog signals output from the output terminal 40c of the resistor 40, and outputs one or the other of the multi-bit digital signals and single-bit digital signals from the A/D converting unit 16 by a switching operation of the switching device 18.

With the present embodiment, the switching device 18 is controlled by control signals sent from the main game unit 100 based on game programs recorded in the optical disk. That is, control signals for instructing whether to cause the A/D converting unit 16 to function as means for outputting multi-bit digital signals or to function as means for outputting single-bit digital signals are output according to the contents of a game program being executed from an optical disk mounted onto the main game unit 100. Based on these control signals, the switching device 18 selects and switches the functions of the A/D converting unit 16.

The A/D converting unit 16 follows the functions selected by the switching device 18, and converts the analog signals output from the output terminal 40c of the resistor 40 into one or other of multi-bit digital signals or single-bit digital signals and outputs the same. In the event that functioning as means for outputting multi-bit digital signals is selected, the output levels uniformly divided by the level dividing unit 15 as described above are converted into corresponding digital signals, and output to

the main game unit 100.

On the other hand, in the event that functioning as means for outputting single-bit digital signals is selected, single-bit digital signals of "1" or "0" output to the main game unit 100, according to the change in analog signals output from the output terminal 40c of the resistor 40. That is, in the event that the A/D converting unit 16 recognizes the analog signals output from the output terminal 40c of the resistor 40 as being the minimal value V_{min} , judgement is made that the operating button is not pressed, and a digital signal "0" is output. On the other hand, in the event that recognition is made based on the output from the A/D converting unit 16, that the analog signals output from the output terminal 40c of the resistor 40 are not the minimal value V_{min} , judgement is made that the operating button is pressed, and a digital signal "1" is output.

Incidentally, the switching device 18 may be configured so as to be switched by manual operations by the user. For example, an arrangement may be made wherein functions for switching the switching device 18 are appropriated to the analog selecting switch 252 provided in the operating device 200, so as to work the switching device 18 manually with the switch 252, thereby switching the functions of the A/D converting unit 16.

Now, as described above, the level dividing unit 15 uniformly divides the output level of analog signals output from the resistor 40 in preset ranges, but in the event that there is an offset between the level range of the analog signals (voltage) actually output from the resistor 40 and this preset range, there may arise a situation wherein digital signals matching the operating state of the operator 11 cannot be output.

However, there are individual differences in the resistor 40 and the electroconductive member 50, and there is also irregularity in the power source voltage, so the output ranges of the analog signals output from the resistor 40 of each operating device 200 also differ.

Accordingly, the operating device 200 according to the present embodiment is provided with a division range setting unit 25 for individually setting the output level ranges of analog signals divided by the level dividing unit 15 (see Fig. 37), such that the level range of the analog signals (voltage) to be divided at the level dividing unit 15 can be calibrated.

Fig. 38 is a diagram for describing the functions of the division range setting unit.

As shown in Fig. 38, initial setting of the minimal value V_{min} and maximal value V_{max} of the analog signals (voltage) output from the resistor 40 is made beforehand at the division range setting unit 25. Also, regarding the maximal value V_{max} , an arbitrary tolerance value α is set beforehand. This arbitrary tolerance value α is for absorbing irregularities in the recognition of the output of the resistor (i.e., analog signals) from the information from the A/D converting unit 16. Further, a judging value γ for judging whether or not the operating button is in a pressed state is set beforehand around the minimal value V_{min} .

The division range setting unit 25 executes the calibration operation as follows under such settings.

Once the power is turned on to the operating device 200, first, the division range setting unit 25 recognizes the level $V_{min}(\text{Real})$ of the analog signal (voltage) actually being output from the resistor 40 based on information from the A/D converting unit 16, in order to adjust the minimal value V_{min} of the analog signals (voltage) output from the resistor 40.

At this time, the user may be pressing the operating button 221, so judgement is made whether $V_{min}(\text{Real})$ is within the range of the margin of error tolerance value α centered around V_{min} . In the event that $V_{min}(\text{Real})$ is out of the range of $(V_{min} + \gamma) < V_{min}(\text{Real}) < (V_{min} - \gamma)$, an action for notifying the user that calibration is running is executed.

As for this action, for example, a display portion 253 provided in the operating device 200 may be lit or may blink, or in the event that the operating device 200 has a vibration function built in this function may be activated, or like means can be used.

Next, under the condition that $V_{\min}(\text{Real})$ is within the range of $(V_{\min} + \gamma) < V_{\min}(\text{Real}) < (V_{\min} - \gamma)$, the value of $V_{\min}(\text{Real})$ is compared with V_{\min} . In the event that the comparison results in $V_{\min}(\text{Real}) > V_{\min}$, the initial setting value V_{\min} is set as the minimal value of the analog signals (voltage) output from the resistor 40. On the other hand, in the event that $V_{\min}(\text{Real}) < V_{\min}$ holds, the actual output value $V_{\min}(\text{Real})$ is newly set as the minimal value of the analog signals (voltage) output from the resistor 40.

Next, the user is made to firmly press the operating button 221 following an operating manual or the like, and the level $V_{\max}(\text{Real})$ of the analog signal (voltage) actually being output from the resistor 40 based on information output from the A/D converting unit 16 is recognized.

In the event that $V_{\max}(\text{Real})$ is greater than $(V_{\max} - \alpha)$ which includes the tolerance value α , recognition is made that the user has pressed the operating button 221 to the limit, and thus $V_{\max}(\text{Real})$ and V_{\max} are compared. In the event that the comparison results in $V_{\max}(\text{Real}) < V_{\max}$, the initial setting value V_{\max} is set as the maximal value of the analog signals (voltage) output from the resistor 40. On the other hand, in the event that $V_{\max}(\text{Real}) > V_{\max}$ holds, the actual output value $V_{\max}(\text{Real})$ is newly set as the maximal value of the analog signals (voltage) output from the resistor 40.

The division range setting unit 25 controls the level dividing unit 15 so as to uniformly divide the analog signals (voltage) output from the resistor 40, within the range from the minimal value V_{\min} to maximal value V_{\max} set as described above.

Fig. 39 is a diagram illustrating a configuration example of the first operating unit according to the present embodiment.

With the configuration example of the first operating unit shown in the Figure, electroconductive members 50 are applied by adhesion to the inner side ceiling plane of the elastic member 213 of the operating keys 211a (operators 11) of the cross-shaped operating member 211 in a corresponding manner. Also, the resistors 40 are arranged
5 such that singular items are positioned so as to face each of the electroconductive members 50.

Fig. 40 is a diagram illustrating the circuit configuration of the resistor. As shown in Fig. 40, the resistor 40 is serially inserted into the electric power source line 13, and voltage is applied between electrodes 40a and 40b. A model representation of
10 the internal resistance of this resistor 40 shows this being divided into a first and second variable resistors 43 and 44, as shown in the Figure. Of these, the first variable resistor 43 is arranged such that for example, the electroconductive member 50 moving with the operating key for moving the character in the upwards direction (the up key) 211a, and the electroconductive member 50 moving with the operating key for moving
15 the character in the left direction (the left key) 211a each come into contact therewith, and the resistance value changes according to the area of contact with the electroconductive members 50. Also, the second variable resistor 44 is arranged such that for example, the electroconductive member 50 moving with the operating key for moving the character in the downwards direction (the down key) 211a, and the
20 electroconductive member 50 moving with the operating key for moving the character in the right direction (the right key) 211a each come into contact therewith, and the resistance value changes according to the area of contact with the electroconductive members 50.

An output terminal 40c is provided at the intermediate portion between the
25 variable resistors 43 and 44, such that analog signals corresponding to the pressing force of the operating keys 211a (operators 11) are output from this output terminal 40c.

The output from the output terminal 40c can be calculated from the division

ratio of the resistance values that the first and second variable resistors 43 and 44 have, and for example, with R1 representing the resistance value of the first variable resistor 43 and R2 representing the resistance value of the second variable resistor 44, and with the power source voltage as Vcc, the output voltage V manifested at the output terminal
5 40c can be expressed as follows:

$$V = V_{cc} \times R2 / (R1 + R2)$$

Accordingly, in the event that the resistance value of the first variable resistor 43 decreases, the output voltage increases, and on the other hand, in the event that the resistance value of the second variable resistor 44 decreases, the output voltage
10 decreases as well.

Fig. 41 is a diagram illustrating the properties of analog signals (voltage) output from the output terminal of the resistor.

First, voltage is applied to the resistor 40 at the time of turning the power on, so even in the event that the operating keys 211a of the operating member 211 are not
15 pressed, a constant analog signal (voltage) V0 is output from the output terminal 40c (position "o" in the Figure).

Next, even in the event that an operating key 211a is pressed, the resistance value of the resistor 40 does not change until the electroconductive member 50 comes into contact with the resistor 40, so the output from the resistor 40 remains unchanged
20 at V0.

Further, in the event that the up direction key or left direction key is pressed and the electroconductive member 50 comes into contact with the first variable resistor 43 portion of the resistor 40 (the pressing position denoted by "p" in the Figure), the contact area of the electroconductive member 50 as to the first variable resistor 43
25 portion subsequently increases according to the pressing force of the operating key 211a (operator), so the resistance value at that portion decreases, and analog signals (voltage) output from the output terminal 40c of the resistor 40 increase. Then, at the point that

the electroconductive member 50 has deformed as much as possible, the analog signals (voltage) output from the output terminal 40c of the resistor 40 reach a maximum V_{max} (the pressing position denoted by "q" in the Figure).

On the other hand, in the event that the down direction key or right direction key is pressed and the electroconductive member 50 comes into contact with the second variable resistor 44 portion of the resistor 40 (the pressing position denoted by "r" in the Figure), the contact area of the electroconductive member 50 as to the second variable resistor 44 portion subsequently increases according to the pressing force of the operating key 211a, so the resistance value at that portion decreases, and consequently analog signals (voltage) output from the output terminal 40c of the resistor 40 decrease. Then, at the point that the electroconductive member 50 has deformed as much as possible, the analog signals (voltage) output from the output terminal 40c of the resistor 40 reach a minimum V_{min} (the pressing position denoted by "s" in the Figure).

The analog signals (voltage) output from the output terminal 40c of the resistor 40 are input to the level dividing unit 15 as shown in Fig. 42, the output level of the analog signals is divided into a plurality at the level dividing unit 15, and further, the A/D converting unit 16 converts the analog signals output from the resistor 40 into digital signals according to the divided output level. The functions of the level dividing unit 15, A/D converting unit 16, and switching unit 18, shown in Fig. 42, are the same as already described based on Fig. 37, so detailed description will be omitted here.

As shown in Fig. 43, initial setting of the non-pressing value V_0 , minimal value V_{min} , and maximal value V_{max} , of the analog signals (voltage) output from the resistor 40 at the time of operating is made beforehand at the division range setting unit 25, for individually setting (calibrating) the output level range of the analog signals divided by the level dividing unit 15. Also, regarding the maximal value V_{max} , an arbitrary tolerance value α is set beforehand, and regarding the minimal value V_{min} , an

arbitrary tolerance value β is set beforehand. These tolerance values α and β are for absorbing irregularities in the recognition of the output of the resistor (i.e., analog signals) from the information from the A/D converting unit 16. Further, a judging value γ for judging whether or not the operating button is in a pressed state is set
5 beforehand around the analog signal (voltage) V_0 output in the case of no pressing operation.

The division range setting unit 25 executes the calibration operation as follows under such settings.

Once the power is turned on to the operating device, first, the division range
10 setting unit 25 recognizes the level $V_0(\text{Real})$ of the analog signal (voltage) actually being output from the resistor 40 based on information from the A/D converting unit 16, in order to adjust the analog signals (voltage) V_0 output from the resistor 40 at the time of the button not being pressed.

At this time, the user may be pressing an operating key, so judgement is made
15 whether or not $V_0(\text{Real})$ is within the range of the margin of error tolerance value γ centered around V_0 . In the event that $V_0(\text{Real})$ is out of the range of $(V_0 + \gamma) < V_0(\text{Real}) < (V_0 - \gamma)$, an action for notifying the user that calibration is running is executed.

As for this action, for example, a display portion 253 provided in the operating
20 device may be lit or may blink, or in the event that the operating device has a vibration function built in this function may be activated, or like means can be used.

Next, under the condition that $V_0(\text{Real})$ is within the range of $(V_0 + \gamma) < V_0(\text{Real}) < (V_0 - \gamma)$, the value of $V_0(\text{Real})$ is compared with V_0 . In the event that the comparison results in $V_0(\text{Real}) > V_0$, the initial setting value V_0 is set as the value of
25 the analog signals (voltage) output from the resistor 40 at the time of non-pressing. On the other hand, in the event that $V_0(\text{Real}) < V_0$ holds, the actual output value $V_0(\text{Real})$ is newly set as the minimal value of the analog signals (voltage) output from

the resistor 40 at the time of non-pressing.

Next, the user is made to firmly press the up direction key following an operating manual or the like, and the level $V_{\max}(\text{Real})$ of the analog signal (voltage) actually being output from the resistor 40 based on information output from the A/D
5 converting unit 16 is recognized.

In the event that $V_{\max}(\text{Real})$ is greater than $(V_{\max} - \alpha)$ which includes the tolerance value α , recognition is made that the user has pressed the up direction key to the limit, and thus $V_{\max}(\text{Real})$ and V_{\max} are compared. In the event that the comparison results in $V_{\max}(\text{Real}) < V_{\max}$, the initial setting value V_{\max} is set as the
10 maximal value of the analog signals (voltage) output from the resistor 40. On the other hand, in the event that $V_{\max}(\text{Real}) > V_{\max}$ holds, the actual output value $V_{\max}(\text{Real})$ is newly set as the maximal value of the analog signals (voltage) output from the resistor 40.

The same operation is performed for the left direction key also, and the
15 maximum value V_{\max} for the analog signals (voltage) output from the resistor 40 at the time of pressing the left direction key is set.

Next, the user is made to firmly press the down direction key following an operating manual or the like, and the level $V_{\min}(\text{Real})$ of the analog signal (voltage) actually being output from the resistor 40 based on information output from the A/D
20 converting unit 16 is recognized.

In the event that $V_{\min}(\text{Real})$ is greater than $(V_{\min} - \beta)$ which includes the tolerance value β , recognition is made that the user has pressed the down direction key to the limit, and thus $V_{\min}(\text{Real})$ and V_{\min} are compared. In the event that the comparison results in $V_{\min}(\text{Real}) > V_{\min}$, the initial setting value V_{\min} is set as the
25 minimal value of the analog signals (voltage) output from the resistor 40. On the other hand, in the event that $V_{\min}(\text{Real}) < V_{\min}$ holds, the actual output value $V_{\min}(\text{Real})$ is newly set as the minimal value of the analog signals (voltage) output from the resistor

40.

The same operation is performed for the right direction key also, and the minimal value V_{min} for the analog signals (voltage) output from the resistor 40 at the time of pressing the right direction key is set.

5 The division range setting unit 25 controls the level dividing unit 15 regarding the up direction key and left direction key so as to uniformly divide the analog signals (voltage) output from the resistor 40, within the range from the output V_0 at the time of non-pressing to the maximal value V_{max} , set as described above. The division range setting unit 25 also controls the level dividing unit 15 regarding the down direction key
10 and right direction key so as to uniformly divide the analog signals (voltage) output from the resistor 40, within the range from the output V_0 at the time of non-pressing to the minimal value V_{min} .

Incidentally, though in the above description, the up direction key and left direction key are appropriated to the first variable resistor portion of the resistor 40, and
15 the down direction key and right direction key are appropriated to the second variable resistor portion, but the present invention is by no means restricted to this arrangement, and it is needless to say that appropriation of the keys and variable resistor portions may be carried out arbitrarily.

Also, regarding the first operating unit 210, individually provided resistors 40
20 may be positioned for the electroconductive members 50 provided for the operating keys 211a of the operating member 211, so as to have a circuit configuration such as shown in Fig. 35. In this case, the properties of the analog signals (voltage) output from the output terminal 40c of the resistor 40 are as shown in Fig. 36.

Variation of Detecting Element

25 Next, a variation example of the detecting element made up of a resistor 40 and electroconductive member 50 will be described. Incidentally, though the following description is made regarding an example of a detecting element provided in the second

operating unit 220, it is needless to say that application of the following detecting element can be made to other operating units, as well.

Figs. 44 through 47 illustrate a detecting element wherein the form of the electroconductive member 50 has been changed. Here, Figs. 44A through 47A are
5 frontal cross-sectional views of the operating unit including the detecting element, Figs. 44B through 47B are frontal views of the electroconductive member, Figs. 44C through 47C are views of the electroconductive member from below, and Figs. 44D through 47D are properties diagrams for analog signals output from the output terminal of the resistor.

10 The electroconductive members 50 showed in these drawings are of a form in each case wherein the area of contact with the resistor 40 is changed according to the pressure of contact with the resistor 40.

That is, the detecting element shown in Figs. 44A-44D is formed so that the electroconductive member 50 has a trapezoidal longitudinal-sectional form (a conical
15 trapezoid in the Figure). With an electroconductive member 50 formed such, the apex 50a of the electroconductive member 50 comes in contact with the resistor 40 in conjunction with the pressing operation of the operating button 221, but the apex 50a is a flat plane, so the resistance value greatly drops at the instant of contact, and there is rapid increase of output voltage (analog signal) as indicated by "a" in Fig. 44D,
20 following which the output voltage continuously changes in a manner corresponding with the pressing force.

Accordingly, digital on/off action can be realized at the instant of the electroconductive member 50 making contact or breaking contact with the resistor 40. Now, though the example in Figs. 44A-44D shows a conical trapezoid, other
25 arrangements may be used for the electroconductive member 50, e.g., longitudinal-sectional trapezoid forms of pyramids with a triangular base, quadrangular base, or polygonal base.

The detecting element shown in Figs. 45A-45D uses an electroconductive member 50 formed having vertical ribs 50b formed on the perimeter of a mountain-like formation. Though the electroconductive member 50 with a mountain-like form shown in Fig. 24 may buckle in the event that the direction of application of pressing
5 force tilts away from the center axis, forming ribs 50b to the electroconductive member 50 as shown in Figs. 45A-45D suppresses the danger of the electroconductive member 50 bucking. Combining this form with a cross-shaped operating member 211 such as shown in Fig. 39 in particular markedly manifests the effects thereof.

The detecting element shown in Figs. 46A-46D has the surface of the
10 electroconductive member 50 formed in a spherical shape. Bucking of the electroconductive member 50 can also be avoided by thus forming the electroconductive member 50 in a spherical shape.

The detecting element shown in Figs. 47A-47D has the electroconductive member 50 formed in a stepped mountain-like form wherein the cross-sectional area
15 thereof becomes smaller in steps toward the apex facing the resistor 40. With an electroconductive member 50 of such a form, the amount of deformation increases as the pressing force increases, but in the processes, at the point that the step portion 50c comes into contact with the resistor 40, the contact area suddenly increases and the resistance value drops. Accordingly, the analog signals output from the output
20 terminal of the resistor 40 change in steps, as shown in Fig. 47D. Accordingly, boundaries at which analog output rapidly changes can be easily recognized, and stable level division can be easily made. Also, the analog output changes in steps as to the pressing force, so the user can easily adjust the pressing force.

Figs. 48A through 50D are diagrams illustrating a variation example of the
25 detecting element wherein the form of the resistor 40 has been changed. In the drawings, Figs. 48A through 50A are frontal cross-sectional views of the operating unit including the detecting element, Figs. 48B through 50B are frontal views of the

electroconductive member, Figs. 48C through 50C are views of the electroconductive member from above, and Figs. 48D through 50D are properties diagrams for analog signals output from the output terminal of the resistor.

The resistors 40 shown in these drawings each have a form wherein the cross-sectional area decreases toward the apex facing the electroconductive member 50. With the detecting element shown in Figs. 48A-48D, the resistor 40 is formed in a mountain-like shape, such that in the event that the electroconductive member 50 descends in conduction with the operating button 221, the electroconductive member 50 comes into contact with the resistor 40 and deforms. The resistor 40 is formed in a mountain-like shape, so the area of contact with the electroconductive member 50 increases continuously in accordance with the pressing force, and analog signals output from the output terminal of the resistor 40 continuously change as shown in Fig. 48D.

The detecting element shown in Figs. 49A-49D is formed so that the resistor 40 has a trapezoidal longitudinal-sectional form (a conical trapezoid in the Figure). With a resistor 40 formed such, the apex 40a of the resistor 40 first comes in contact with the electroconductive member 50 in conjunction with the pressing operation of the operating button 221, but the apex 40a is a flat plane, so there is rapid increase of output voltage as indicated by "a" in Fig. 49D at the instant of contact, following which the output voltage continuously changes in a manner corresponding with the pressing force.

Accordingly, digital on/off action can be realized at the instant of the electroconductive member 50 making contact or breaking contact with the resistor 40. Now, though the example in Figs. 49A-49D shows a conical trapezoid, other arrangements may be used for the resistor 40, e.g., longitudinal-sectional trapezoid forms of pyramids with a triangular base, quadrangular base, or polygonal base.

The detecting element shown in Figs. 50A-50D has the surface of the resistor 40 formed in a spherical shape. Thus forming the resistor 40 in a spherical shape provides properties approximately the same as those of the detecting element shown in

Figs. 46A-46D.

The detecting element shown in Figs. 51A-51D has the resistor 40 formed in a stepped mountain-like form wherein the cross-sectional area thereof becomes smaller in steps toward the apex thereof facing the electroconductive member 50. In the process of a resistor 40 of such form coming into contact while deforming electroconductive member 50 in conjunction with the pressing operation, and at the point that the step portion 40c of the resistor 40 comes into contact with the electroconductive member 50, the contact area suddenly increases and the resistance value drops. Accordingly, the analog signals output from the output terminal of the resistor 40 change in steps, as shown in Fig. 51D. Accordingly, boundaries at which analog signals rapidly change can be easily recognized, and stable level division can be easily made. Also, the analog output changes in steps as to the pressing force, so the user can easily adjust the pressing force.

The detecting element shown in Figs. 52A-52D has the electroconductive member 50 formed in a mountain-like form and configured so as to divide the contact area of the resistor 40 with the electroconductive member 50 by gaps 41, so as to increase in steps the contact area with the electroconductive member 50 in conjunction with the deformation thereof. More specifically, the resistor 40 is formed as shown in Figs. 52B-52D.

With the detecting device of the configuration shown in the Figure, the apex of the electroconductive member 50 first comes into contact with the center portion 40c of the resistor 40, in conjunction with the pressing operation of the operating button 221. Subsequently, as the electroconductive member 50 deforms with the increasing pressing force, the contact area with the electroconductive member 50 increases in steps in the order of the perimeter portions 40d, 40e, and 40f of the resistor 40, and the resistance value decreases accordingly.

The portions 40a through 40f of the resistor 40 are divided by the gaps 41, so as

long as the electroconductive member 50 passes through the gaps 41, there is no change in the resistance value, and accordingly the output voltage (analog signal) is generally constant.

Accordingly, the analog signals output from the output terminal of the resistor 5 40 change in steps, as shown in Fig. 52D. Accordingly, boundaries at which analog output rapidly changes can be easily recognized, and stable level division can be easily made.

Now, in the detecting elements of the above-described configurations, the positions of the resistor 40 and elastic member 50 in the operating unit may be reversed. 10 For example, as shown in Fig. 53, an arrangement may be made wherein the resistor 40 is applied by adhesion to the inner side ceiling plane of the elastic portion 222a formed on the elastic member 222, and the electroconductive member 50 is placed at a position facing the resistor 40, yielding the same effects and advantages as the above-described detecting elements.

15 It should be noted that the present invention is by no means restricted by the above-described embodiments.

For example, the operating device according to the present invention is not restricted to application to an operating device 200 for use with a video game apparatus as shown in Fig. 2; rather, it is needless to say that the present invention may be applied 20 to various types of operating devices wherein capabilities can be improved by enabling digital operation and analog operation.

According to the present invention as described above, the configuration is such that with the pressing operation of a single operator, multi-bit digital signals enabling analog control are output from multi-bit digital signal output means, and on the other 25 hand, single-bit digital signals enabling digital control are output from binary digital signal output means, so both digital operation and analog operation can be realized with a single operator, simply by selecting between these digital signals with switching

means and outputting the same.

CLAIMES

1. An operating device, comprising:
 - an operator operable by pressing;
 - 5 a detecting element for outputting analog signals corresponding to the pressing operation of said operator;
 - first digital signal output means for converting analog signals output from said detecting element in response to the pressing operation of said operator into digital signals comprising multiple bits according to the output level thereof;
 - 10 second digital signal output means for outputting digital signals comprising single bit according to change in the analog signals output from said detecting element;
 - and
 - switching means for switching output between digital signals output from said first digital signal output means and digital signals output from said second digital
 - 15 signal output means.
2. An operating device according to Claim 1, wherein said detecting element is a pressure-sensitive element placed at a position where pressing force acting upon said operator is transferred.
- 20 3. An operating device according to Claim 1, wherein said detecting element comprises:
 - an electroconductive member which moves in conjunction with said operator and has elasticity; and
 - 25 a resistor placed at the position where said electroconductive member makes and breaks contact;
 - and wherein said resistor outputs analog signals according to the area of contact

of said electroconductive member.

4. An operating device according to Claim 1, wherein said detecting element comprises:

5 a resistor which moves in conjunction with said operator; and
an electroconductive member which has elasticity and is placed at the position where said resistor makes and breaks contact;

and wherein said resistor outputs analog signals according to the area of contact of said electroconductive member.

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5. An operating device according to Claims 3 or 4, wherein said electroconductive member is of a configuration which deforms at the surface thereof facing said resistor according to the contact pressure applied thereupon with said resistor, such that the area of contact with said resistor changes.

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6. An operating device according to Claim 5, wherein said electroconductive member is formed so as to have a mountain-shaped longitudinal-sectional form.

7. An operating device according to Claim 5, wherein said electroconductive member is formed so as to have a trapezoidal longitudinal-sectional form.

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8. An operating device according to Claim 5, wherein said electroconductive member is formed so as to have a shape wherein the cross-sectional area thereof becomes smaller in steps toward the apex facing said resistor.

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9. An operating device according to Claim 5, wherein said electroconductive member is formed such that the surface facing said resistor has a spherical shape.

10. An operating device according to Claim 3 or 4, wherein said resistor is formed so as to have a shape wherein the cross-sectional area thereof becomes smaller toward the apex facing said electroconductive member.

5

11. An operating device according to Claim 10, wherein said resistor is formed so as to have a mountain-shaped longitudinal-sectional form.

12. An operating device according to Claim 10, wherein said resistor is formed so as to have a trapezoidal longitudinal-sectional form.

10

13. An operating device according to Claim 10, wherein said resistor is formed such that the surface facing said electroconductive member has a spherical shape.

14. An operating device according to Claim 3 or 4, wherein said resistor is formed so as to have a shape wherein the cross-sectional area thereof becomes smaller in steps toward the apex facing said electroconductive member.

15

15. An operating device according to Claims 3 or 4, wherein said electroconductive member is of a configuration which deforms according to the contact pressure applied thereupon with said resistor, such that the area of contact with said resistor changes; and wherein said resistor divides the contact area of said electroconductive member by gaps, and is configured so as to increase in steps the contact area of said electroconductive member in conjunction with the deformation thereof.

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16. An operating device, comprising:
an operator operable by pressing;

a detecting element for outputting analog signals corresponding to the pressing operation of said operator;

first digital signal output means for converting analog signals output from said detecting element in response to the pressing operation of said operator into digital signals comprising multiple bits according to the output level thereof;

a digital switch for turning on and off according to the pressing operation of said operator;

second digital signal output means for detecting the on/off state of said digital switch and outputting digital signals comprising single bits; and

switching means for switching output between digital signals output from said first digital signal output means and digital signals output from said second digital signal output means.

17. An operating device according to Claim 16, wherein said detecting element is a pressure-sensitive element positioned at a position to which the pressing force acting upon said operator is transferred;

and wherein said digital switch comprises:

first and second fixed terminals; and

an electroconductive movable member which makes and breaks contact with said first and second fixed terminals in conjunction with movement of said operator.

18. An operating device according to Claim 17, wherein first and second fixed terminals of said digital switch are formed on a sheet member which has flexibility, so as to transfer the pressing force from said operator to said pressure-sensitive element via said sheet member.

19. An operating device according to Claim 17, wherein said pressure-sensitive element is formed on the front side of a sheet member having flexibility, the moving member of said digital switch is formed on the rear side of said sheet member, and said first and second fixed terminals are positioned facing said moving member.

5

20. An operating device according to Claim 17, wherein the first and second fixed terminals of said digital switch are formed on the front side of a sheet member having flexibility, and said pressure-sensitive element is formed on the rear side of said sheet member.

10

21. An operating device according to any of the Claims 1 through 20, wherein said first digital signal output means comprises:

level dividing means for dividing into a plurality of levels the output level of analog signals output from said detecting element in conjunction with the pressing operation of said operator; and

15

A/D converting means for converting said analog signals into digital signals, according to each output level divided by said level dividing means.

22. An operating device according to Claim 21, wherein said level dividing means uniformly divides into a plurality the output level of said analog signals output from said detecting element in conjunction with the pressing operation of said operator.

20

FIG. 1

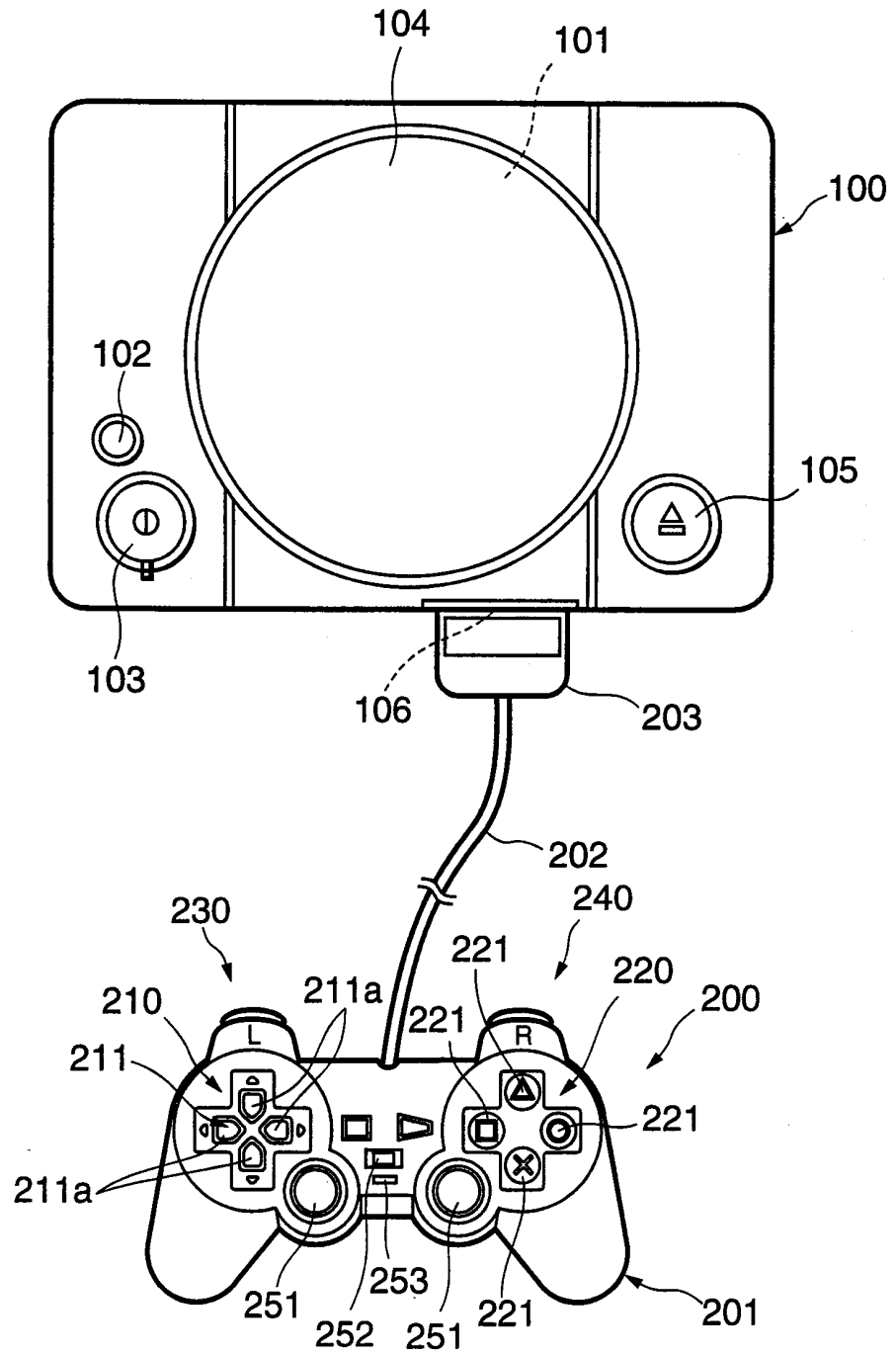


FIG. 2

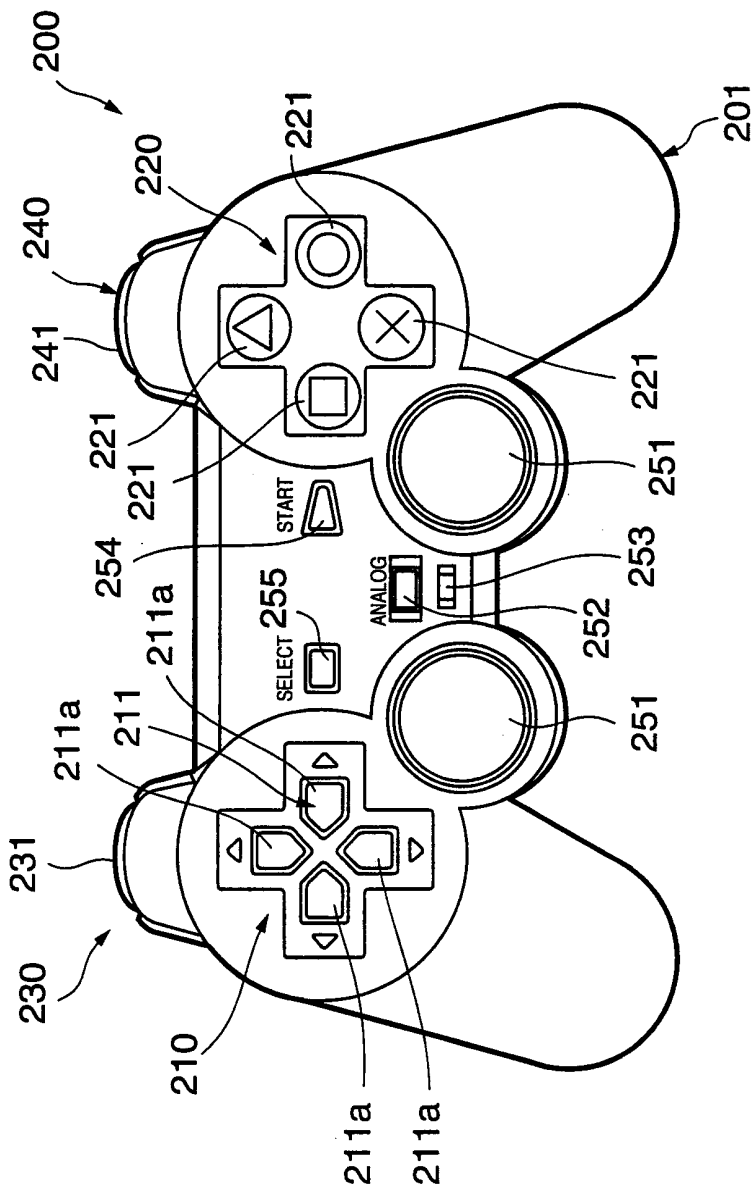


FIG. 3

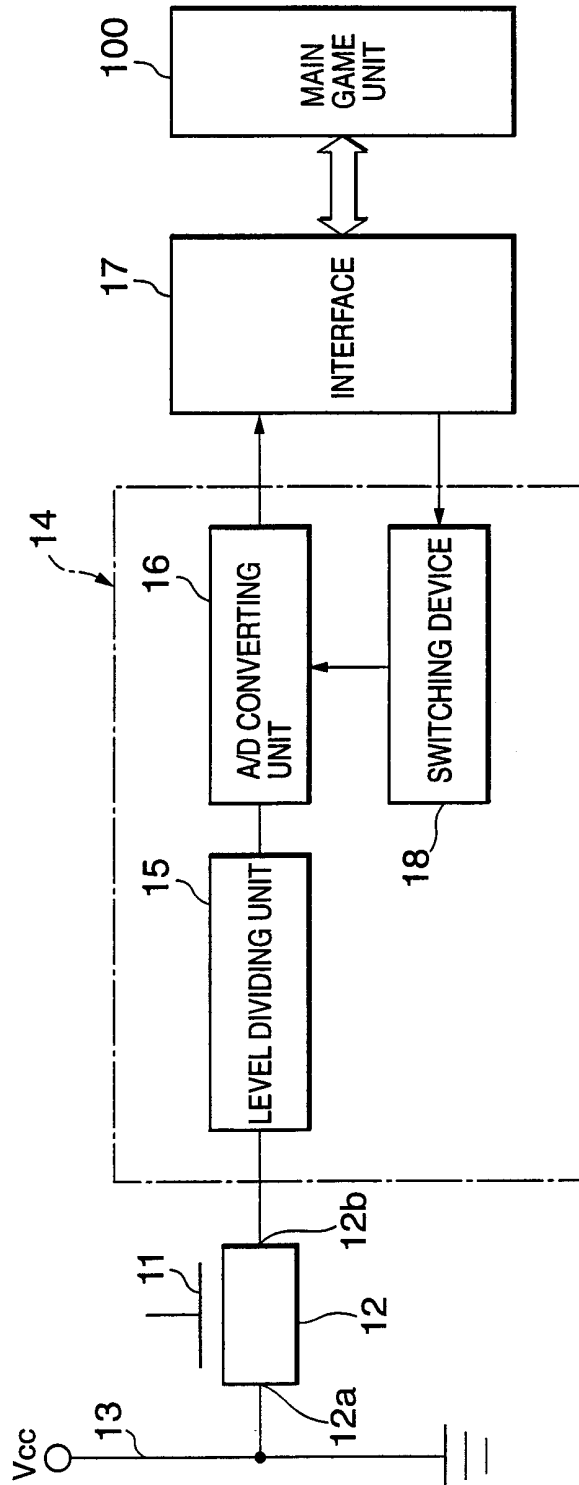


FIG. 4

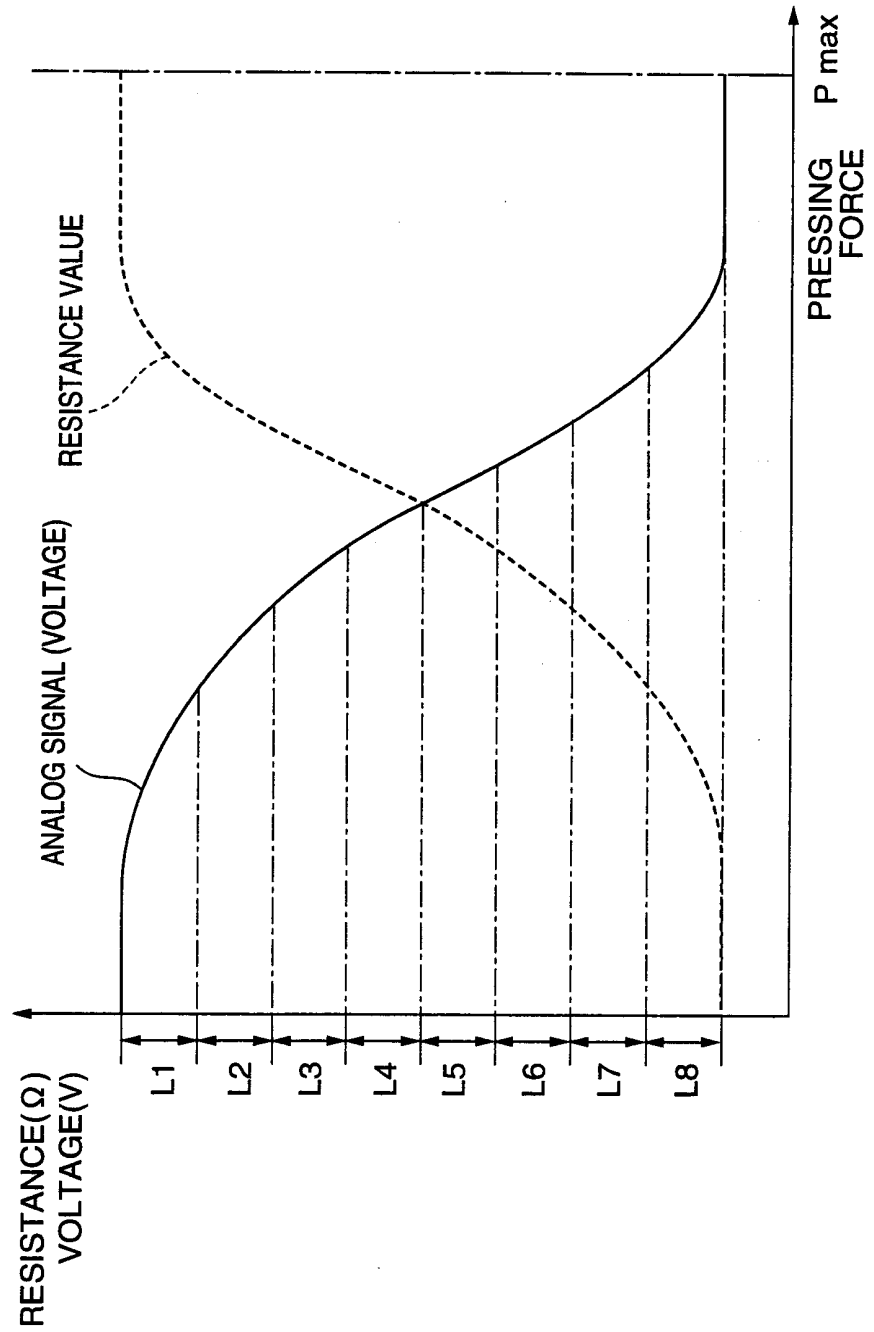


FIG. 5

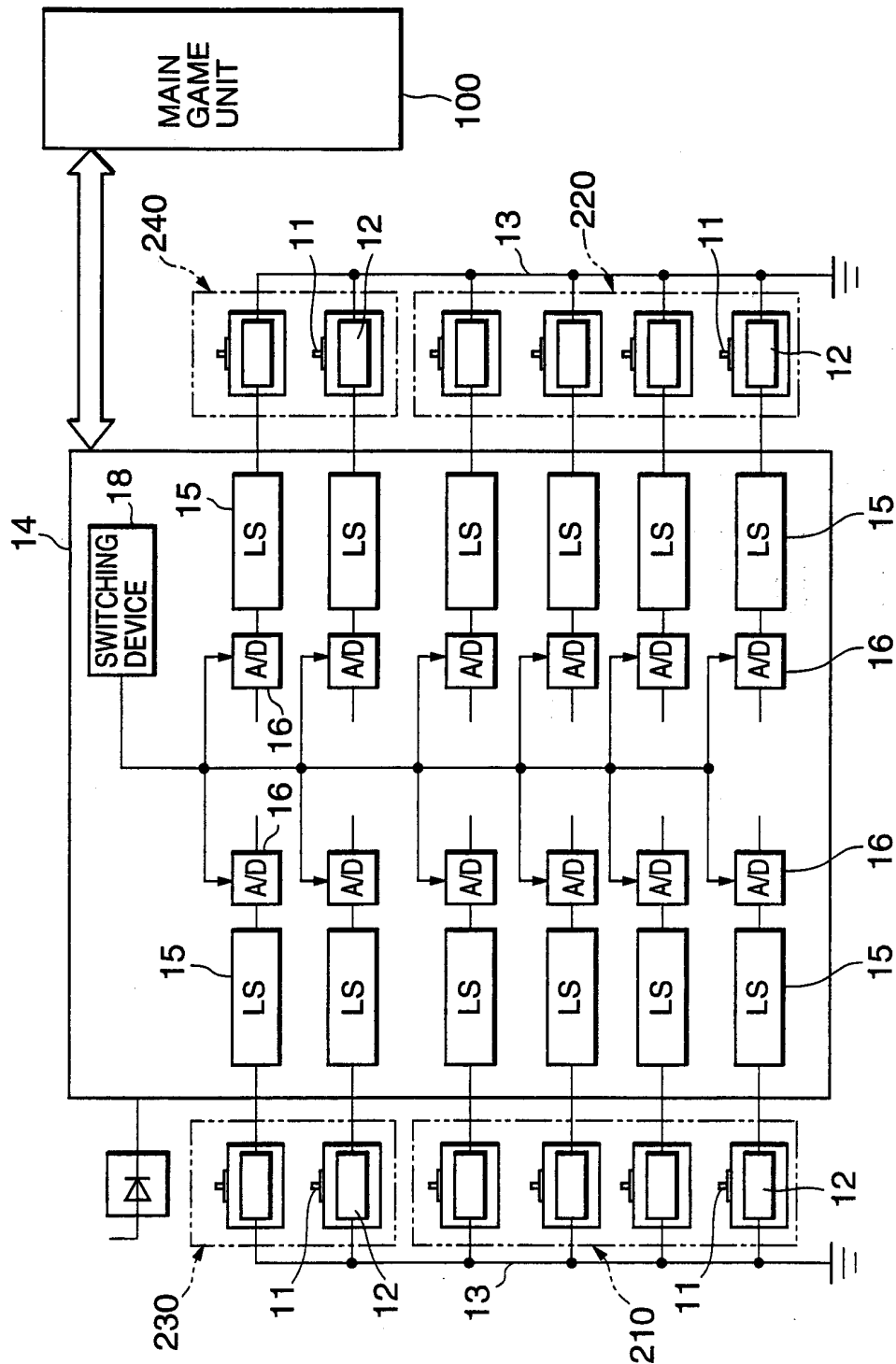


FIG. 6

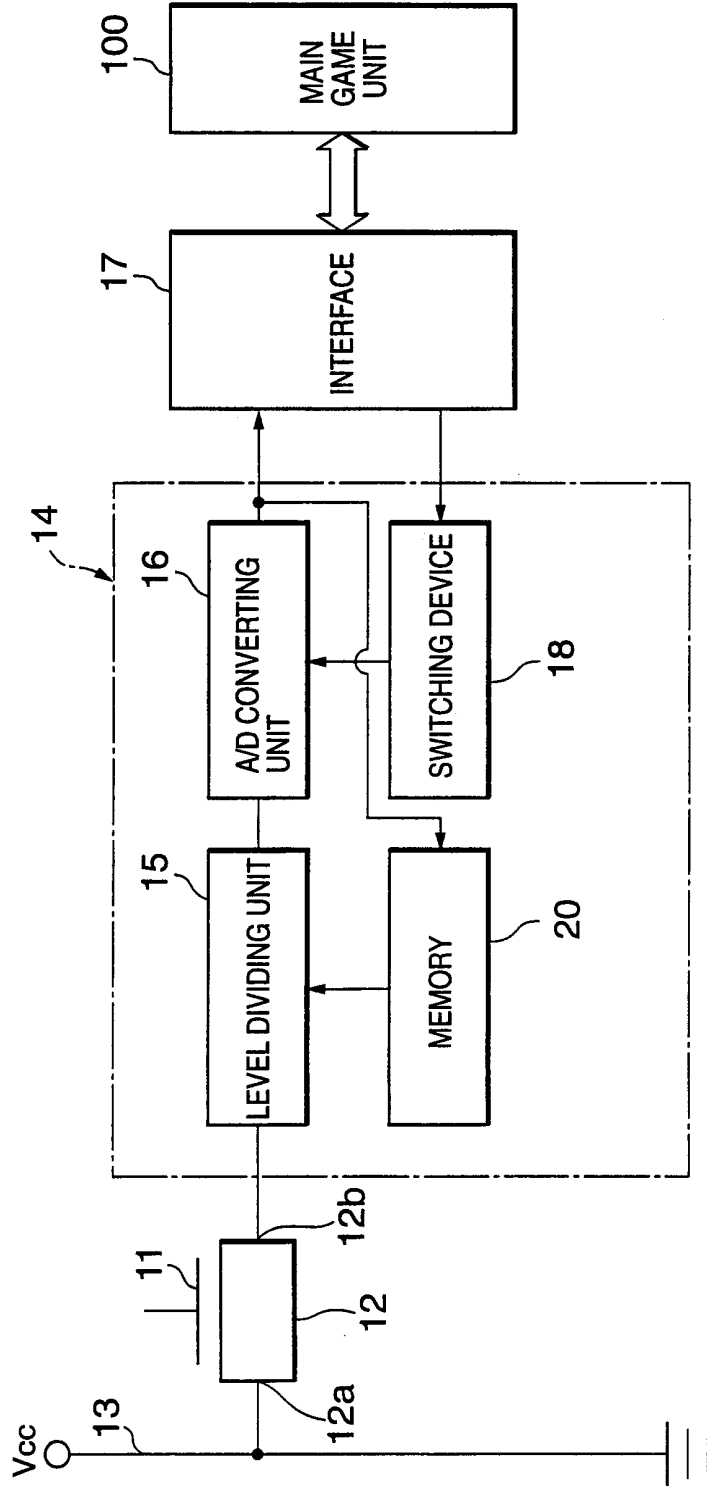


FIG. 7

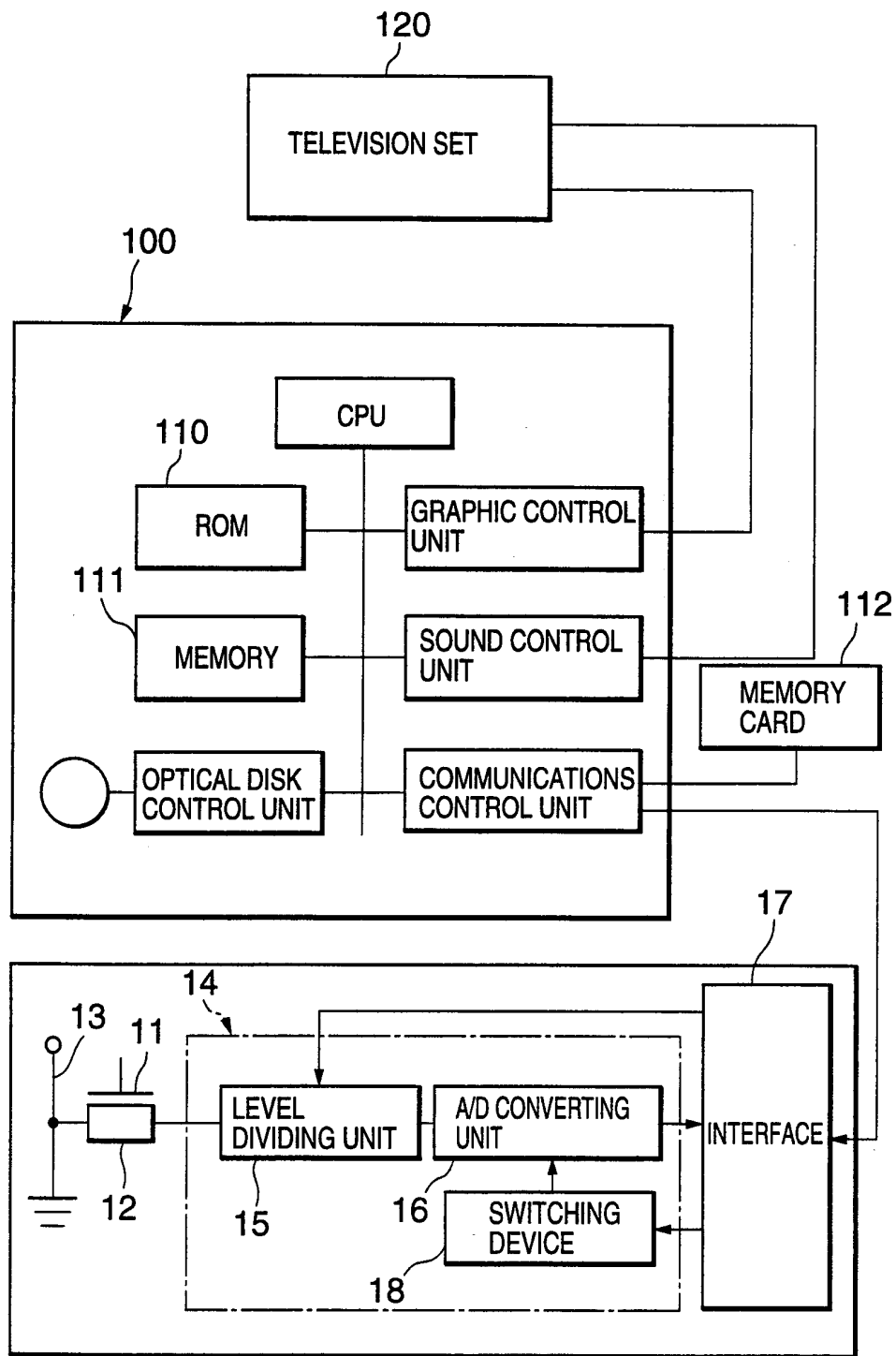


FIG. 8

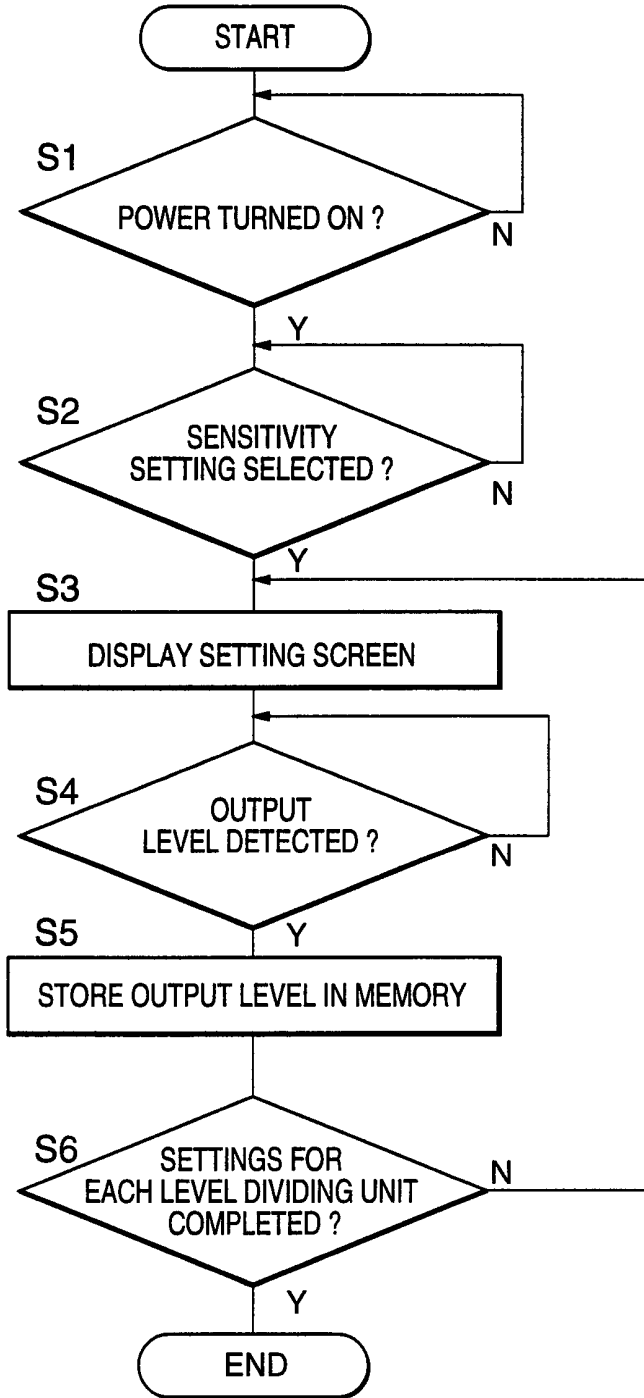


FIG. 9

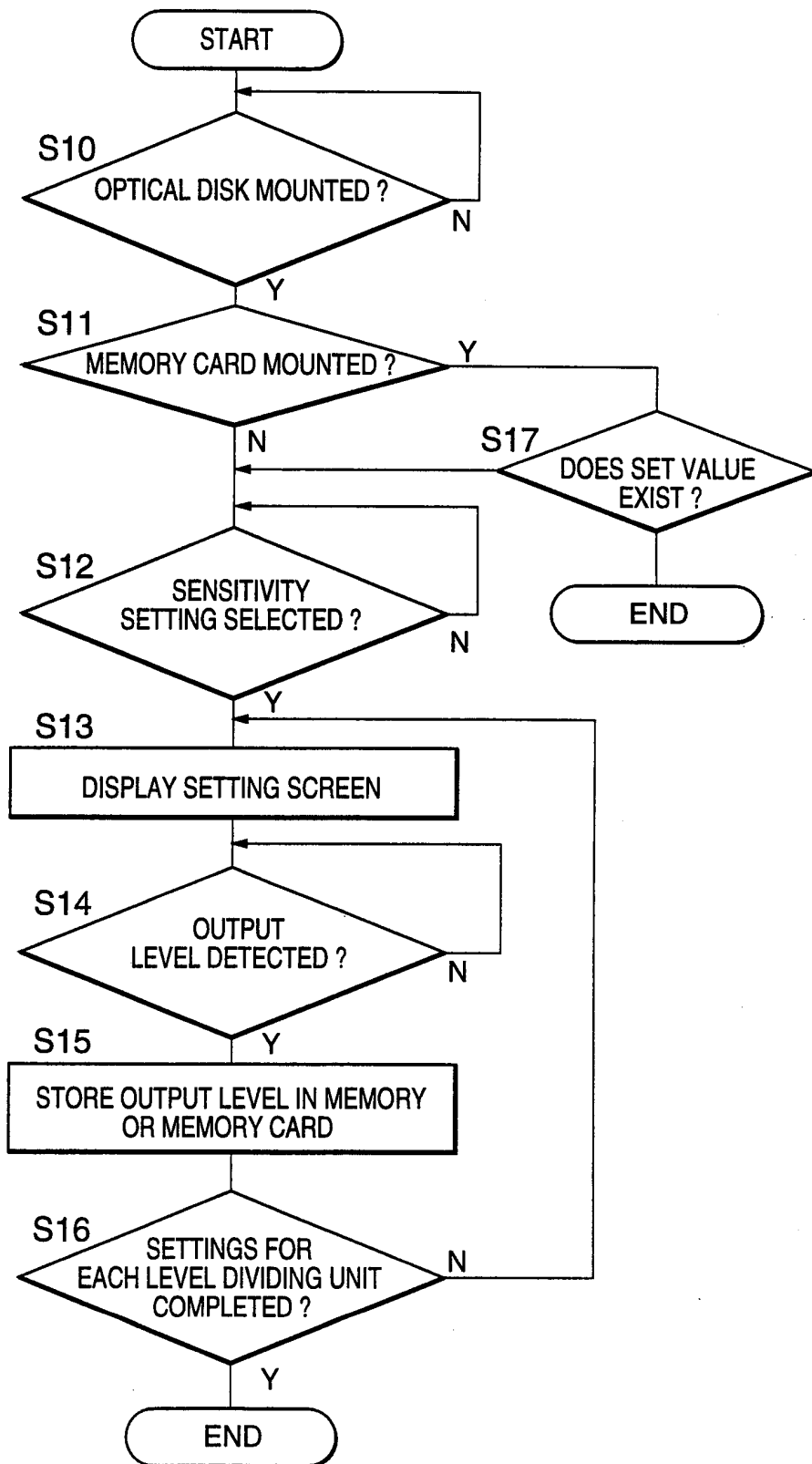


FIG. 10

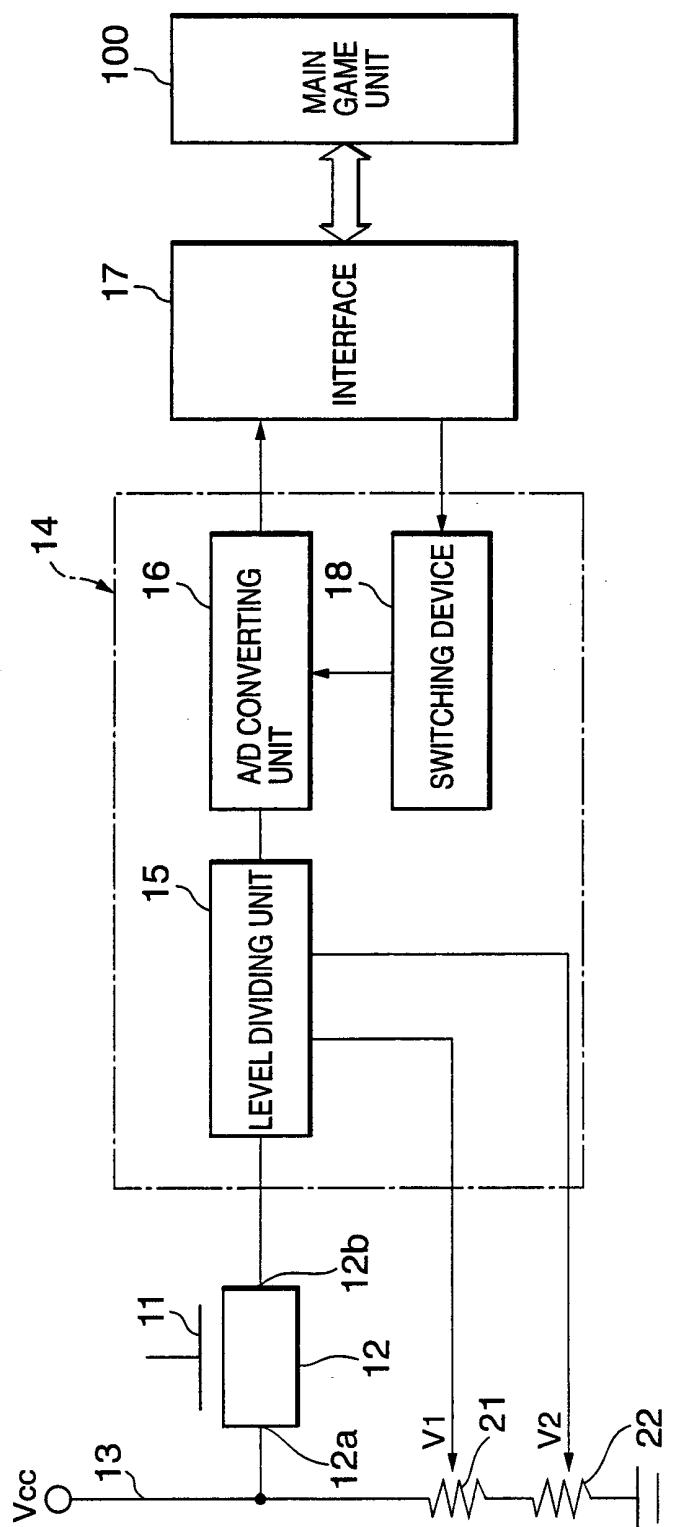


FIG. 11

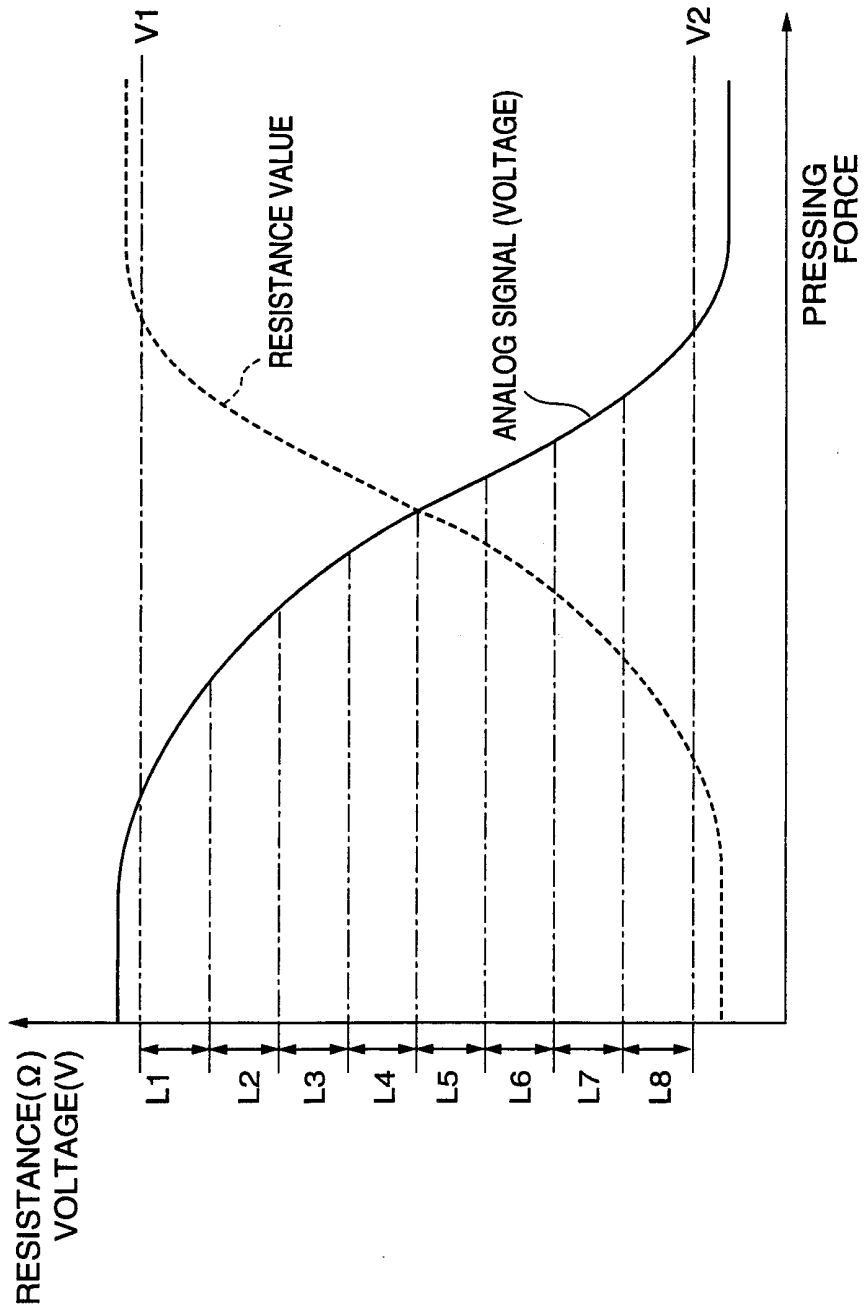


FIG. 12

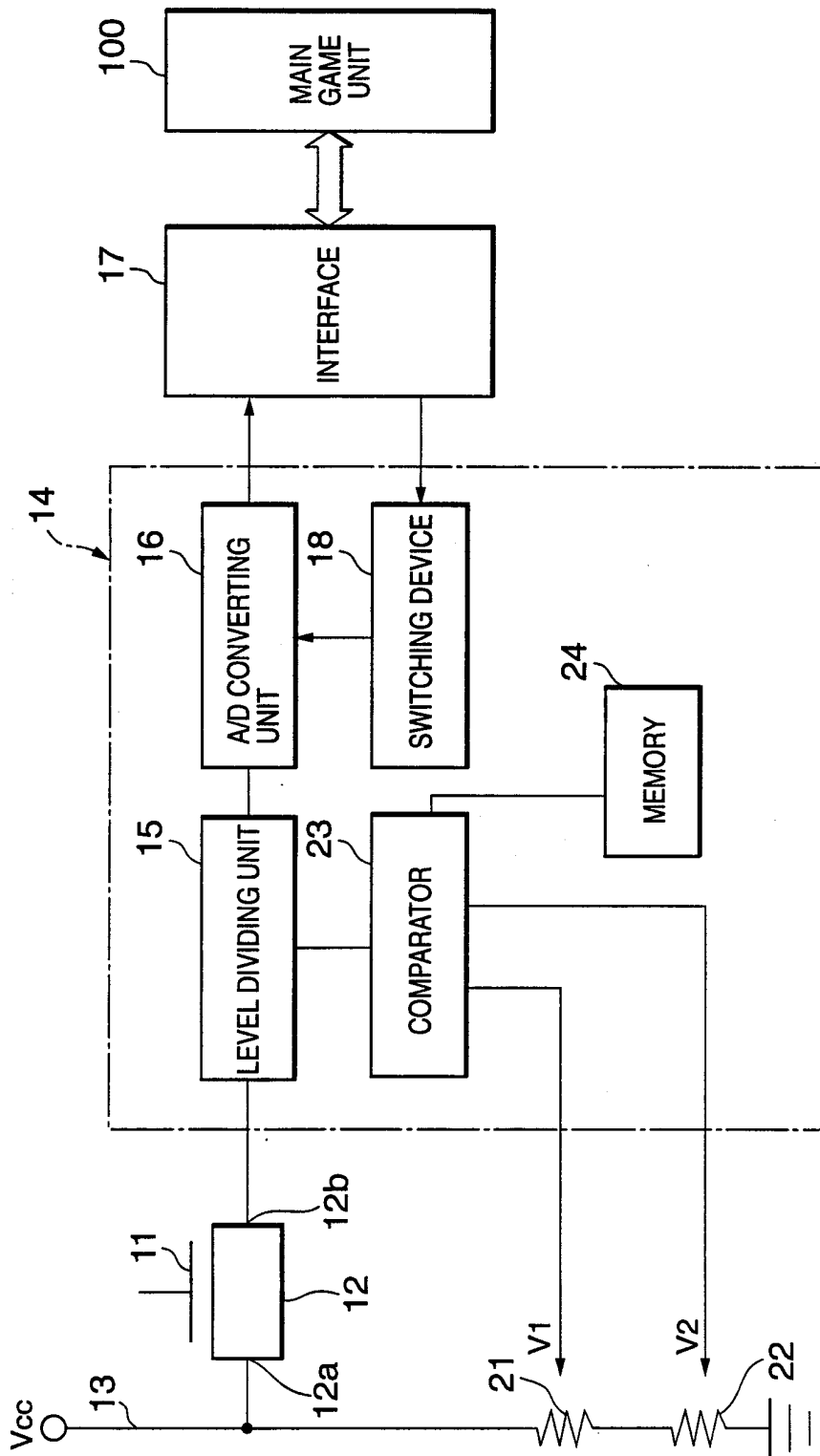


FIG. 13

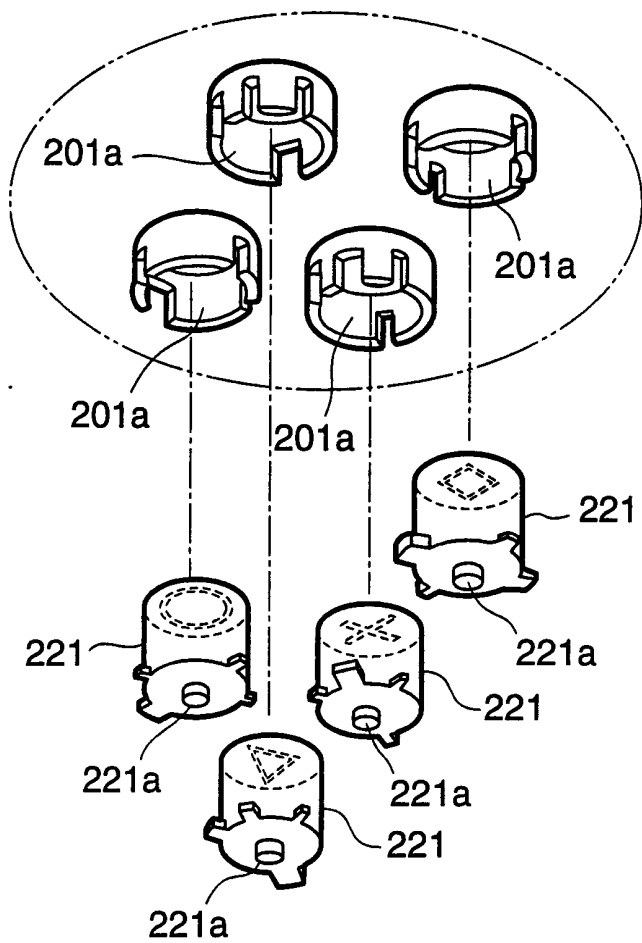


FIG. 14

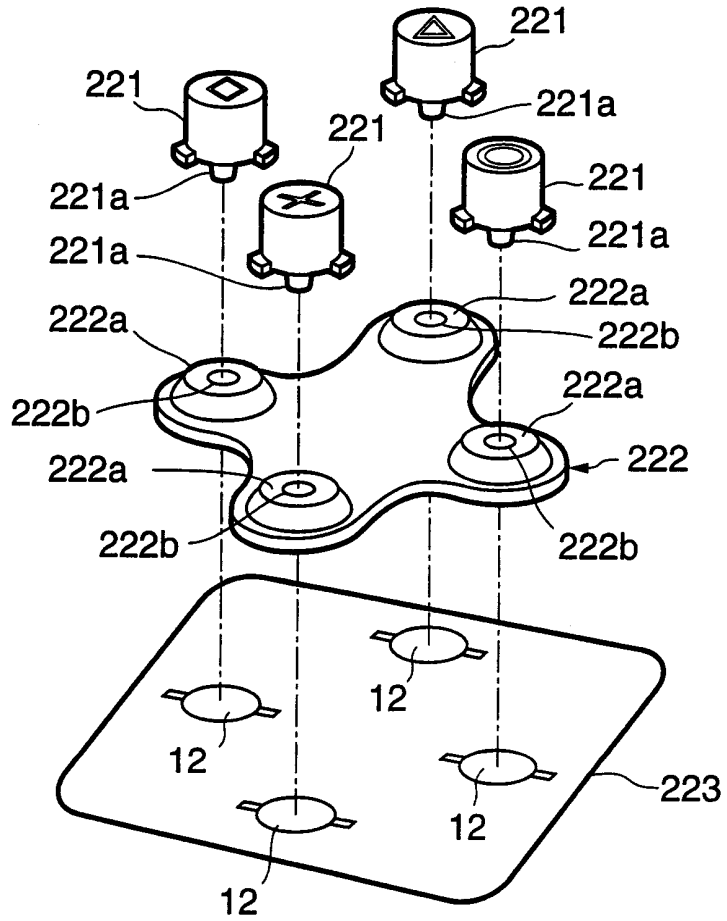


FIG. 15

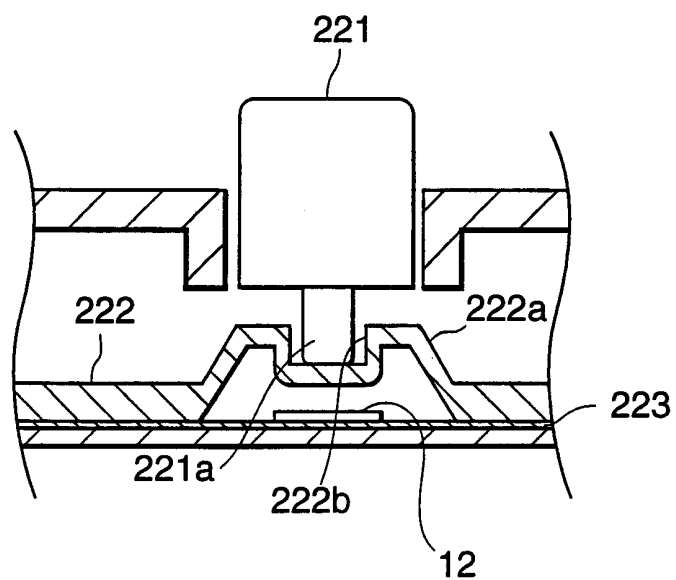


FIG. 16

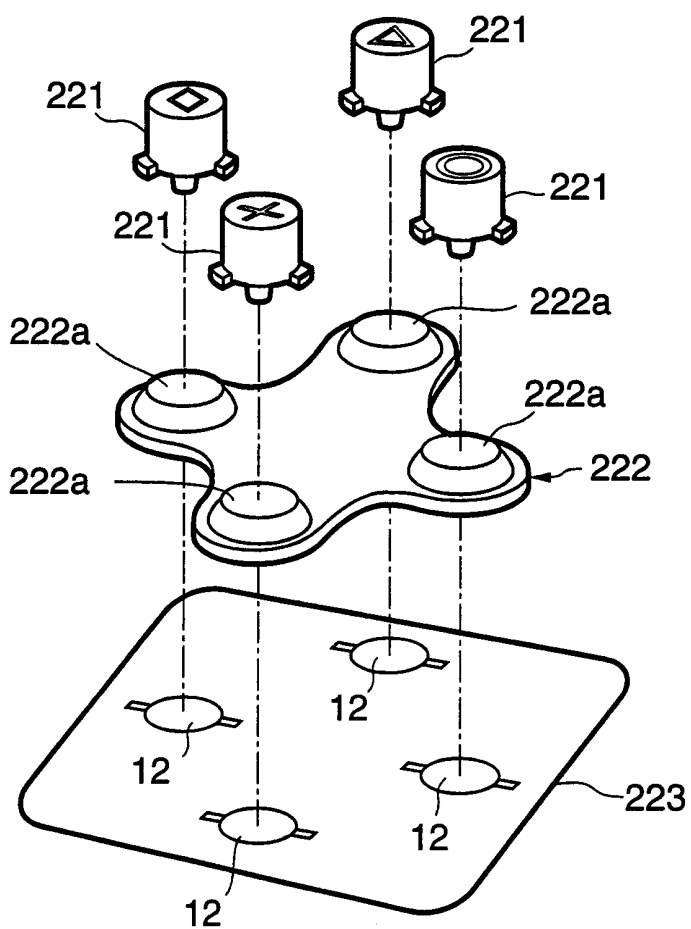


FIG. 17

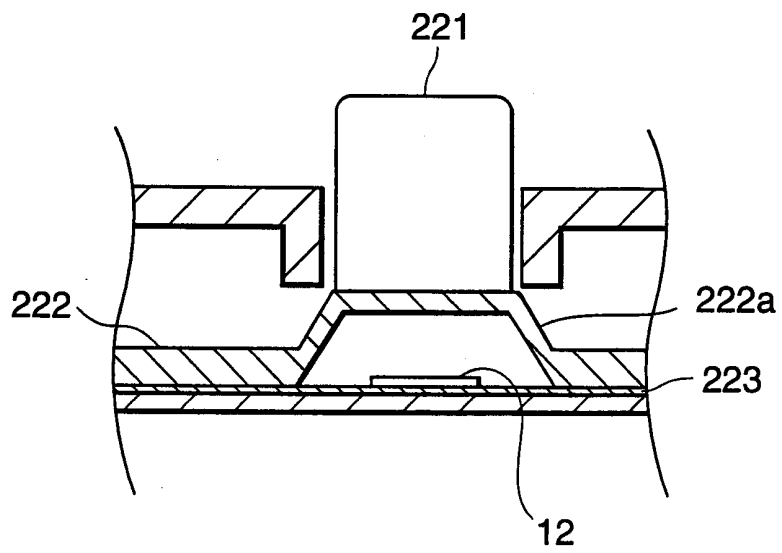


FIG. 18

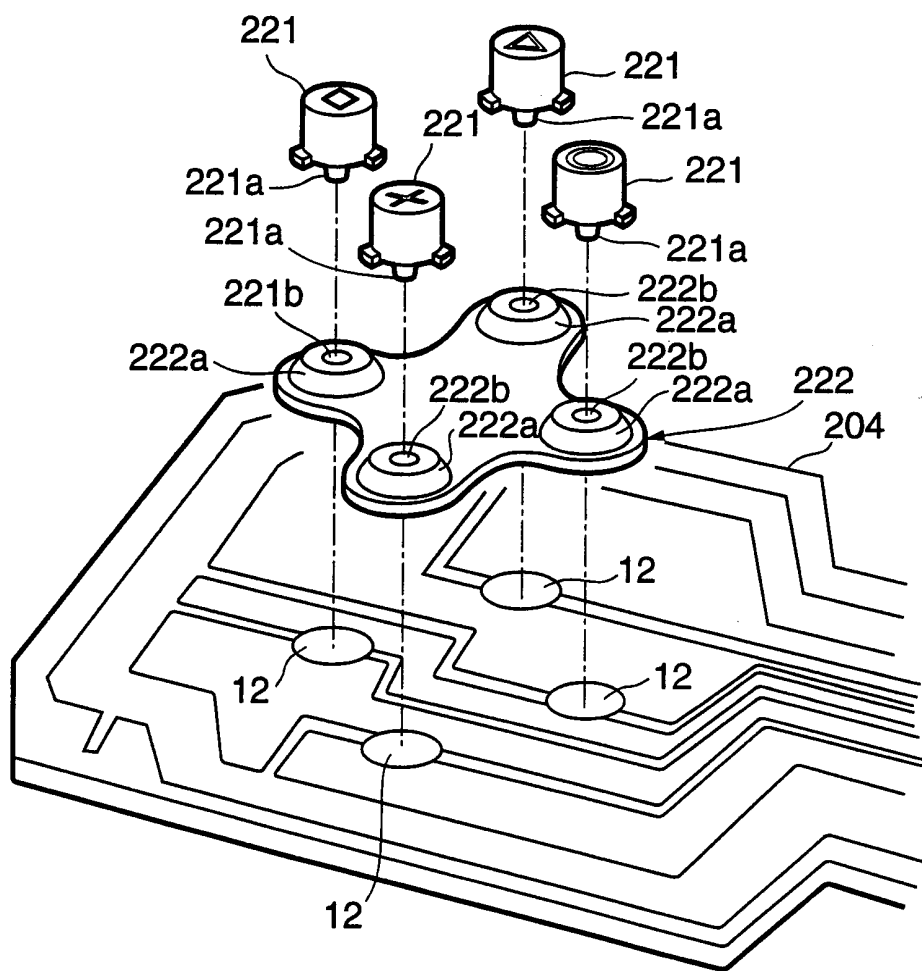


FIG. 19

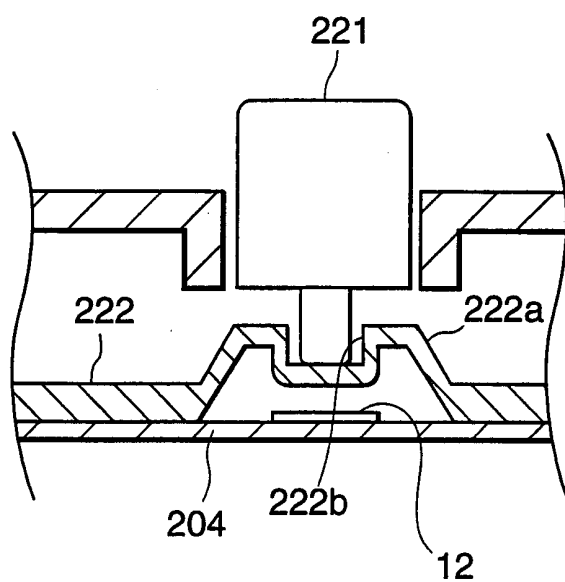


FIG. 20

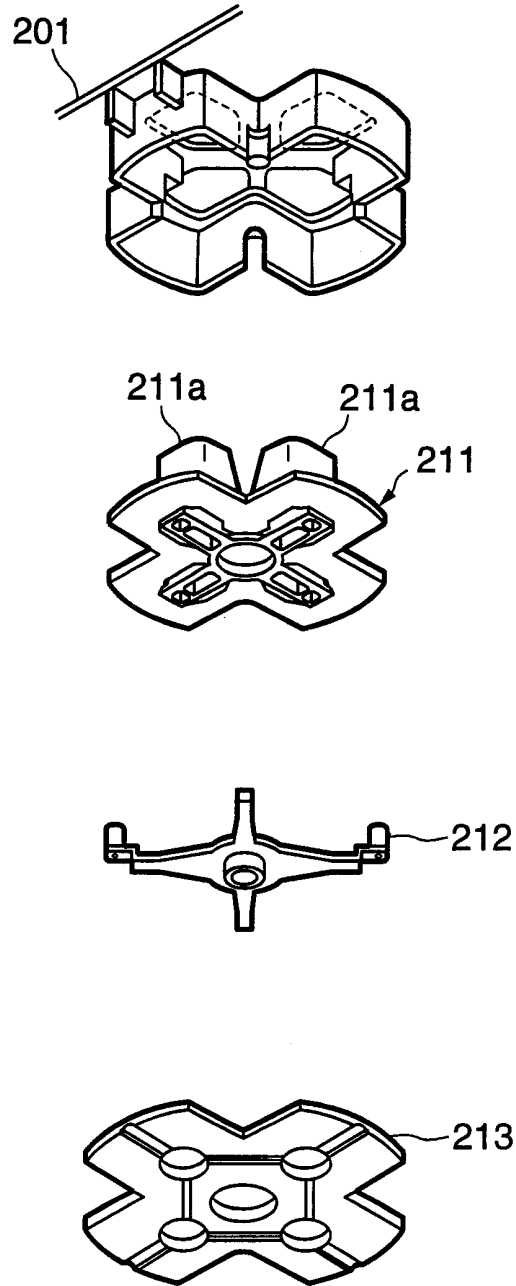


FIG. 21

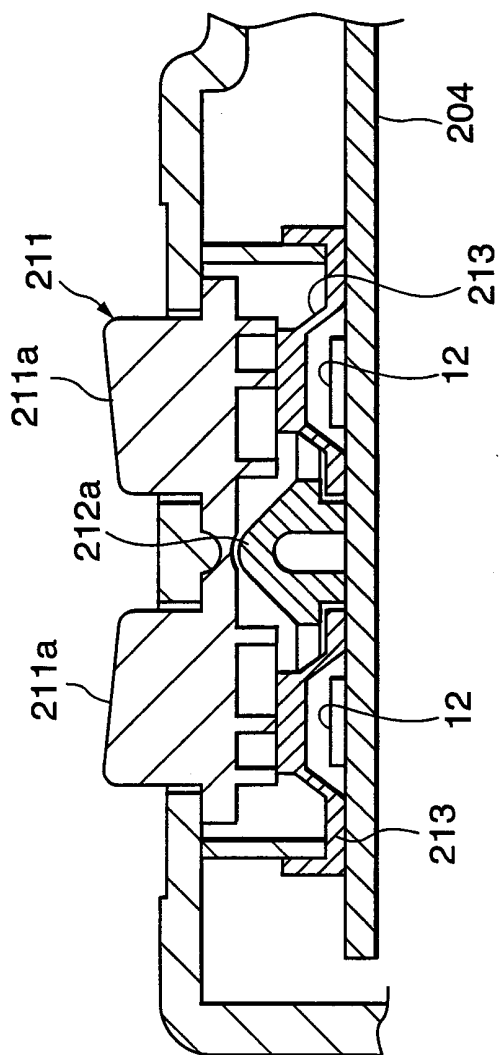


FIG. 22

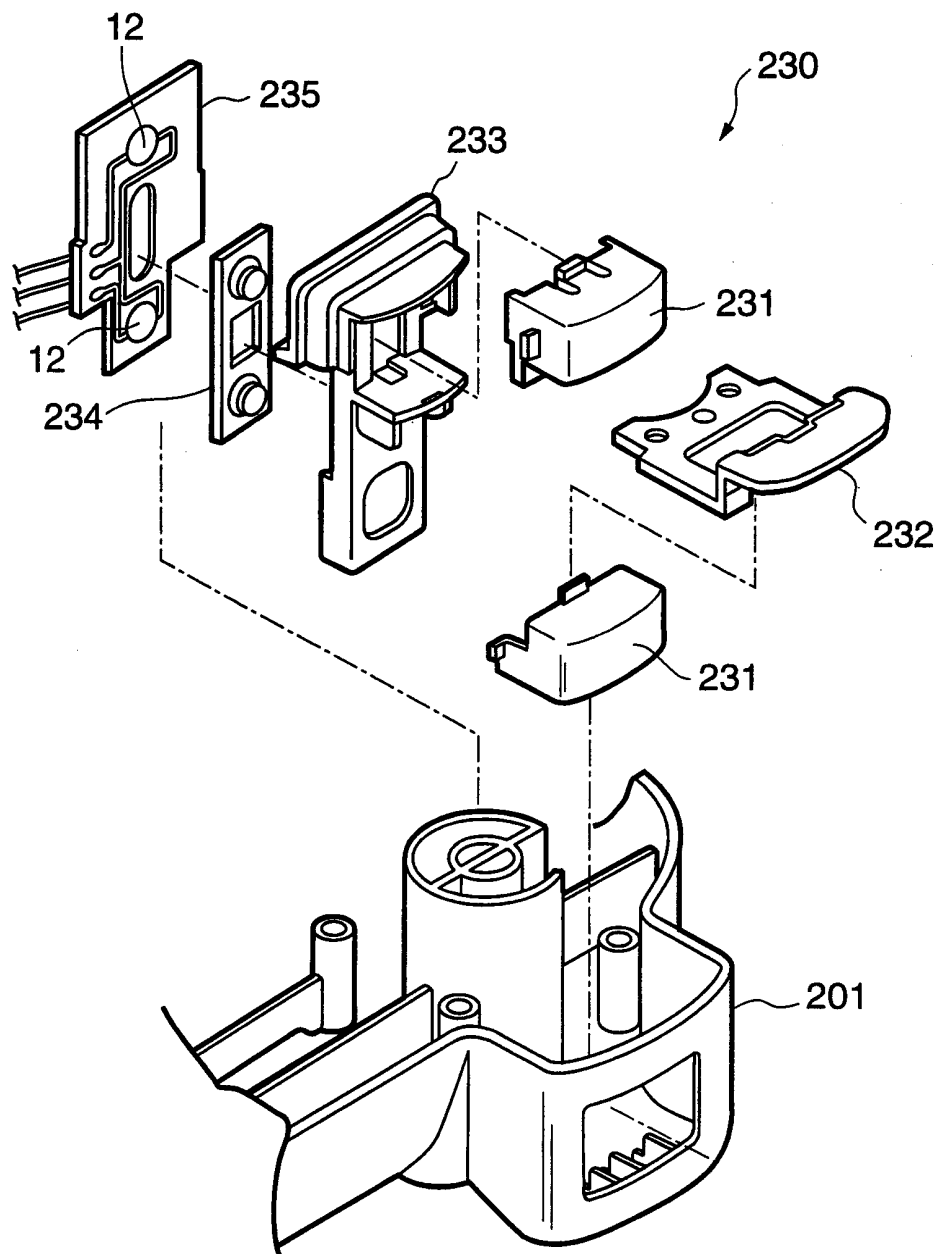


FIG. 23

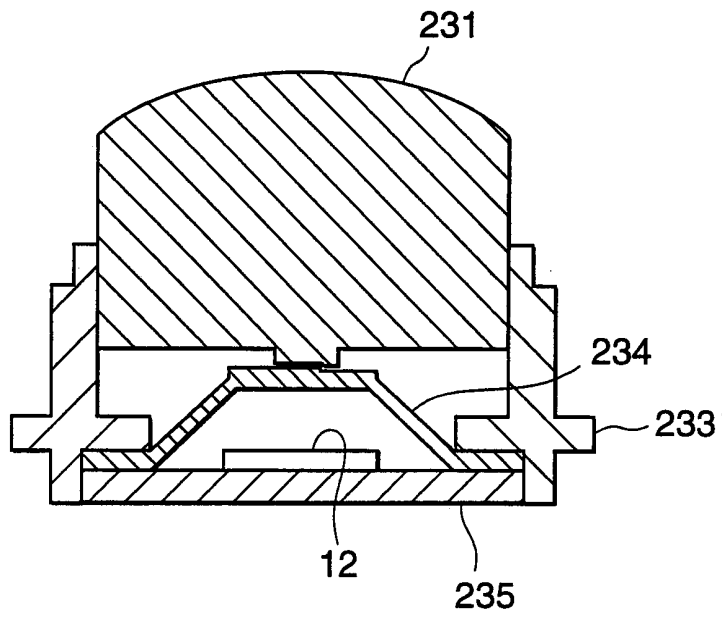


FIG. 24

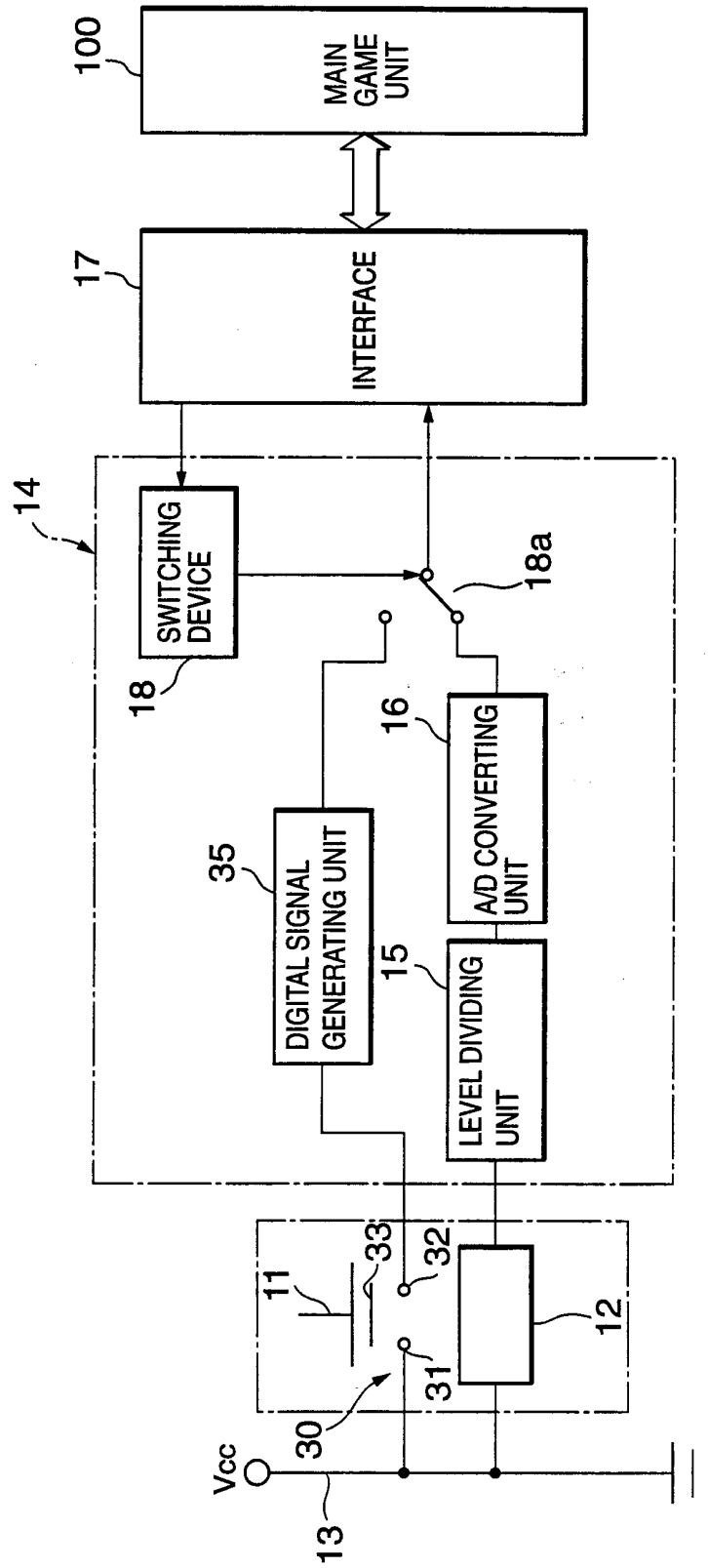


FIG. 25

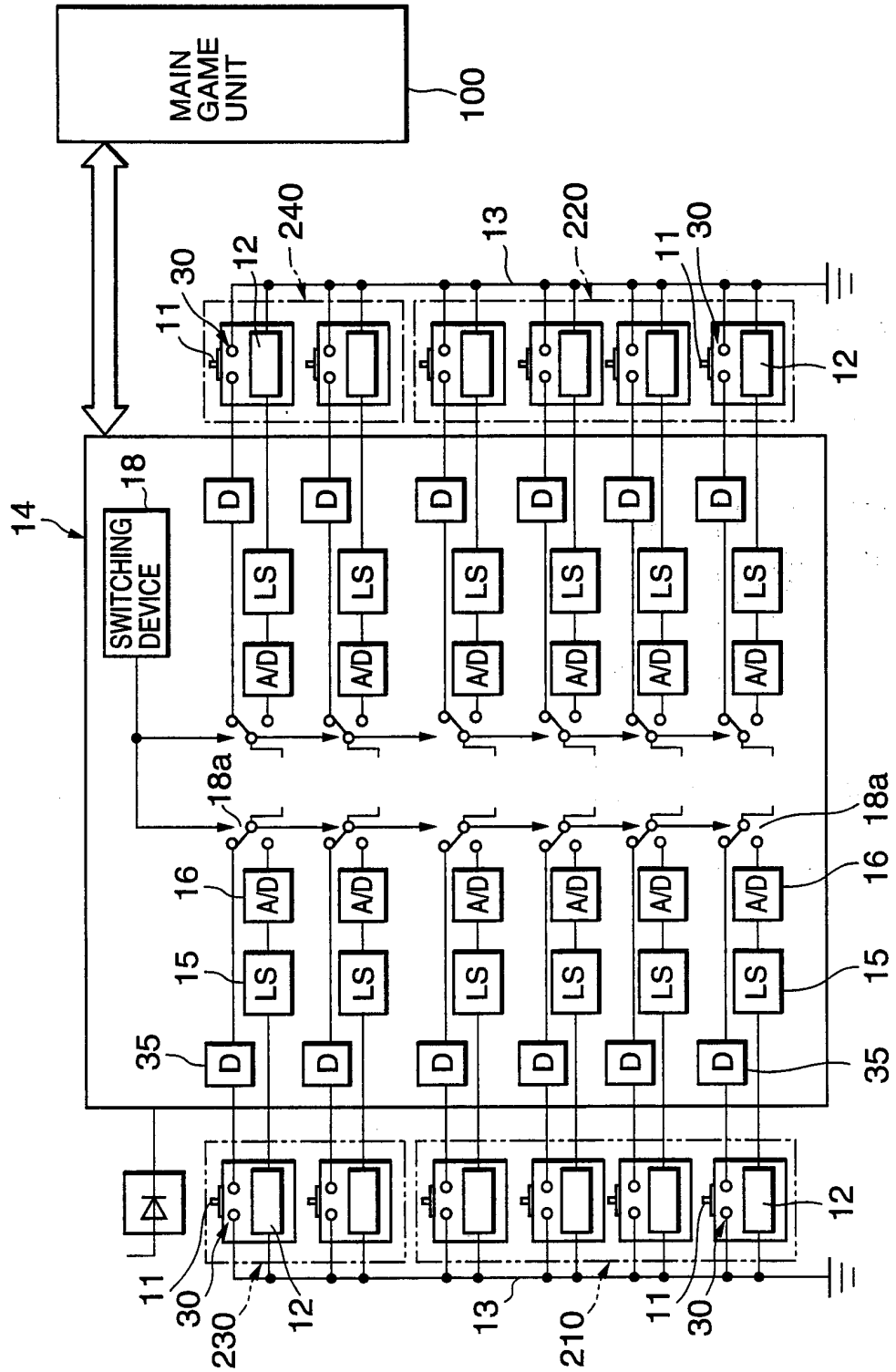


FIG. 26

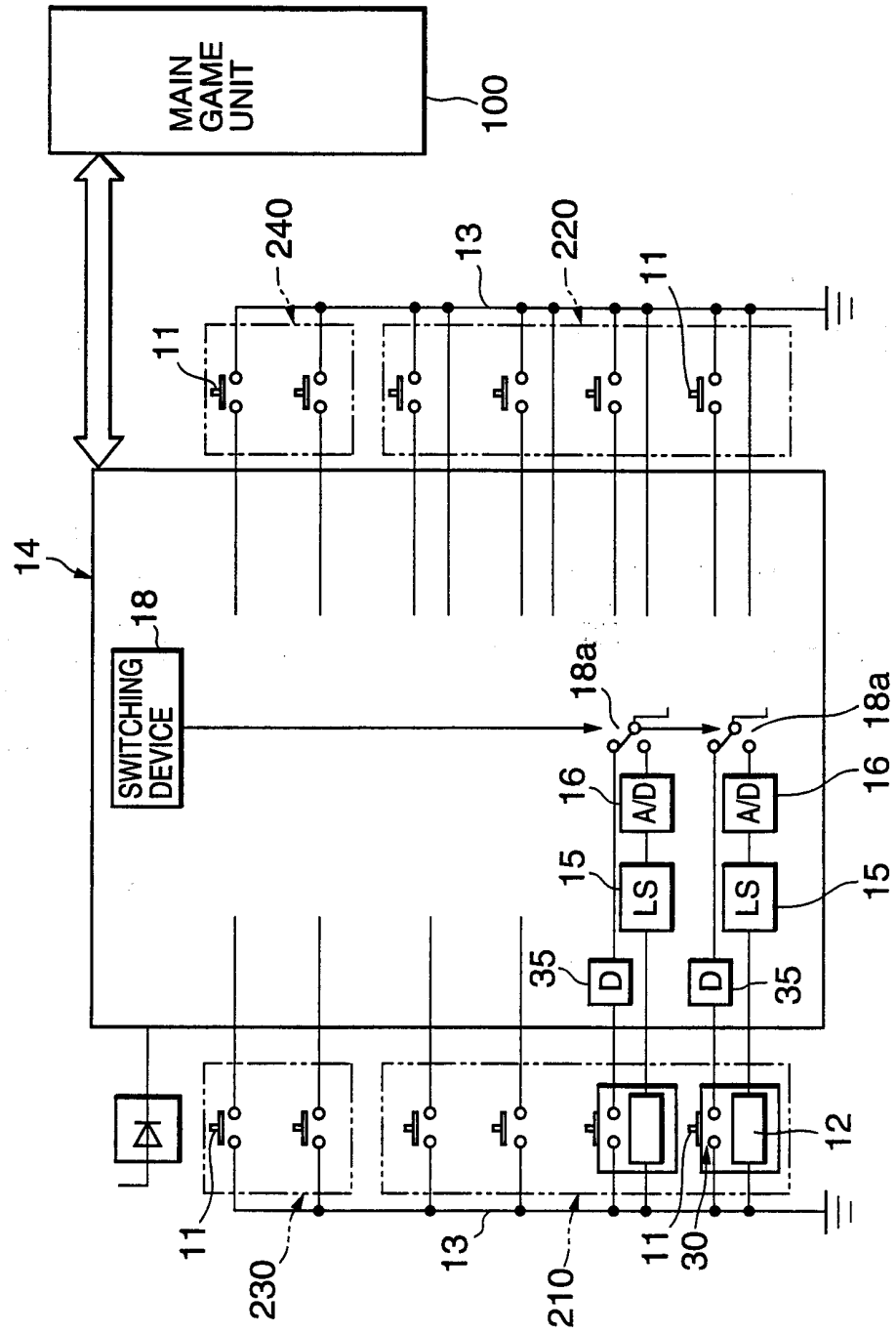


FIG. 27

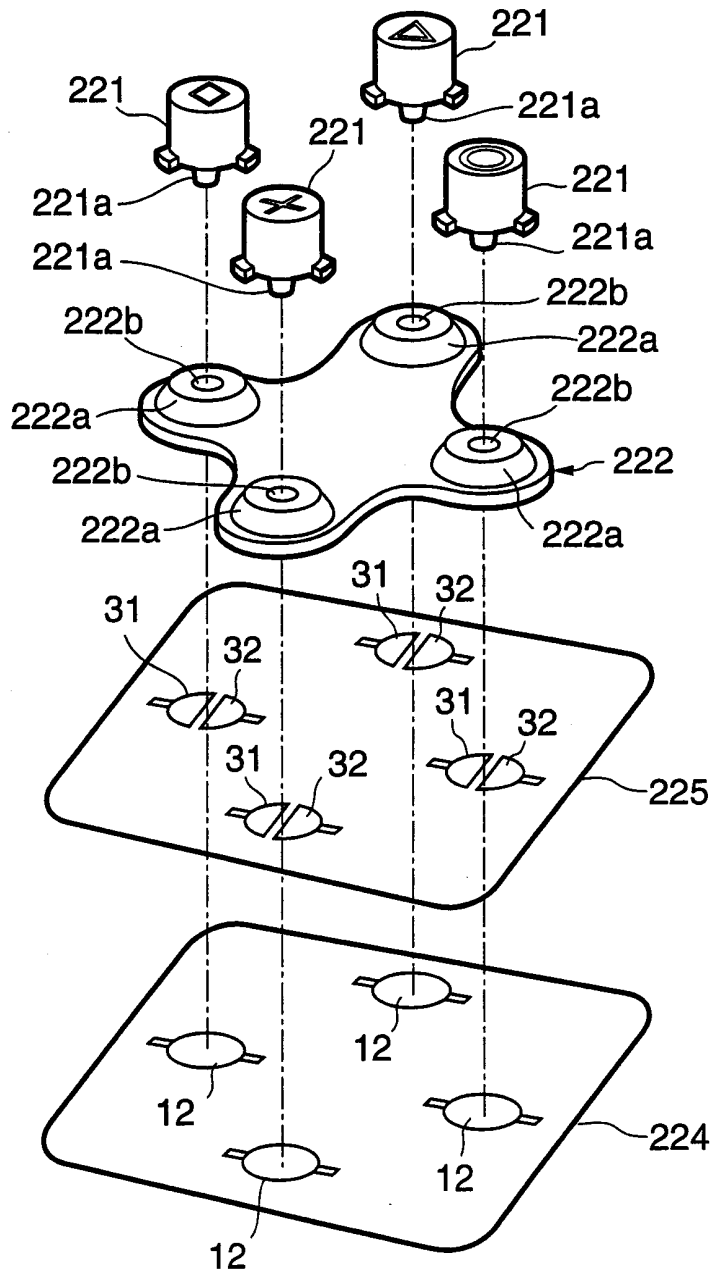


FIG. 28

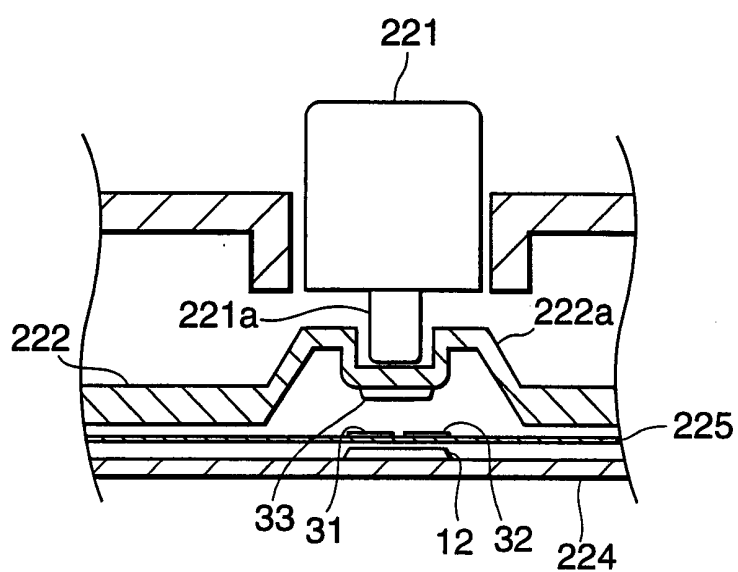


FIG. 29

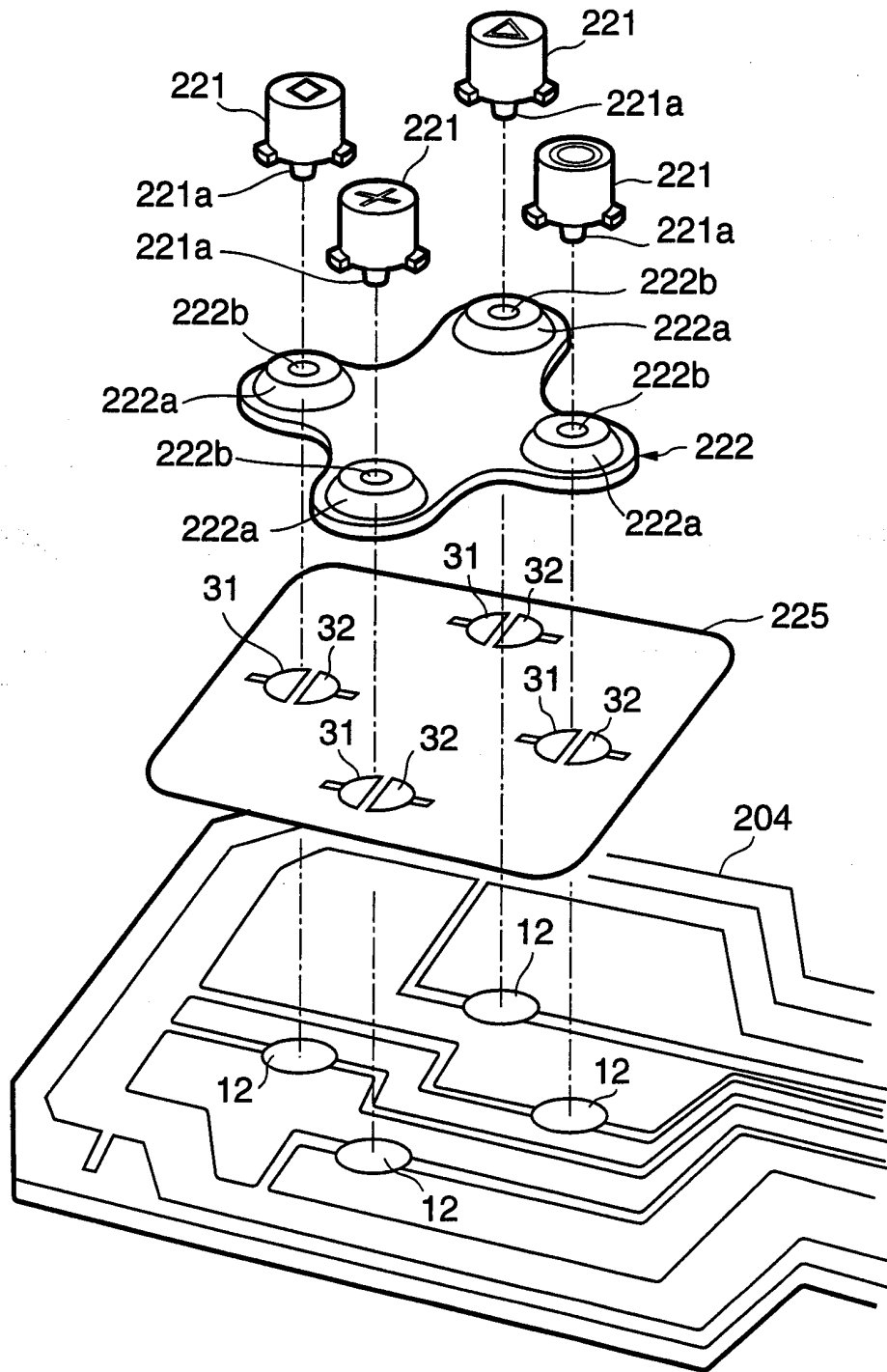


FIG. 30

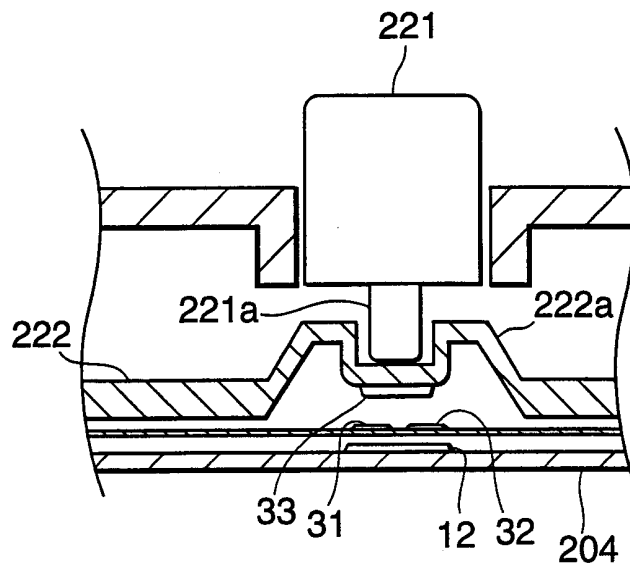


FIG. 31A

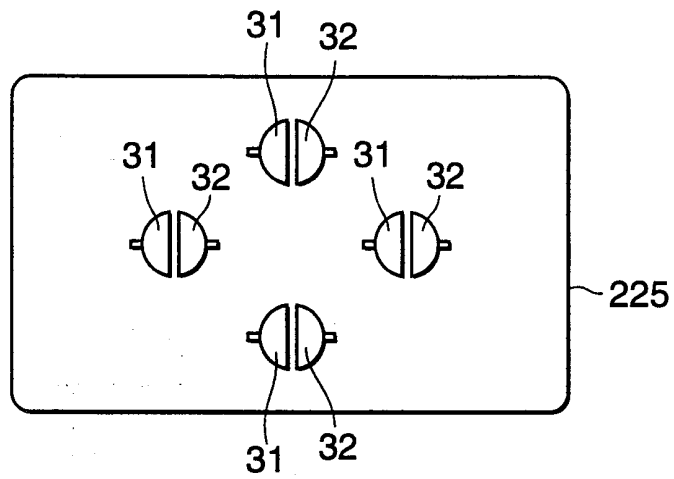


FIG. 31B

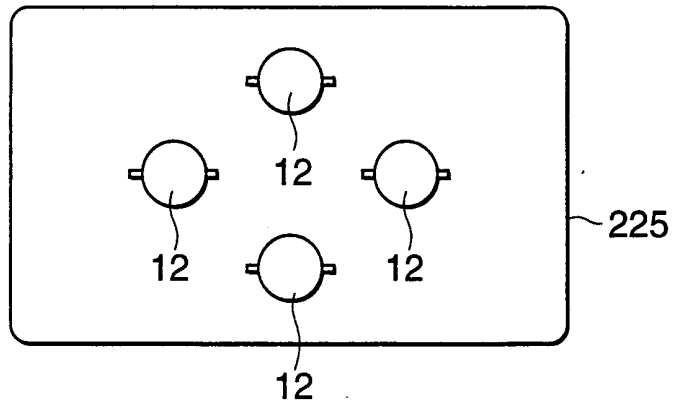


FIG. 32

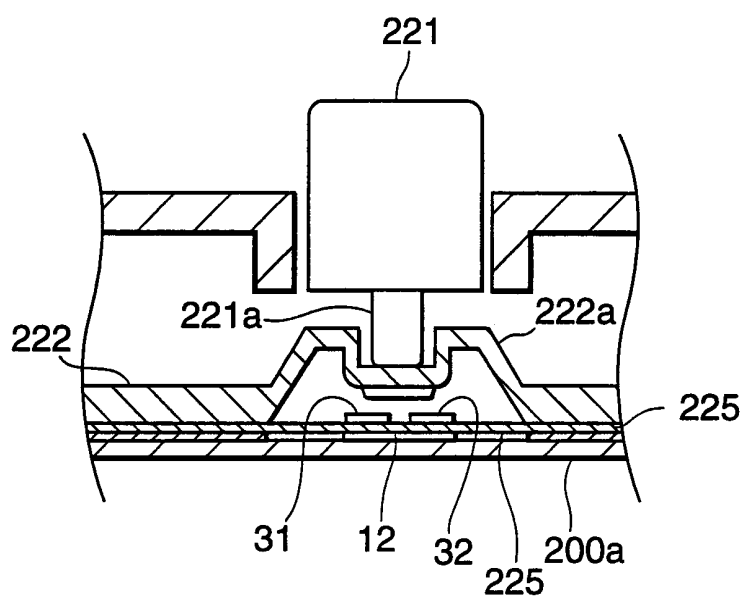


FIG. 33

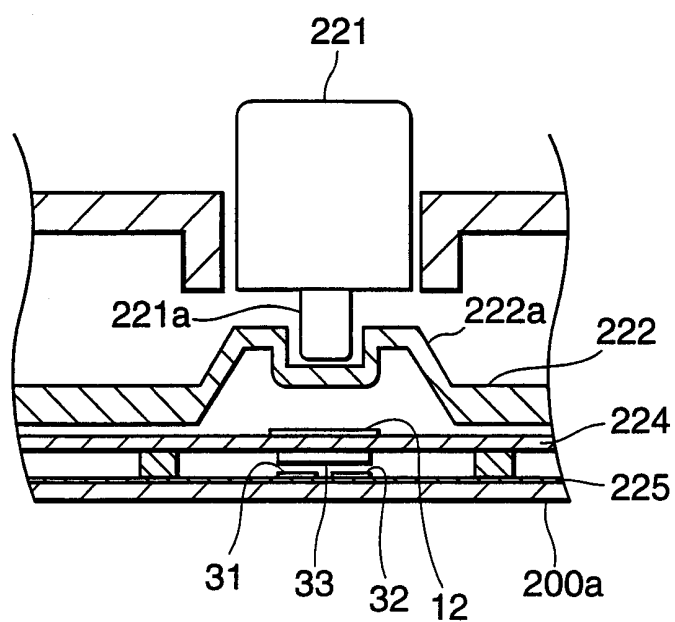


FIG. 34A

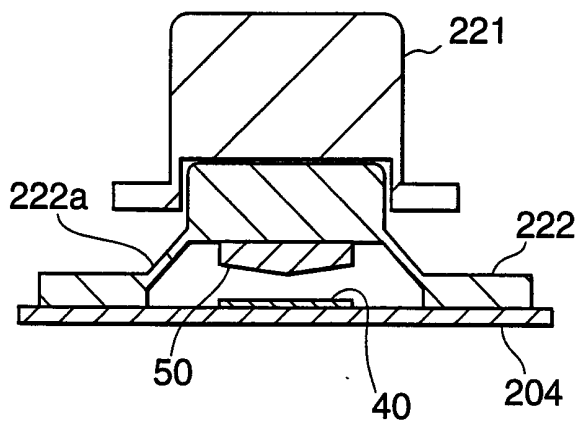


FIG. 34B

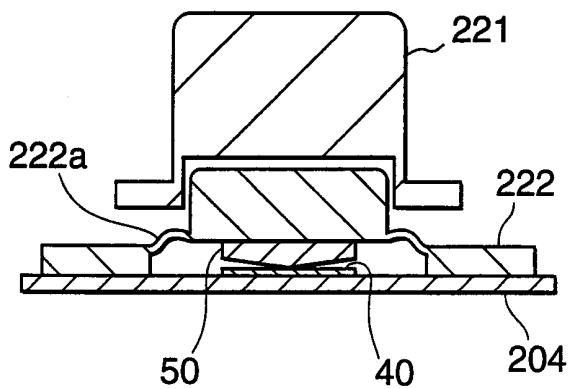


FIG. 34C

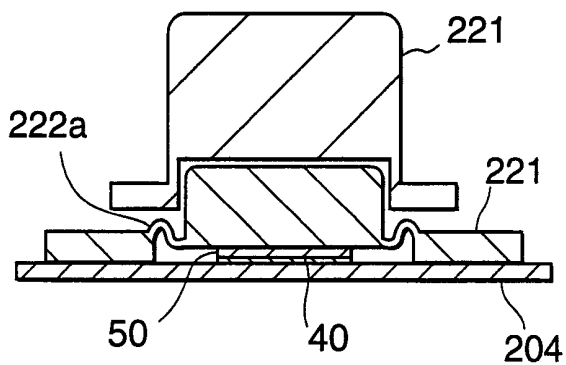


FIG. 35

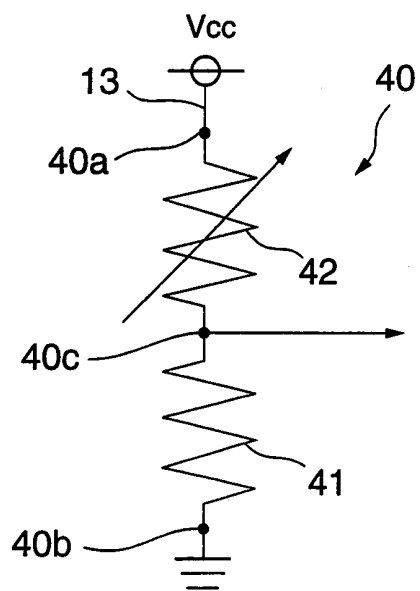


FIG. 36

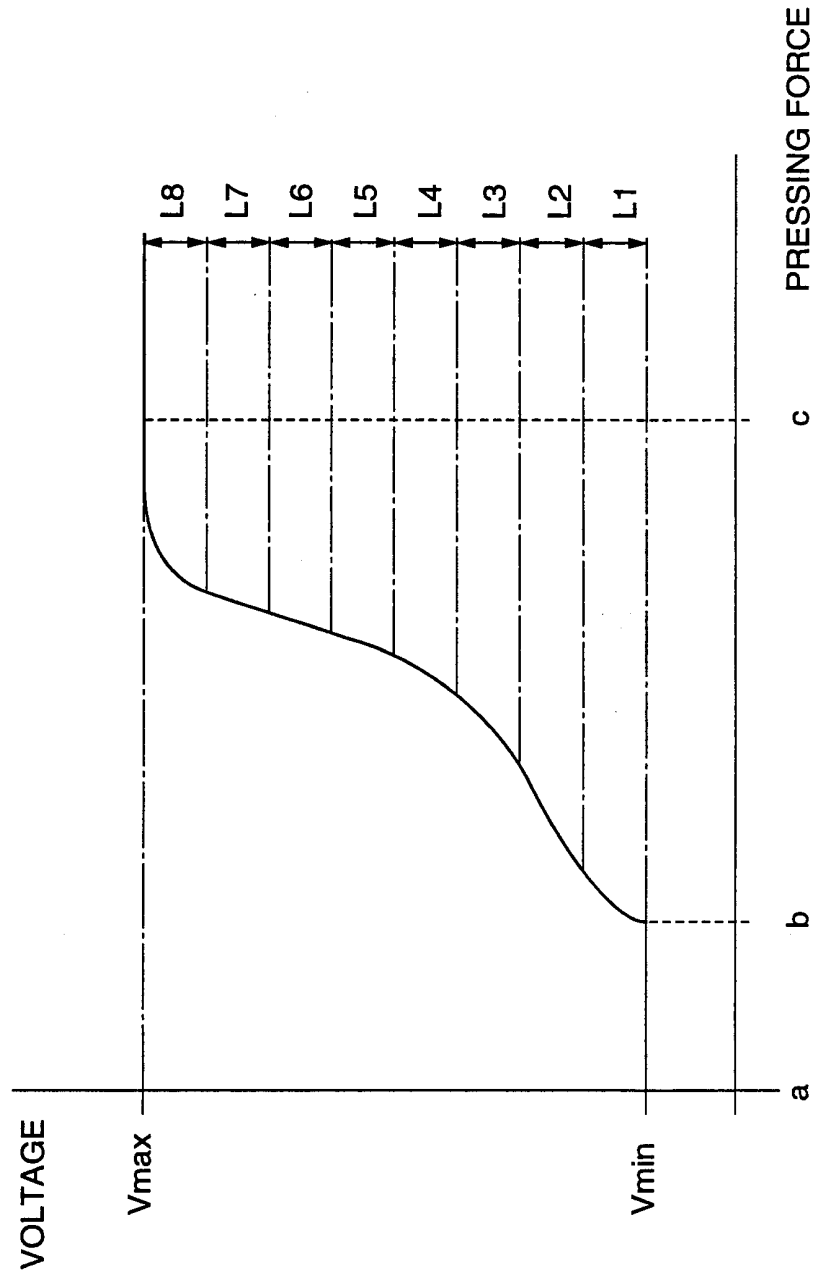


FIG. 37

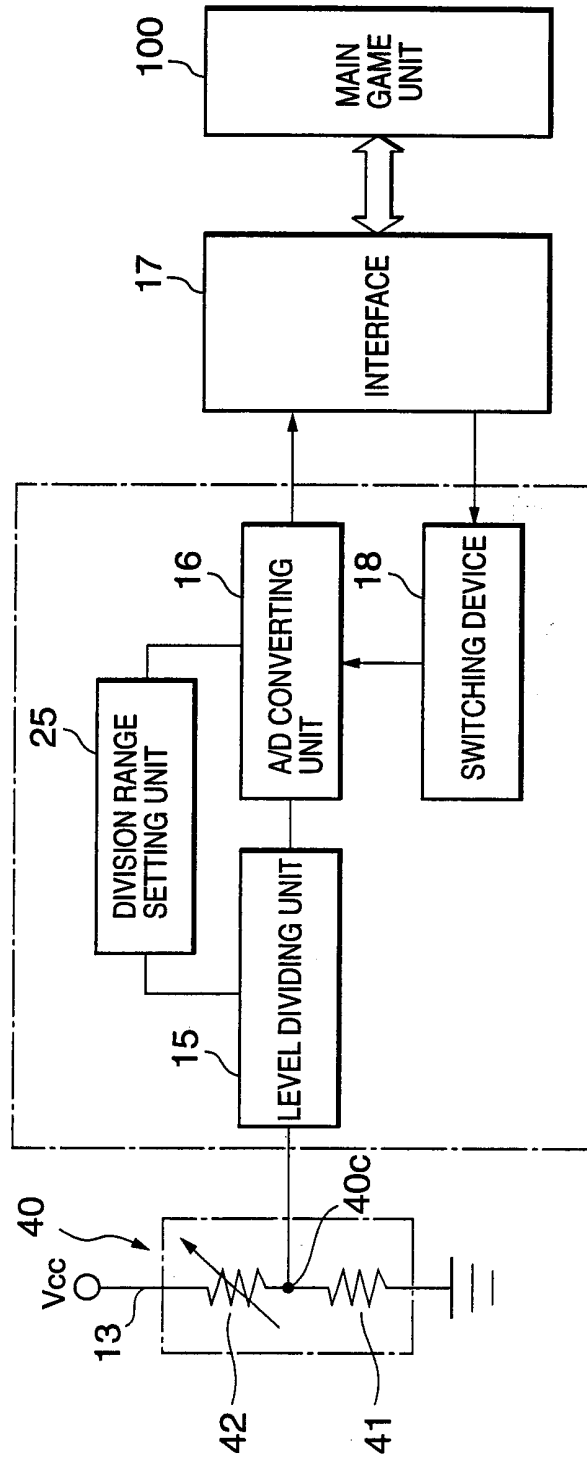


FIG. 38

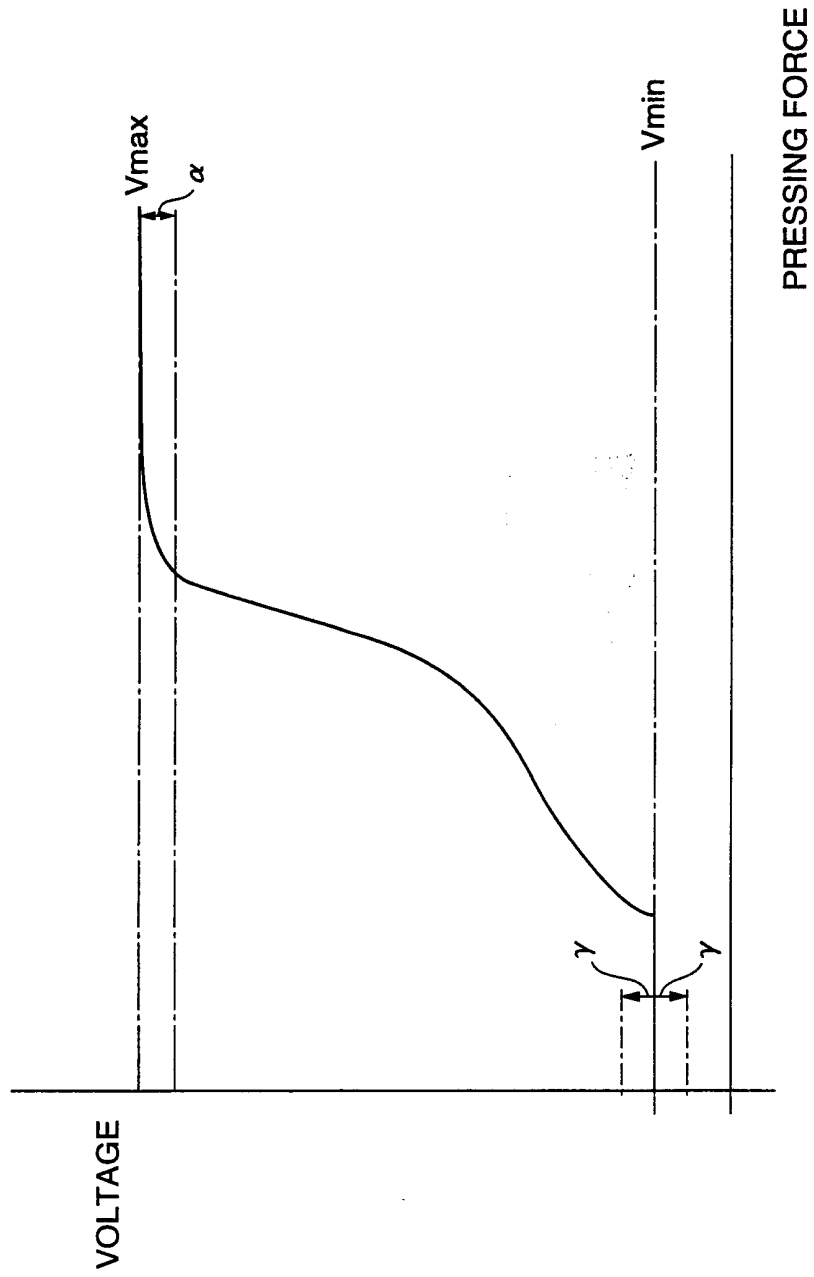


FIG. 39

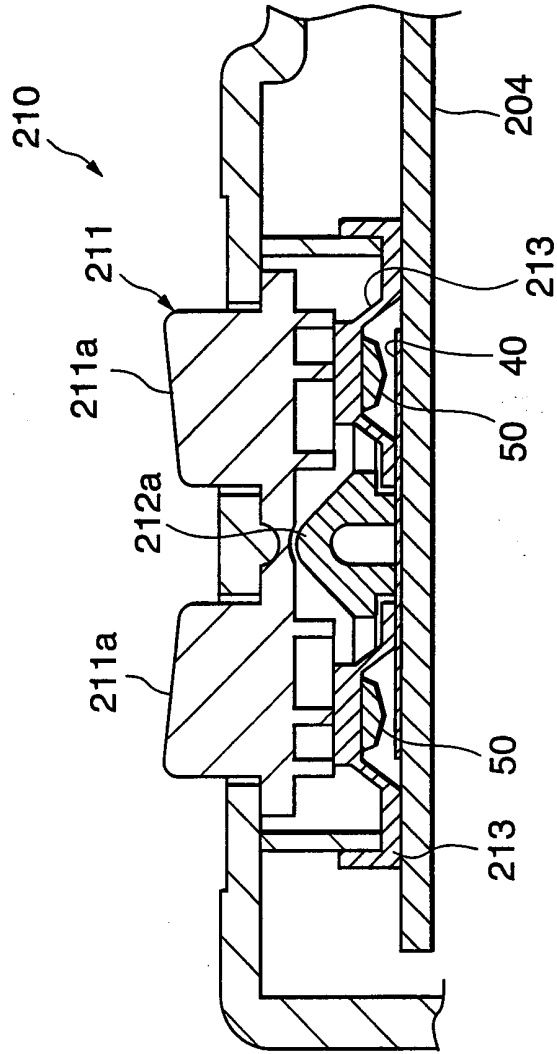


FIG. 40

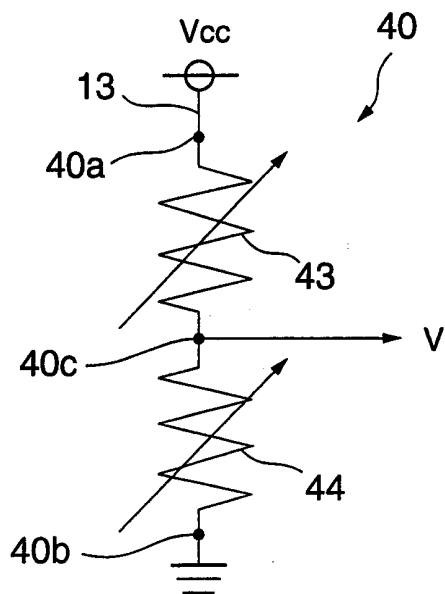


FIG. 41

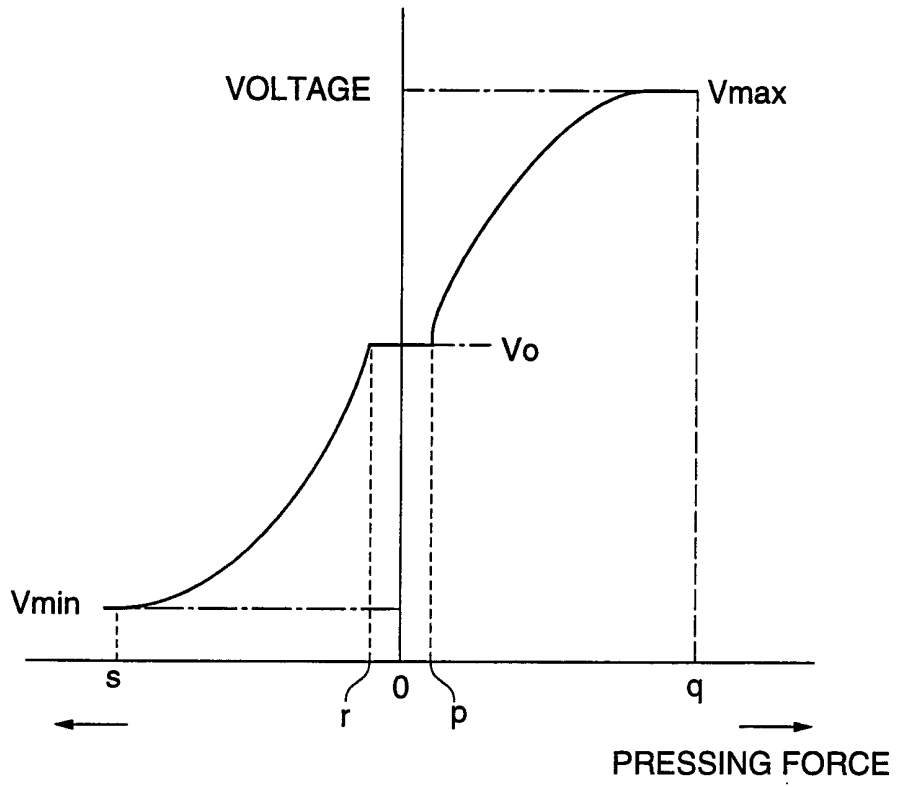


FIG. 42

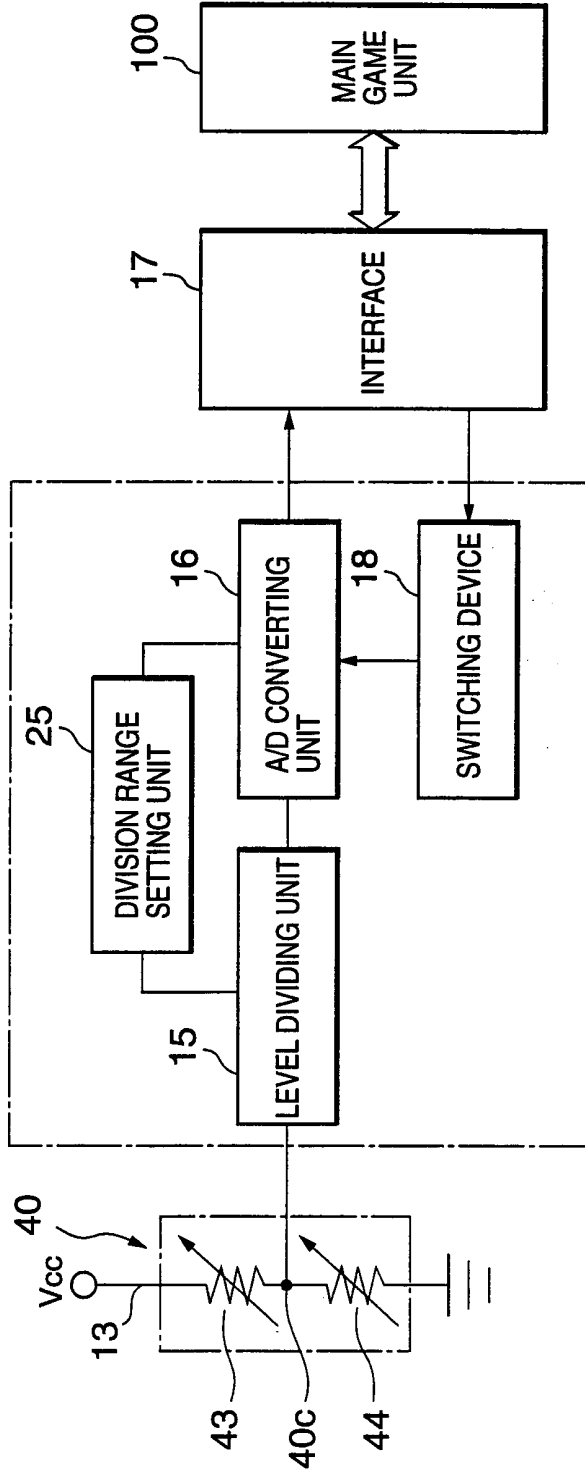


FIG. 43

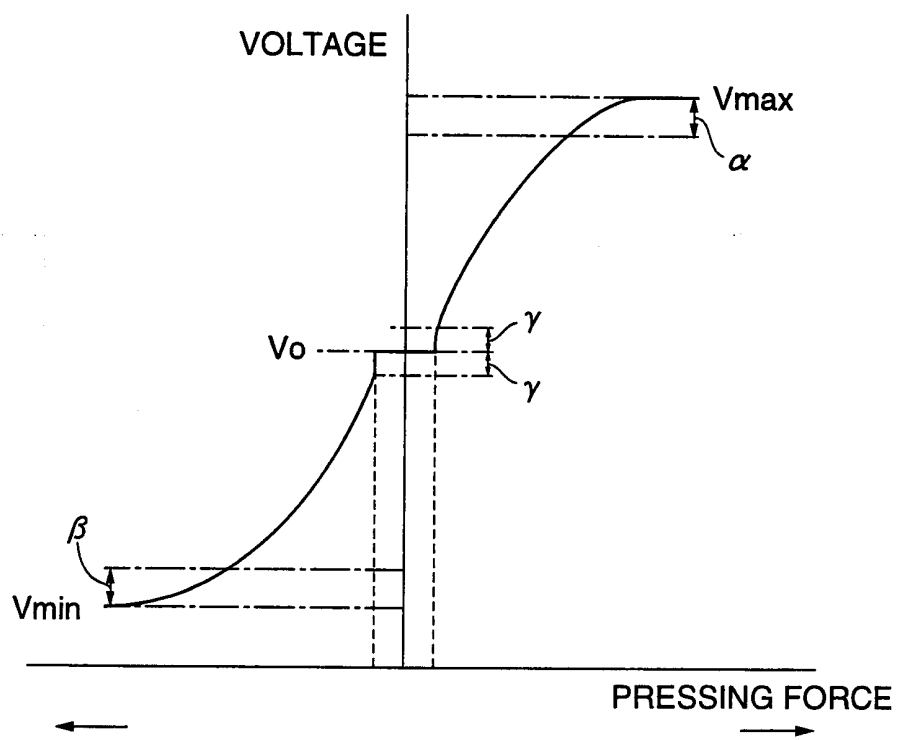


FIG. 44A

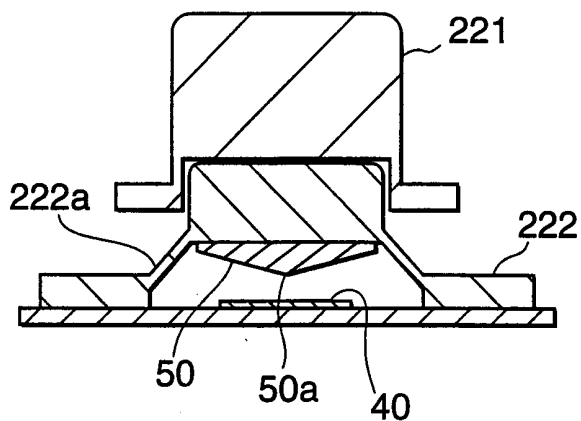


FIG. 44B

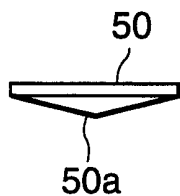


FIG. 44C

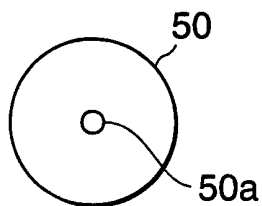


FIG. 44D

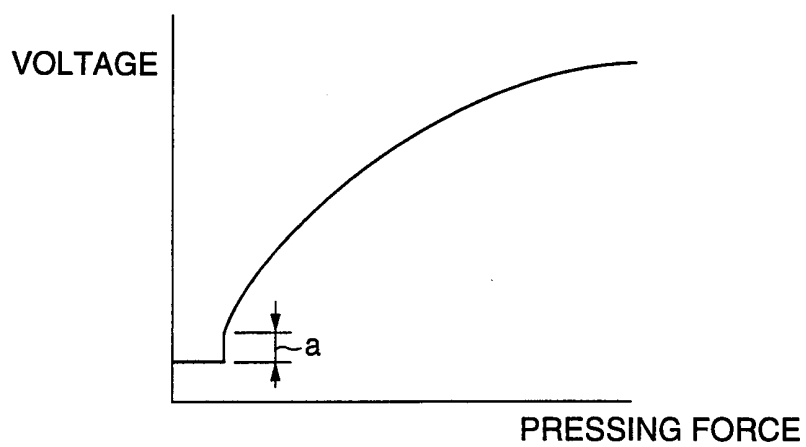


FIG. 45A

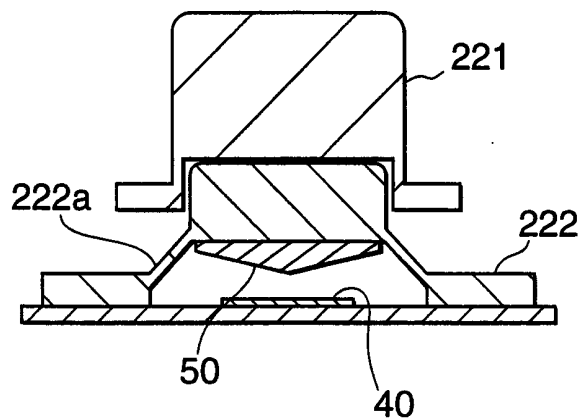


FIG. 45B

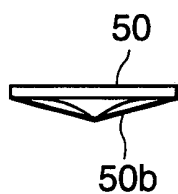


FIG. 45C

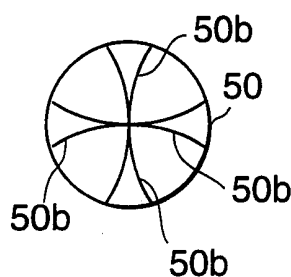


FIG. 45D

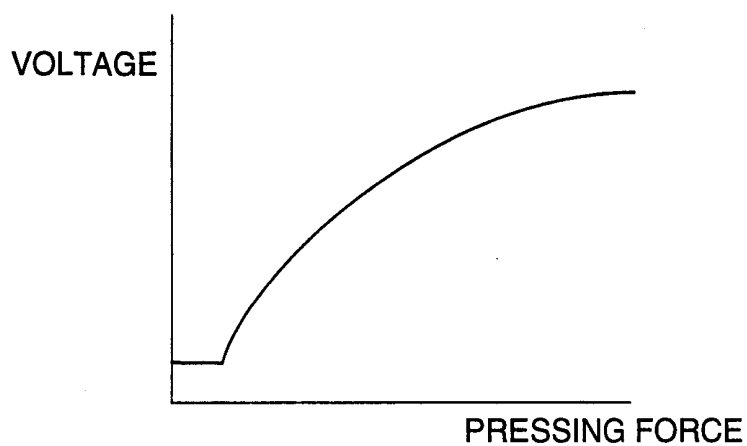


FIG. 46A

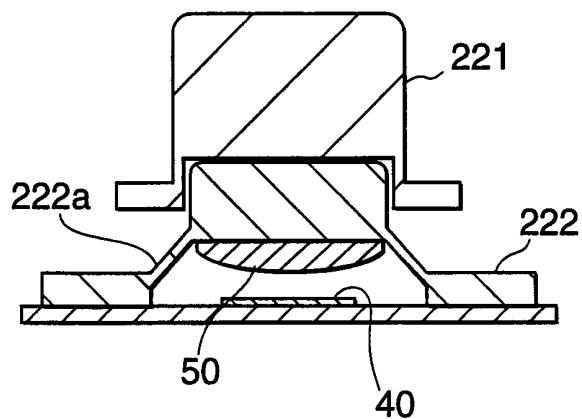


FIG. 46B

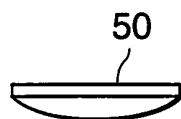


FIG. 46C

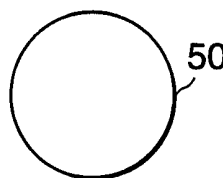


FIG. 46D

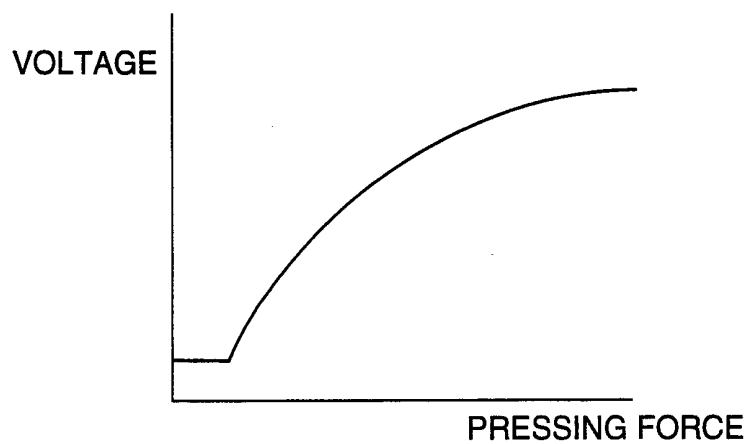


FIG. 47A

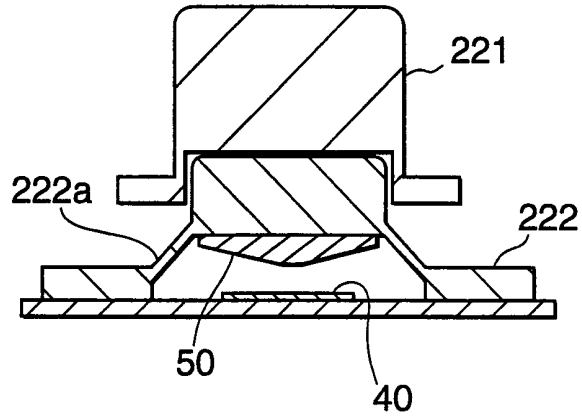


FIG. 47B

FIG. 47C

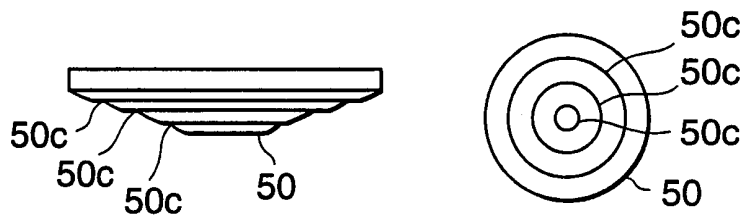


FIG. 47D

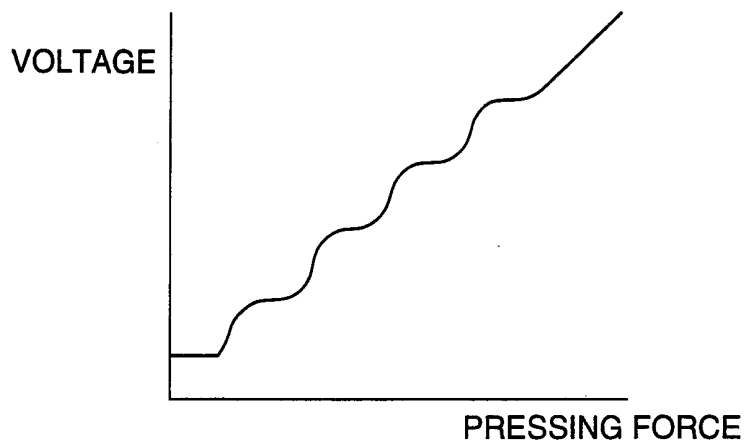


FIG. 48A

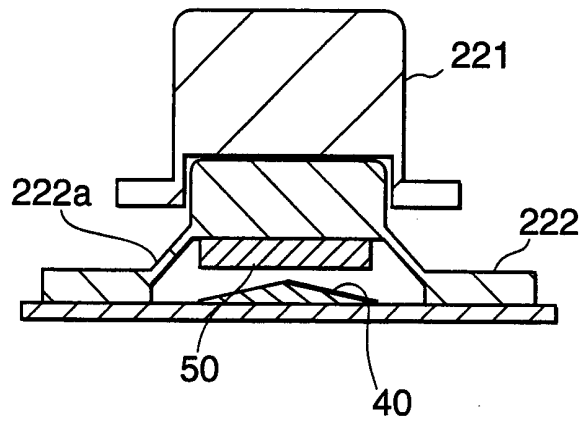


FIG. 48B



FIG. 48C

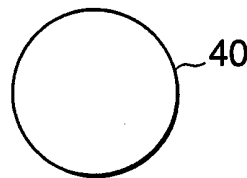


FIG. 48D

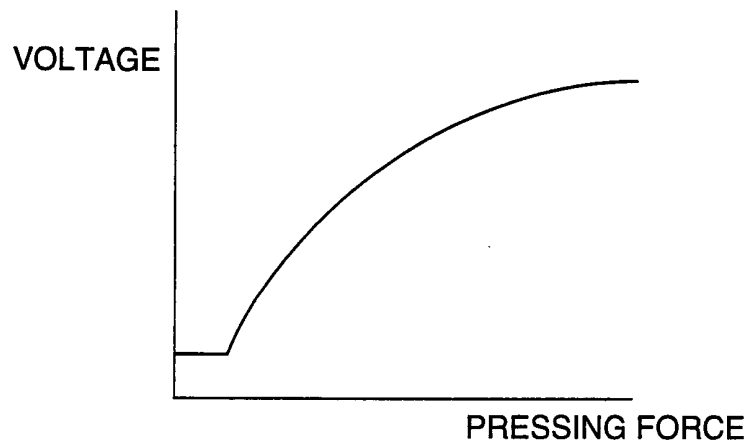


FIG. 49A

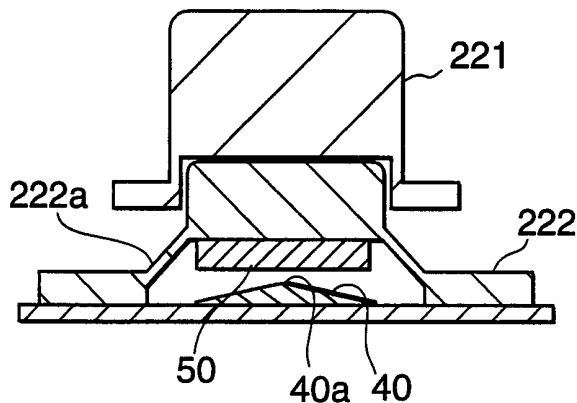


FIG. 49B

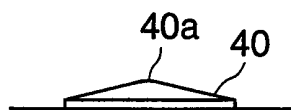


FIG. 49C

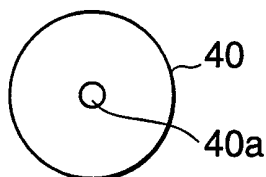


FIG. 49D

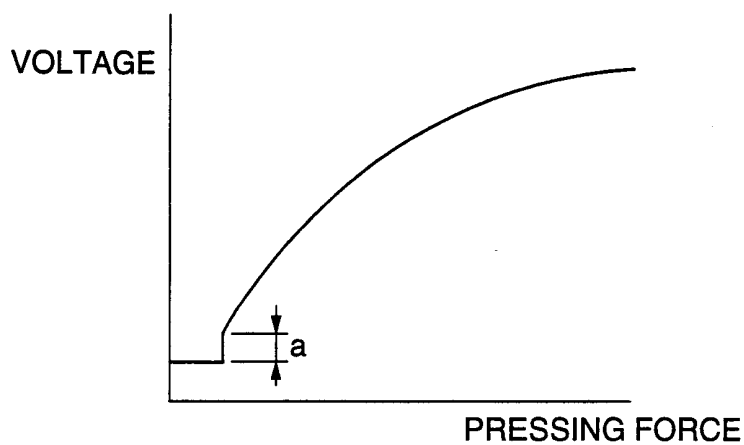


FIG. 50A

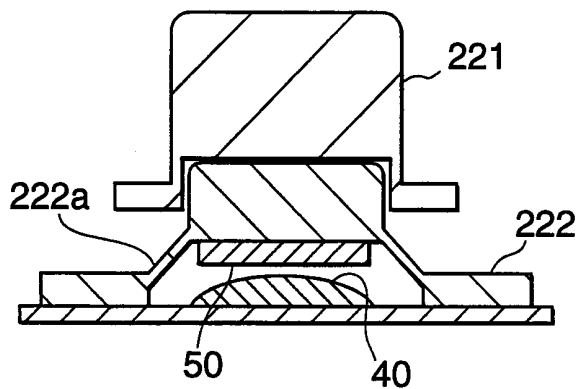


FIG. 50B



FIG. 50C

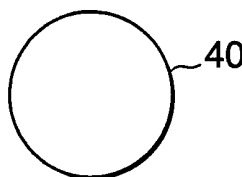


FIG. 50D

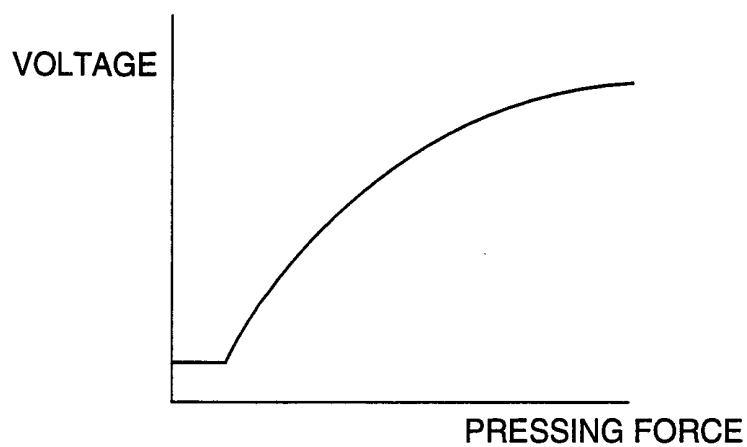


FIG. 51A

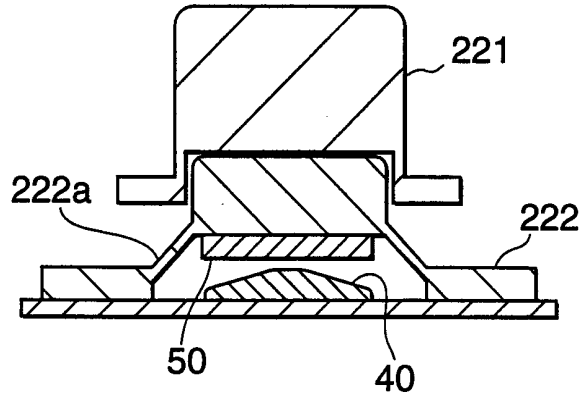


FIG. 51B

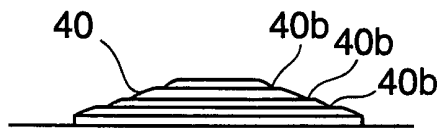


FIG. 51C

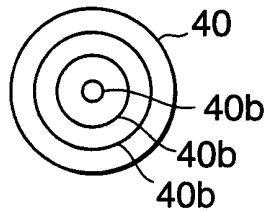


FIG. 51D

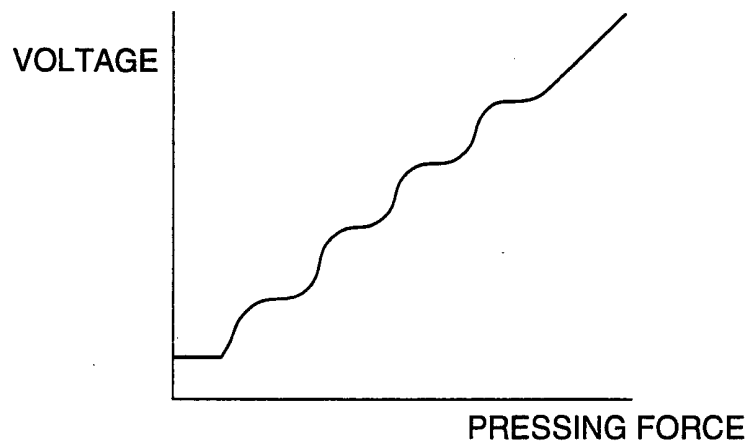


FIG. 52A

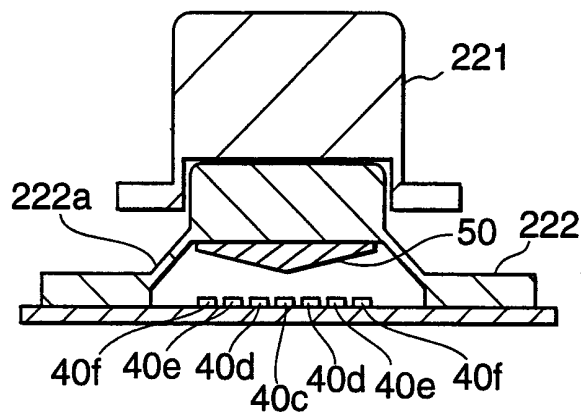


FIG. 52B

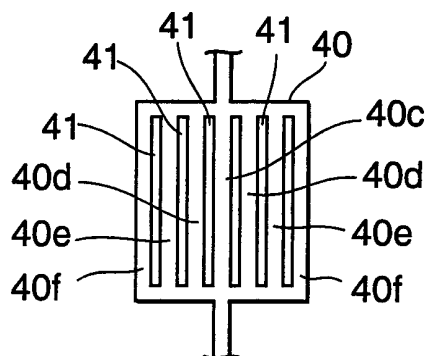


FIG. 52C

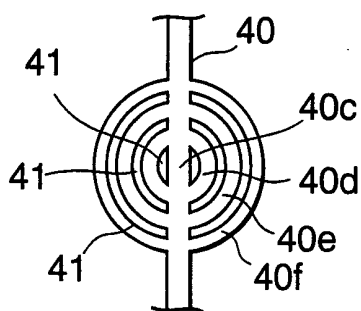


FIG. 52D

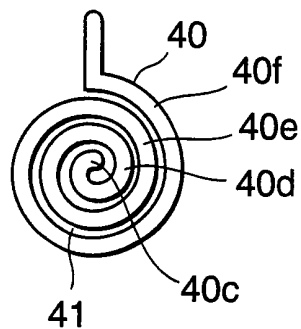
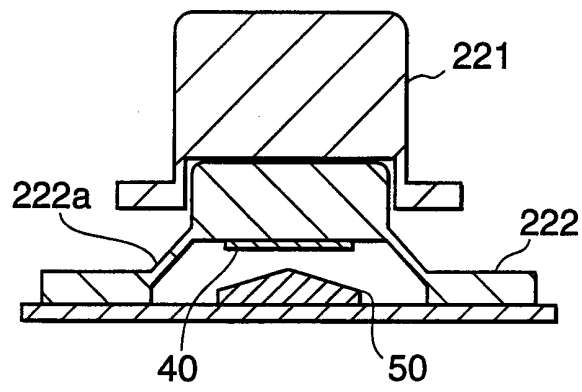


FIG. 53



INTERNATIONAL SEARCH REPORT

International Application No
PCT/JP 00/02625

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 A63F13/06

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 A63F H03M G06F G06K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 049 079 A (FURTADO MATTHEW P ET AL) 17 September 1991 (1991-09-17) column 9, line 58 -column 10, line 24 column 16, line 19 - line 29 ---	1,2,16, 21
A	US 5 714 981 A (CHI WAI WAN PETER ET AL) 3 February 1998 (1998-02-03) column 2, line 42 - line 48 column 12, line 47 - line 61 column 17, line 41 -column 18, line 25 ---	1,16,21
A	PATENT ABSTRACTS OF JAPAN vol. 017, no. 122 (P-1501), 15 March 1993 (1993-03-15) & JP 04 305725 A (NEC CORP), 28 October 1992 (1992-10-28) abstract ---	1,2,16, 21
-/--		

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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Date of the actual completion of the international search

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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Tito Martins, J

INTERNATIONAL SEARCH REPORT

International Application No

PCT/JP 00/02625

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