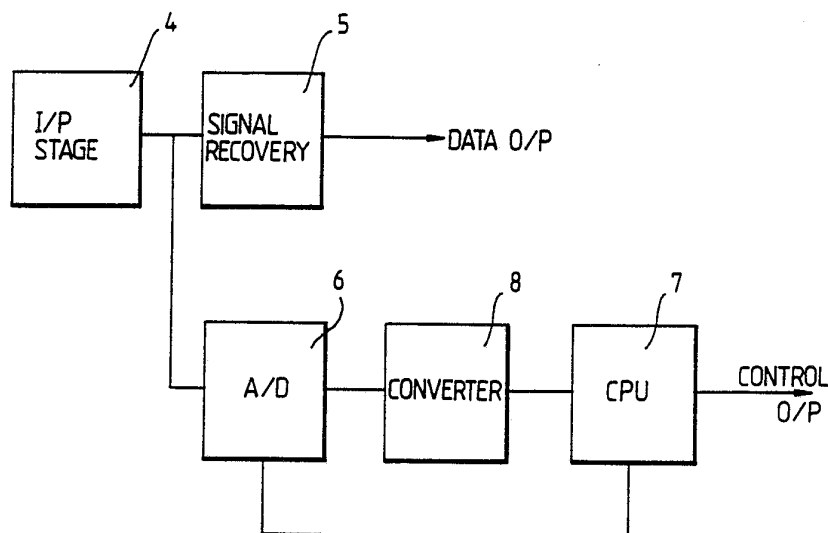




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(21) International Application Number: PCT/GB90/01758 (22) International Filing Date: 15 November 1990 (15.11.90) (30) Priority data: 8926548.2 24 November 1989 (24.11.89) GB (71) Applicant (for all designated States except US): BRITISH TELECOMMUNICATIONS PUBLIC LIMITED COMPANY [GB/GB]; 81 Newgate Street, London EC1A 7AJ (GB). (72) Inventor; and (75) Inventor/Applicant (for US only) : CLARKE, Donald, Eric, Arthur [GB/GB]; 65 Northgate Street, Colchester, Essex CO1 1EY (GB). (74) Agent: SEMOS, Robert, Ernest, Vickers; Intellectual Property Unit, British Telecom, 151 Gower Street, London WC1E 6BA (GB).		(81) Designated States: AT (European patent), BE (European patent), CA, CH (European patent), DE (European patent), DK (European patent), ES (European patent), FI, FR (European patent), GB (European patent), GR (European patent), IT (European patent), JP, LU (European patent), NL (European patent), NO, SE (European patent), US. Published <i>With international search report.</i>

(54) Title: PASSIVE OPTICAL NETWORK**(57) Abstract**

A passive optical network includes a head-end station (1) and a plurality of terminations (2). The terminations (2) are responsive to control signals from the head-end station (1). The head-end station (1) is arranged to receive frames having a predetermined bit rate and comprising signals transmitted by the terminations (2). Ranging pulses are transmitted from each of the terminations (2) to the head-end station (1) where the received ranging pulses are sampled at a high sampling rate greater than the predetermined bit rate. The data samples are applied to a processor (7) which determines the phase and amplitude of the ranging pulses and generates control signals for the terminations (2) to modify the phase or timing and amplitude of the transmissions from the terminations (2). A locally generated marker pulse may be applied to the incoming data frame before it is sampled in the head-end station (1), the marker pulse providing a phase and amplitude reference for use by the control processor.

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PASSIVE OPTICAL NETWORK

The present invention relates to a passive optical network, and in particular to the recovery of information from signals transmitted from terminations to a head-end station in such a network.

The present applicant has developed a bit transport system (BTS) for use in a TPO (telephony on a passive optical network) network. In this BTS a head-end station broadcasts time division multiple access (TDMA) frames to all the terminations on the network. The transmitted frames include both traffic data and control data. Each termination recognises and responds to an appropriately addressed portion of the data in the broadcast frame and ignores the remainder of the frame.

In the upstream direction, each termination transmits data in a predetermined time slot and the data from the different terminations are assembled at the head-end into a TDMA frame of predetermined format.

One feature necessary to such a network is the provision of compensation for the differing delays and attenuation associated with the different distances of the various terminations from the head-end station. To this end, in the BTS each termination is arranged to transmit a ranging pulse timed to arrive in a respective predetermined portion of the upstream TDMA frame. The head-end station is arranged to monitor the timing, i.e. phase, and amplitude of the arrival of the pulse from each of the terminations and to return a servo-control signal to the termination to retard or advance its transmissions as appropriate and to adjust the launch power. This active fine ranging enables the BTS to ensure the stability of the upstream TDMA frame and, for example, to compensate for fluctuations in timing and received power due to such effects as changes in the

operating temperature of the network. However this places severe demands on the design of the head-end station, requiring the measurement of the timing of received signals to within a fraction of a clock cycle and amplitude to within a fraction of a dB in real time.

According to a first aspect of the present invention, there is provided a method of operating a passive optical network including a head-end station and a plurality of terminations, the terminations being responsive to control signals from the head-end station and the head-end station being arranged to receive frames having a predetermined bit rate and comprising signals transmitted by the plurality of terminations, the method including transmitting respective ranging pulses from the terminations to the head-end station, sampling the received ranging pulses at a high sampling rate greater than the said predetermined bit rate and producing corresponding data samples, applying the data samples to processor means arranged to determine the respective phases of the ranging pulses relative to respective expected values and to determine the respective amplitudes of the ranging pulses, and generating respective control signals for the terminations to modify the phase and amplitude of transmissions from the terminations accordingly.

The present invention by sampling the received ranging pulses at a high sampling rate makes it possible to capture the phase and amplitude information from a ranging pulse in a form which can be readily processed to provide the necessary control signals. At the same time, the sampling process effectively provides a snap-shot of the received pulse, which carries information relating to the overall shape of the pulse. This further information can be used to implement other control and diagnostic procedures simply by the provision of appropriate additional software for the processor.

There is residual phase jitter in the TDMA data received at the head-end due to varying path lengths and clock jitter in the terminations. With the method of the present invention, this jitter may be used effectively to increase the phase resolution by providing additional sampling points on the sloped edges of the received ranging pulses. The waveform data is then integrated and processed by a CPU over a period of time.

Preferably the method further comprises writing the data samples into a plurality of latches connected to a clock signal source arranged to clock the latches at a low clock rate less than the high sampling rate, the clock signal for each latch having a different respective phase offset, writing data samples from each latch into a respective serial memory, and reading data samples from the serial memories into the processor means.

Although a high sampling rate is necessary initially to capture all the required information from the ranging pulse, it is advantageous to convert the data samples to a lower bit rate, and in particular to the bit rate of the frames before it is accessed by the processor.

Preferably the head-end station includes a masterclock operating at one of the said low clock and the said high sampling rate and means to derive from the masterclock a clock signal at the other of the said low clock rate and the said high sampling rate and the plurality of latches are latched on the leading edge of a pulse of the clock signal at the low clock rate and the serial memories are latched on the trailing edge of the said pulse.

According to a further aspect of the present invention, there is provided a method of operating a passive optical network including a head-end station and a plurality of terminations, the terminations being responsive to control signals from the head-end station and the head-end station being arranged to receive frames comprising signals transmitted by the plurality of

terminations, the method including superimposing on the received frames at the head-end station a locally generated marker pulse having a phase locked to a masterclock for the system, subsequently sampling a portion of the frame including the marker pulse to produce corresponding data samples, and applying the data samples to a control processor having its own local clock, the control processor being arranged to determine characteristics of received signals in the sampled portion of the frame relative to those of the marker pulse.

The further aspect of the present invention is preferably used in combination with the first aspect to analyse ranging pulses. It is not however limited to use in this manner and may, for example, be used to derive information from portions of the data frame other than the ranging pulses.

According to a still further aspect of the present invention, there is provided a head-end station for use in a passive optical network system and arranged to transmit control signals to a plurality of terminations, the terminations being responsive to control signals from the head-end station, and the head-end station being arranged to receive frames having a predetermined bit rate and comprising signals transmitted by the plurality of terminations, the head-end station including means to receive respective ranging pulses from the terminations, sampling means arranged to sample the received ranging pulses at a high sampling rate greater than the said predetermined bit rate and produce corresponding data samples, processor means responsive to data samples and arranged to determine the respective phase of the ranging pulses relative to respective expected values and to determine the respective amplitudes of the ranging pulses, and control signal generating means arranged to generate respective control signals to modify the phase and amplitude of transmissions from respective terminations.

Preferably the head-end station further comprises a plurality of latches connected to a clock signal source arranged to clock the latches at a low clock rate less than the said high sampling rate, the clock signal for each latch having a different respective phase offset, and a respective serial memory for each of the plurality of latches, data samples being written in use from each latch into the respective serial memory, and each serial memory being reset by a reset pulse having a corresponding respective phase offset, thereby converting the data samples from the high sampling rate to the said low clock rate.

Preferably the serial memories comprise dual port serial FIFOs.

Preferably the head-end station further includes marker means disposed upstream of the sampling means and arranged to superimpose a marker pulse of predetermined phase and amplitude on the received frames and thereby provide a reference for use by the processor means.

A passive optical network in accordance with the present invention will now be described in detail with reference to the accompanying drawings in which:-

Figure 1 is a block diagram showing a TPON network;

Figure 2 is a diagram showing the structure of an upstream TDMA frame;

Figure 3 is a block diagram of a head-end station;

Figure 4 is a block diagram showing the input stage in greater detail;

Figure 5 is a block diagram showing the converter of Figure 3 in greater detail; and

Figure 6 is a diagram showing the sampling of the ranging pulse.

A TPON network comprises a head-end station 1, a number of terminations 2, and a passive optical fibre network 3 linking the head-end station 1 to the terminations 2. Although, for clarity, only three terminations are shown, in practice many more terminations

will be connected to a single head-end station. Typically the head-end station is located in a local exchange and the terminations 2 are subscriber stations in domestic or commercial premises or street cabinets in the neighbourhood of the local exchange.

The head-end station 1 broadcasts data over the fibre network 3 as time division multiple access (TDMA) frames having a predetermined format. The frames include control channels addressed to specific ones of the terminations 2 to control, amongst other parameters, the amplitude and phase (timing) of the optical signals transmitted onto the fibre network 3 by the terminations 2.

In the upstream direction, each termination 2 transmits data in a predetermined time slot, which data is assembled into a TDMA frame at the head-end station 1. Since the TPON network is synchronous in operation it is necessary to control the timing of the terminations both to compensate for the different delays associated with different positions of the terminations on the fibre network 3 and to correct for any variation in the delay and amplitude arising, for example, from local fluctuations in the temperature of the network.

Figure 2 shows the format of the return frame. Traffic data is transmitted to the head-end station in 80 basic frames BF1 BF80. The basic frames BF1 BF80 are preceded by a header H which includes a phase-2 ranging section R 720 bits long. Each termination 2 is arranged to transmit onto the fibre network 3 a ranging pulse timed to arrive at a respective predetermined position within the ranging section R. Using the circuit described in further detail below the head-end station 1 determines the phase of each arriving ranging pulse relative to the respective predetermined positions of the ranging pulses within the ranging section R, and then transmits control signals to the respective termination 2 to retard or advance the timing of the transmission from

that termination 2 in order to minimize the phase offset between the received data from that termination and the intended position of that data within the return frame structure.

Figure 3 shows the structure of the head-end station 1. The incoming optical signals are received by an input stage 4 which includes a conventional photo-electric detector followed by a linear gain block A. The output from the input stage 4 is branched, going both to a signal recovery stage 5 and to an A/D sampler 6. The sampler 6 operates under the control of a CPU 7 to sample any selected portion of the incoming signals. In the present example, the sampler 6 is arranged to sample the portion R of the incoming frame containing the ranging pulses and produce corresponding samples in the form of 8-bit data words (the data). The output from the sampler 6 is taken to a converter 8 which converts the data to the lower clock rate of a masterclock for the head-end station. The CPU 7 calculates from the data the phase and amplitude of the ranging pulses. These parameters are compared with expected values and, where necessary, control signals generated for a corresponding termination 2 to retard or advance the timing of transmissions from the termination 2 to minimize the phase offset, and/or to increase or reduce the amplitude of the transmissions until the received ranging pulse from that termination 2 has an amplitude falling within predetermined limits.

Figure 5 shows the converter in greater detail. The high-speed sampler 6 provides an input to the converter 8 comprising 8-bit data words having a clock rate, in the present example, of 81.92 MHz. The input to the converter 8 is fed in parallel to four 8-bit latches L. The latches L are locked to the masterclock with differing phase offsets of 0,90,180, and 270 degrees respectively. The output from each latch is fed to a 8-bit dual port serial FIFO of the type known as a video

line memory VLM. In order to ensure that the data written to the video line memories is correctly ordered the memories are reset by pulses having a predetermined phase with respect to the write clock. The write clock and reset pulses with appropriate phases are derived from respective shift registers, as shown in Figure 5. The video line memories VLM are polled consecutively by the CPU 7 so that the data received at the converter 8 as four 8-bit data words with a clock rate of substantially 80MHz are read into the CPU as a single 32-bit word at a lower clock rate of substantially 20MHz. A write gate signal having the same periodicity as the received TDMA frames is applied to the write enable inputs WE of the video line memories. The write gate is used to select the portion of the TDMA frame which is written into the video line memories.

The head-end station 4 includes a masterclock operating at the lower clock rate and the components of the head-end station, together with the rest of the TPON system are locked to the masterclock. In an alternative arrangement, the head-end station includes a clock operating at the high sampling rate of substantially 80MHz and the 20MHz system clock is derived from the 80MHz clock. However although the control systems provided for the head-end station and the network ensure that operation is synchronous at the system clock rate it is not possible to provide similarly synchronous operation at the higher clock rate used in the high-speed sampler. As shown in Figure 6 this rate is four times the masterclock rate in order to capture the necessary phase and amplitude information from the ranging pulse. In order to provide the phase reference required at the higher sampling rate a marker pulse is superimposed on the incoming data upstream of the high-speed sampler 6.

As shown in Figure 4 the input stage includes a marker pulse generator 9, a photodetector 11 and a summing amplifier 10. The marker pulse generator 9

produces a marker pulse having a phase locked to the masterclock. The marker pulse is locally summed with the analogue input signal at the summing amplifier 10 and so appears in the data output from the sampler 6. The CPU 7 then detects the phase of a given sampled ranging pulse with respect to the marker pulse and compares this with the expected value to determine the appropriate phase control signal. Similarly, the marker pulse provides an amplitude reference for use by the CPU 7. The CPU 7 includes its own asynchronous clock. The execution of the CPU program is synchronised using 10mS interrupts (the TDMA frame rate) and data is read from the VLM's at a rate much lower than the system clock, controlled by the interrupts.

CLAIMS

1. A method of operating a passive optical network including a head-end station and a plurality of terminations, the terminations being responsive to control signals from the head-end station and the head-end station being arranged to receive frames having a predetermined bit rate and comprising signals transmitted by the plurality of terminations, the method including transmitting respective ranging pulses from the terminations to the head-end station, sampling the received ranging pulses at a high sampling rate greater than the said predetermined bit rate and producing corresponding data samples, applying the data samples to processor means arranged to determine the respective phases of the ranging pulses relative to respective expected values and to determine the respective amplitudes of the ranging pulses, and generating respective control signals for the terminations to modify the phase and amplitude of transmissions from the terminations accordingly.
2. A method according to claim 1, further comprising writing the data samples into a plurality of latches connected to a clock signal source arranged to clock the latches at a low clock rate less than the high sampling rate, the clock signal for each latch having a different respective phase offset, writing data samples from each latch into a respective serial memory, and reading data samples from the serial memories into the processor means.
3. A method according to claim 2, in which the low clock rate is substantially the same as the bit rate of the received data frames.
4. A method according to claim 2 or 3, in which the head-end station includes a masterclock operating at one of the said low clock rate and the said high sampling rate and means to derive from the masterclock a clock signal at the other of the said low clock rate and the

said high sampling rate, and the plurality of latches are latched on the leading edge of a pulse of the clock signal at the low clock rate and the serial memories are latched on the trailing edge of the said pulse.

5. A method of operating a passive optical network including a head-end station and a plurality of terminations, the terminations being responsive to control signals from the head-end station and the head-end station being arranged to receive frames comprising signals transmitted by the plurality of terminations, the method including superimposing on the received frames at the head-end station a locally generated marker pulse having a phase locked to a masterclock for the system, subsequently sampling a portion of the frame including the marker pulse to produce corresponding data samples, and applying the data samples to a control processor having its own local clock, the control processor being arranged to determine characteristics of received signals in the sampled portion of the frame relative to those of the marker pulse.

6. A head-end station for use in a passive optical network system and arranged to transmit control signals to a plurality of terminations, the terminations being responsive to control signals from the head-end station, and the head-end station being arranged to receive frames having a predetermined bit rate and comprising signals transmitted by the plurality of terminations, the head-end station including means to receive respective ranging pulses from the terminations, sampling means arranged to sample the received ranging pulses at a high sampling rate greater than the said predetermined bit rate and produce corresponding data samples, processor means responsive to said data samples and arranged to determine the respective phase of the ranging pulses relative to respective expected values and to determine the respective amplitudes of the ranging pulses, and

control signal generating means arranged to generate respective control signals to modify the phase and amplitude of transmissions from respective terminations.

7. A head-end station according to claim 6, further comprising a plurality of latches connected to a clock signal source arranged to clock the latches at a low clock rate less than the said high sampling rate, the clock signal for each latch having a different respective phase offset, and a respective serial memory for each of the plurality of latches, data samples being written in use from each latch into the respective serial memory, and each serial memory being reset by a reset pulse having a corresponding respective phase offset, thereby converting the data samples from the high sampling rate to the said low clock rate.

8. A head-end station according to claim 6 or 7, in which the head-end station includes a masterclock operating at one of the said low clock rate and the said high sampling rate and means to derive from the masterclock a clock signal at the other of the said low clock rate and the said high sampling rate, and the plurality of latches are latched on the leading edge of the pulse of the clock signal at the low clock rate and the serial memories are latched on the trailing edge of the said pulse.

9. A head-end station according to claim 7, in which the serial memories comprise dual port serial FIFOs.

10. A head-end station according to any one of claims 6 to 9, further including marker means disposed upstream of the sampling means and arranged to superimpose a marker pulse of predetermined phase and amplitude on the received frames and thereby provide a reference for use by the processor means.

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Fig.1.

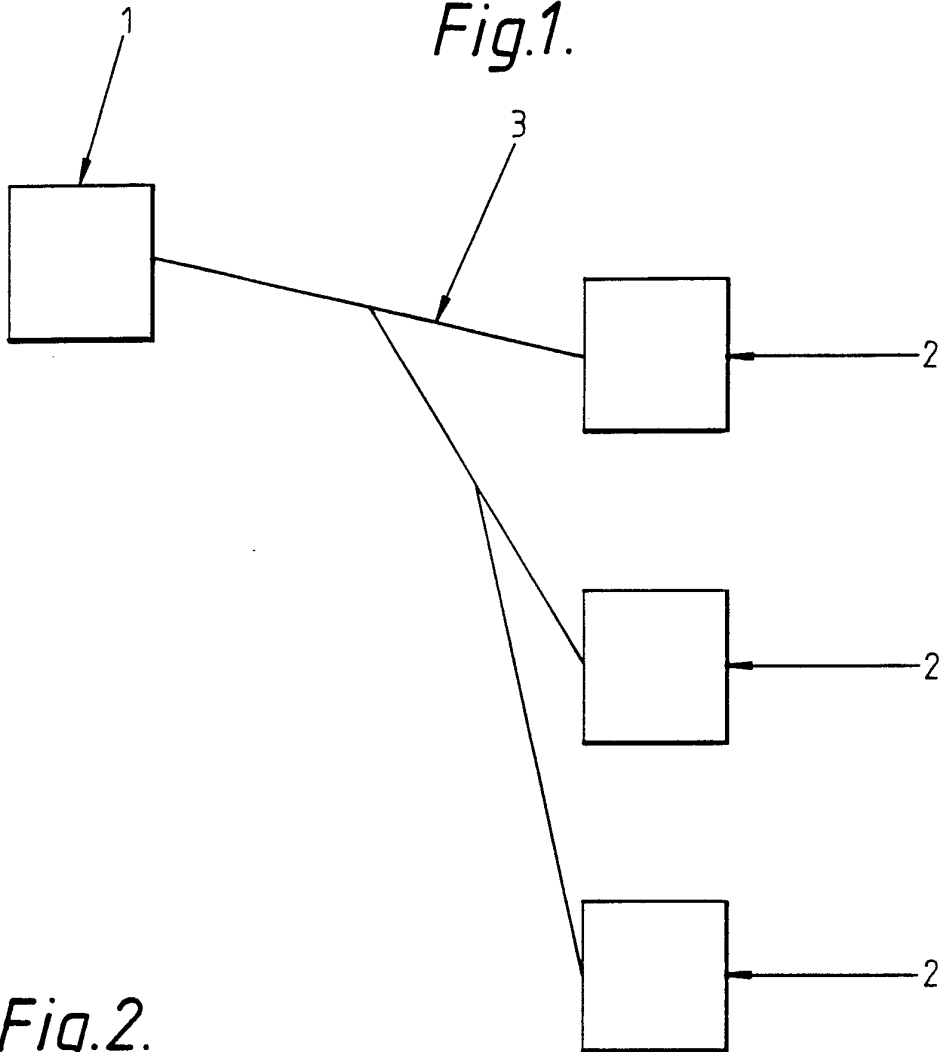


Fig.2.

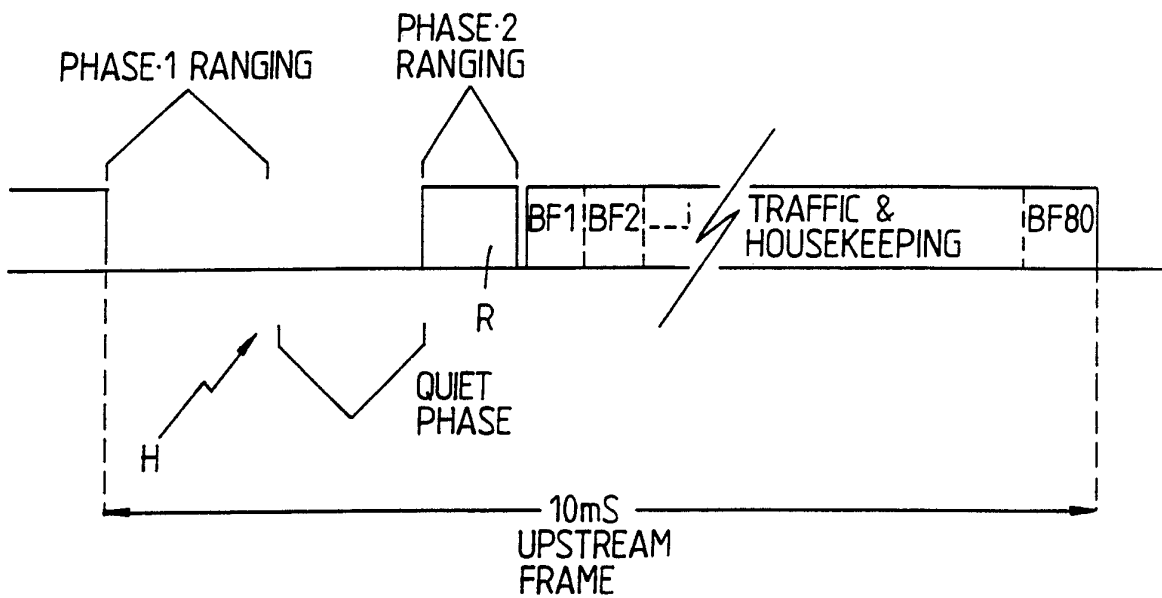


Fig.3.

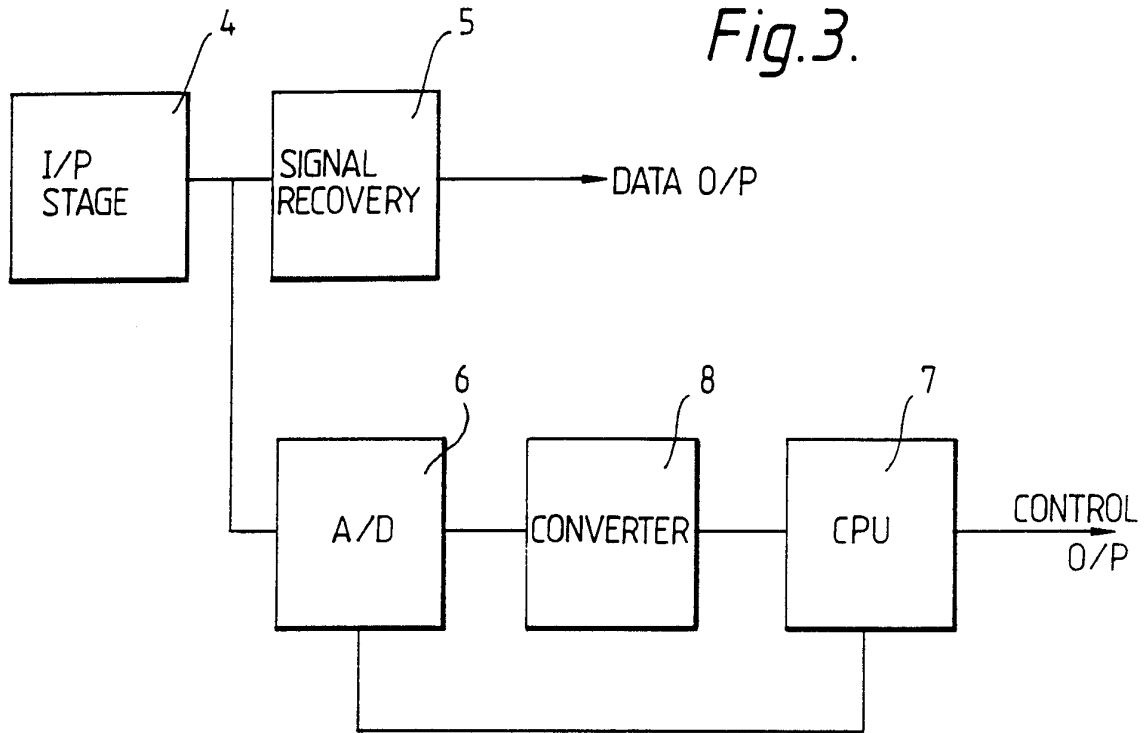


Fig.6.

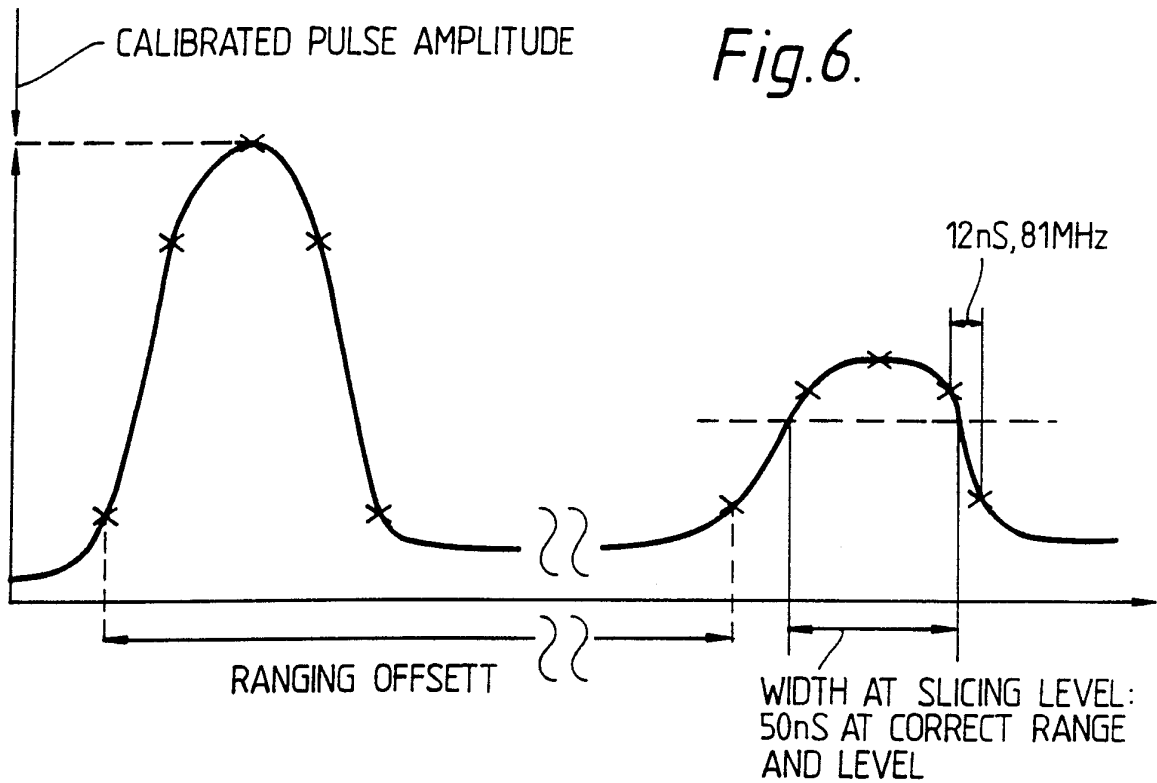


Fig.4.

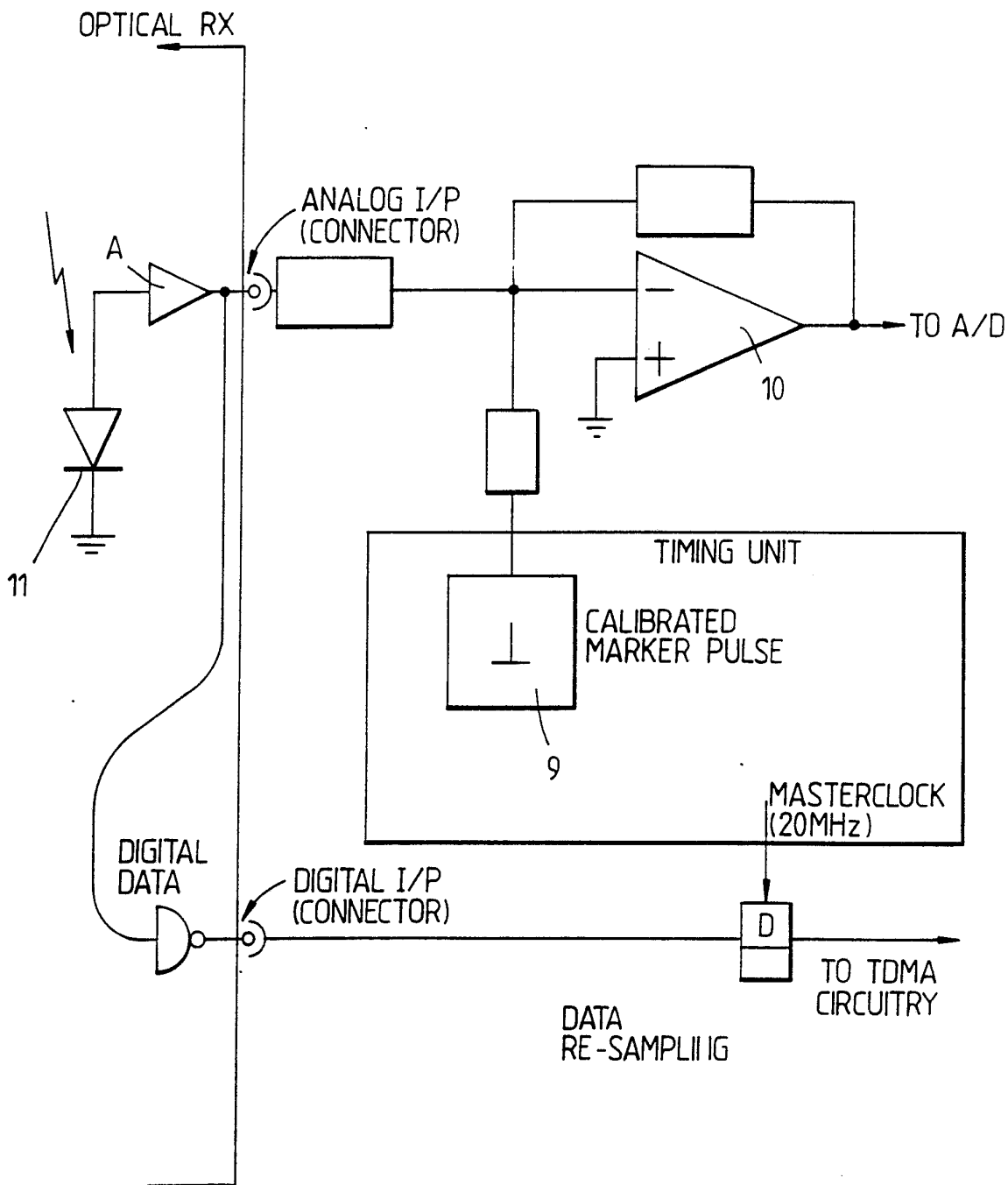
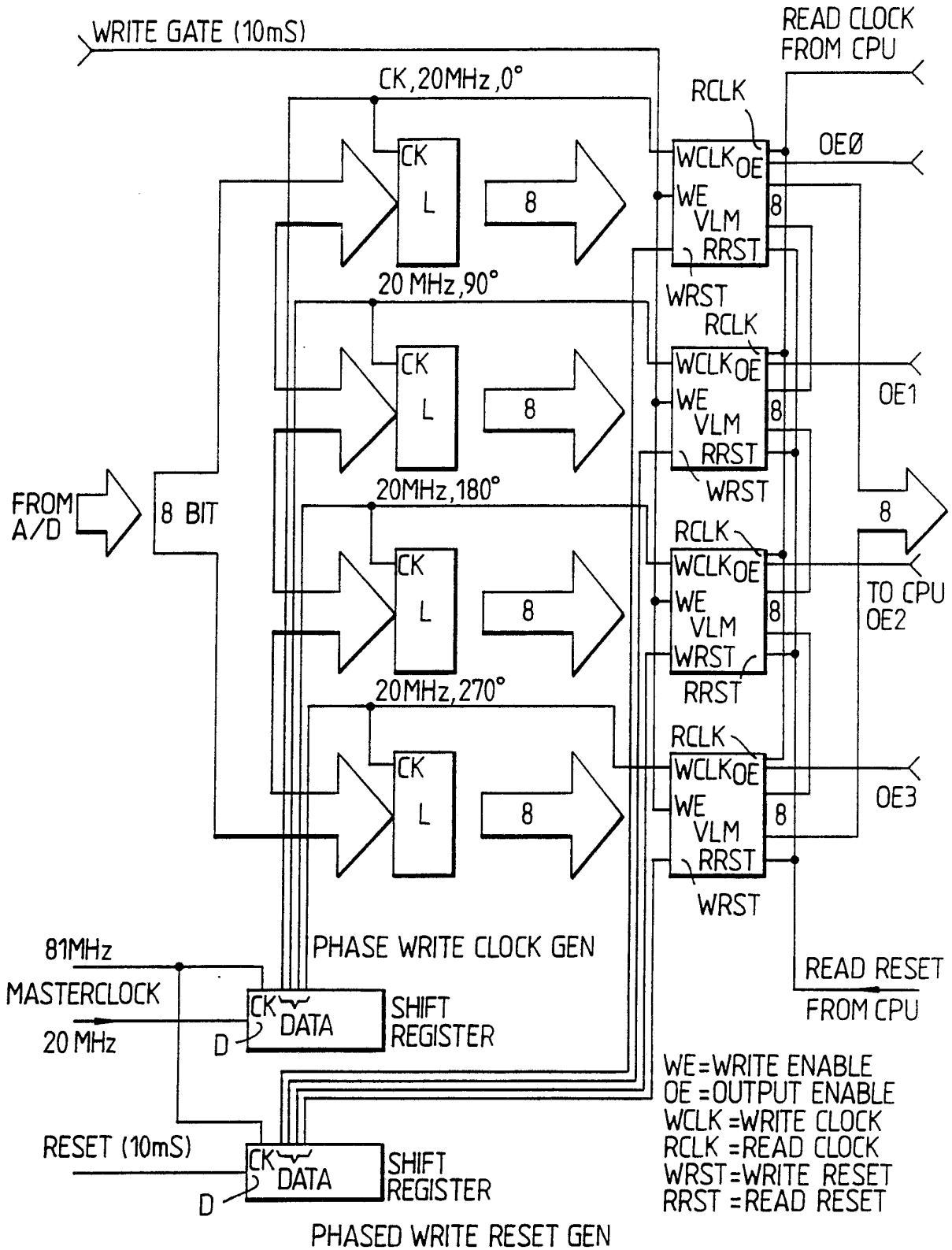



Fig.5.



INTERNATIONAL SEARCH REPORT

International Application No **PCT/GB 90/01758**

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) ⁴		
According to International Patent Classification (IPC) or to both National Classification and IPC		
IPC ⁵ : H 04 B 10/20		
II. FIELDS SEARCHED		
Minimum Documentation Searched ⁷		
Classification System	Classification Symbols	
IPC ⁵	H 04 B, H 04 J, H 04 L	
Documentation Searched other than Minimum Documentation to the extent that such Documents are included in the Fields Searched ⁸		
III. DOCUMENTS CONSIDERED TO BE RELEVANT ⁹		
Category ⁶	Citation of Document, ¹¹ with indication, where appropriate, of the relevant passages ¹²	Relevant to Claim No. ¹³
A	WO, A, 89/05070 (BRITISH TELECOMMUNICATIONS) 1 June 1989 see page 4, line 21 - page 5, line 8; page 12, lines 17-26 ---	1-10
A	WO, A, 88/09093 (BRITISH TELECOMMUNICATIONS) 17 November 1988 see page 3, line 8 - page 4, line 2; abstract; figure 4 ---	1-10
A	Patent Abstracts of Japan, volume 8, no. 81, (E-238)(1518), 13 April 1984 & JP, A, 592454 (NIPPON DENKI K.K.) 9 January 1984 see abstract ---	1,3,5,6
	./.	
<p>¹⁰ Special categories of cited documents:</p> <p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier document but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p> <p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.</p> <p>"Z" document member of the same patent family</p>		
IV. CERTIFICATION		
Date of the Actual Completion of the International Search	Date of Mailing of this International Search Report	
8th February 1991	26. 02. 91	
International Searching Authority	Signature of Authorized Officer	
EUROPEAN PATENT OFFICE		

III. DOCUMENTS CONSIDERED TO BE RELEVANT (CONTINUED FROM THE SECOND SHEET)		
Category *	Citation of Document, ** with indication, where appropriate, of the relevant passages	Relevant to Claim No.
A	Patent Abstracts of Japan, volume 10, no. 228 (E-426)(2284), 8 August 1986, & JP, A, 6163124 (MITSUBISHI ELECTRIC CORP.), 1 April 1986 see abstract -----	1,3,5,6

**ANNEX TO THE INTERNATIONAL SEARCH REPORT
ON INTERNATIONAL PATENT APPLICATION NO.**

GB 9001758

SA 41880

This annex lists the patent family members relating to the patent documents cited in the above-mentioned international search report. The members are as contained in the European Patent Office EDP file on 19/02/91
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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		AU-A- 1081488	27-07-88
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		EP-A- 0318335	31-05-89
		EP-A- 0296201	28-12-88
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