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- as to the identity of the inventor (Rule 4.17(i))
- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))
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[Continued on next page]

(54) Title: METHOD OF WAFER-TO-WAFER BONDING

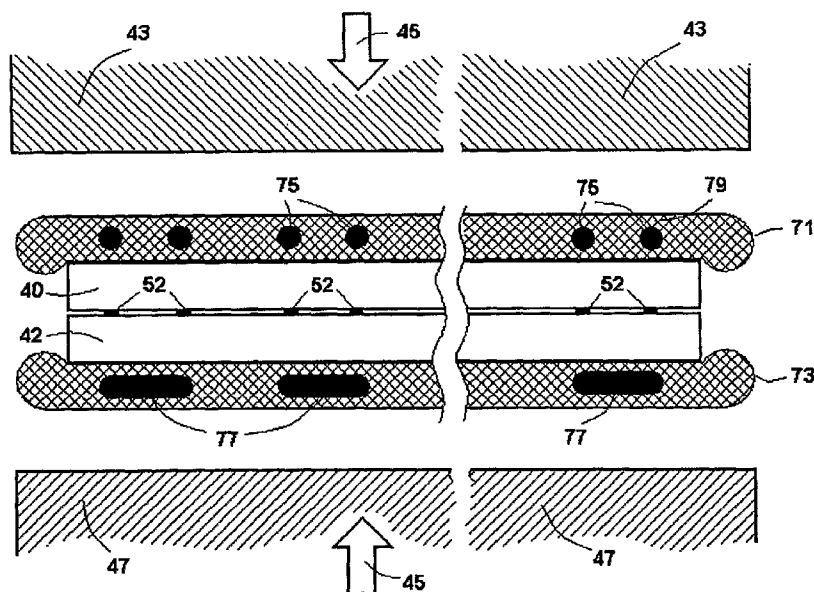


Fig. 10

(57) Abstract: Methods of wafer-to-wafer bonding are disclosed. These methods use a force-transposing substrate providing redistribution of the applied force to the local bonding areas across the wafer. Certain versions of the Present Invention also provide a compliant force-distributing member along with applying bonding material to bonding areas in selectable locations. A predetermined sequence of external force loading and temperature steps ensure creating bonds between the wafers in the bonding areas. The disclosed methods improve wafer bonding, by increasing its uniformity and strength across the wafer, increase both reproducibility and yield process and decrease cost of semiconductor and MEMS devices.

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B. FIELDS SEARCHED

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Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
EAST See search terms above and classes above.)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X, E	US2007/0181633 A1 (Erlach) 09 Aug 2007 (09.08.2007), pp 2-4.	1-33

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

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