(12) **UK Patent Application** (19)**GB** (11)**2494825** (13)**A**

(43) Date of Reproduction by UK Office 20.03.2013

(21) Application No: 1300084.9

(22) Date of Filing: 08.06.2011

Date Lodged: 04.01.2013

(30) Priority Data:
(31) **12821789** (32) **23.06.2010** (33) **US**

(86) International Application Data:
PCT/EP2011/059425 En 08.06.2011

(87) International Publication Data:
WO2011/160947 En 29.12.2011

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(51) INT CL:
G06F 1/32 (2006.01)

(56) Documents Cited:
US2008104587 A1
US2010037038 A1
EP2071458 A1

(58) Field of Search:
INT CL **G06F**
Other: **EPO-Internal**

(54) Title of the Invention: **Transparently increasing power savings in a power management environment**
Abstract Title: **Transparently increasing power savings in a power management environment**

(57) A mechanism is provided for transparently consolidating resources of logical partitions. Responsive to the existence of the non-folded resource on an originating resource chip, the virtualization mechanism determines whether there is a destination resource chip to either exchange operations of the non-folded resource with a folded resource on the destination chip or migrate operations of the non- folded resource to a non-folded resource on the destination chip. Responsive to the existence of the folded resource on the destination resource chip, the virtualization mechanism transparently exchanges the operations of the non- folded resource from the originating resource chip to the folded resource on the destination resource chip, where the folded resource remains folded on the originating resource chip after the exchange. Responsive to the absence of another non-folded resource on the originating resource chip, the vitalization mechanism places the originating resource chip into a deeper power saving mode.

FIG. 3

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