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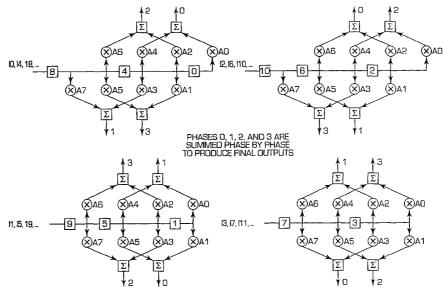
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### (54) Title: A HIGH SPEED BURST-MODE DIGITAL DEMODULATOR ARCHITECTURE



(57) Abstract: A unique demodulator device structure is disclosed which includes methods for enhancing the reliability of processing digital data at high speeds, while in some cases reducing the amount of hardware required for such structures. Parallel data paths within several common functions of the overall demodulator design allow for reducing the rate at which data is processed within each such data path (e.g., Fig. 3, paths 0, 1, 2 and 3) to a rate lower than that at which the data was received. Subsequently, the data may be passed on at the original received data rate or processing may be completed at a lower rate. The reduction in hardware typically results in reductions in mass, size, and power for the final demodulator while simultaneously allowing the demodulator to work at speeds not otherwise possible.



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### INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER  IPC(7) :HO4L 27/06, 27/22				
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According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED				
Minimum documentation searched (classification system followed by classification symbols)				
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched				
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where ap	opropriate, of the relevant passages	Relevant to claim No.	
A	US 5,454,009 A (FRUIT et al) 26 Sept	, ,	1-24	
	1 and 11, with corresponding description.			
A	US 5,459,524 A (COOPER) 17 October 1995, abstract, column 1 lines 38-53, column 4 lines 27-47, column 5 lines 1-12m column 7			
	lines 9-16, column 10 line 61 through column 11 line 24.			
Α	US 5,867,400 A (EL-GHOROURY et al) 02 February 1999, abstract, column 1 lines 5-9, column 2 lines 5-9, column 2 line 66			
	through column 3 line 23, column 4 lines 51-54			
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Further documents are listed in the continuation of Box C. See patent family annex.				
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