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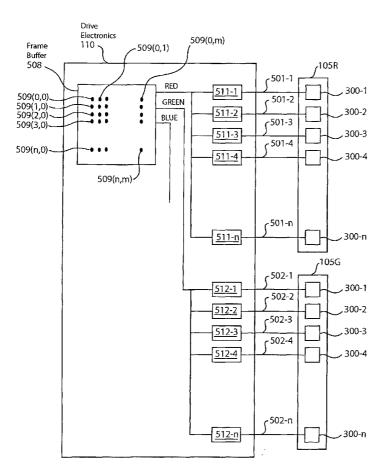
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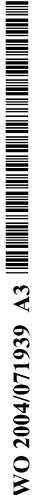
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## (54) Title: SYSTEM AND METHOD FOR SUB-PIXEL ELECTRONIC ALIGNMENT



(57) Abstract: In one embodiment, a delay circuit (511,512) is configured to delay pixel information from an image source, such as a frame buffer (508). The delay circuit (511,512) may be configured to delay the pixel information by an amount of time that would move a pixel projected on a surface by a distance less than a dimension of the pixel. A light modulator (300) may modulate a light beam onto a surface, such as a display screen, based on the delayed pixel information. This advantageously allows for sub-pixel electronic alignment.



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Continuation of B. FIELDS SEARCHED Item 3:	
WEST: (1) (correct\$3 or adjust\$4 or align\$4) SAME pixel\$5 SAME delay\$3 SAME modulat\$3 and delay\$3 WITH circuit\$4 SAME	
pixel\$5 SAME modulat\$3 SAME (dimension\$1 or distance\$1 or length\$1)	
(2) (MEMS or GLV or pixel\$5) SAME (project\$3 or modulat\$3 or align	\$4) SAME (display\$3 or scan\$4) SAME (dimension\$2
or tim\$3) and circuit\$4 WITH delay\$3 SAME (scan\$4 or modulat\$3) and circuit\$6 (compar\$5 or count\$3 or conver\$4) and circuit\$4 WITH delay\$3 SAME (scan\$4 or	or modulat\$3) SAME (scans4 or modulat\$3) SAME (scans4 or count\$3 or
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