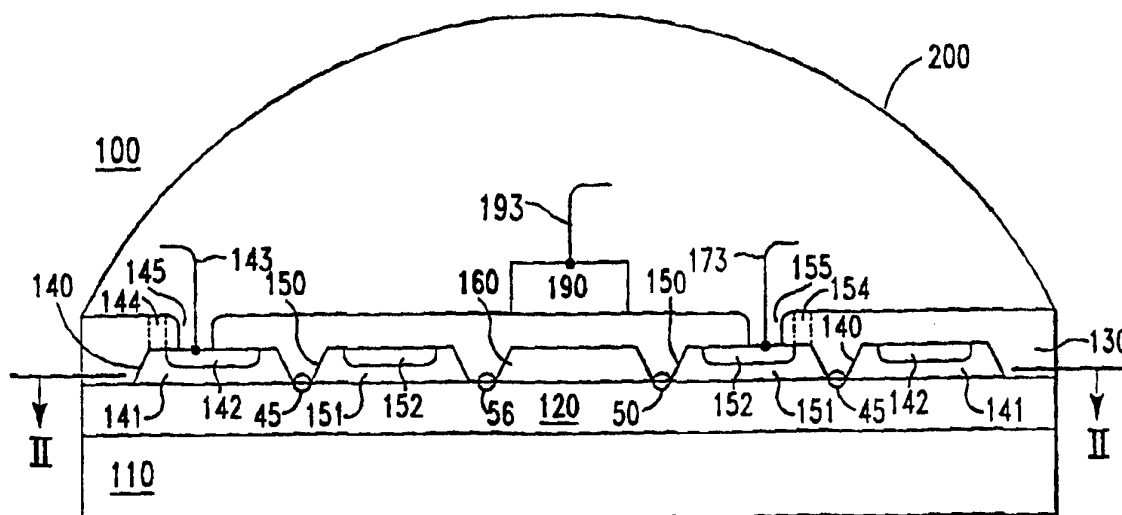




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(21) International Application Number: PCT/US96/03735 (22) International Filing Date: 19 March 1996 (19.03.96) (30) Priority Data: 08/491,323 30 June 1995 (30.06.95) US (71) Applicant: SIEMENS COMPONENTS, INC. [US/US]; 10950 North Tantau Avenue, Cupertino, CA 95014 (US). (72) Inventor: WHITNEY, David; 235 Porter Lane, San Jose, CA 95127 (US). (74) Agents: CODISPOTI, Joseph, S. et al.; Siemens Corporation, Intellectual Property Dept., 186 Wood Avenue South, Iselin, NJ 08830 (US).		(81) Designated States: CA, CN, JP, KR, European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i>

(54) Title: MONOLITHIC LINEAR OPTOCOUPLER



(57) Abstract

A monolithic optocoupler having a light emitting diode for emitting light, a first photodiode for producing a current based on light sensed by it, a second photodiode for producing a current based on light sensed by it and an insulator for electrically isolating the light emitting diode from the first photodiode. The insulator is substantially transparent to light emitted by the light emitting diode. The first and second photodiodes are positioned with respect to the light emitting diode such that directional non-uniformities of light emitted by the light emitting diode are compensated.

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MONOLITHIC LINEAR OPTOCOUPLER**TECHNICAL FIELD**

A related application entitled "A METHOD OF
5 MANUFACTURING A MONOLITHIC LINEAR OPTOCOUPLER" by the same
inventor, is being filed on the same day herewith and is
incorporated by reference herein.

The present invention relates to an optocoupler. The
present invention relates, in particular, to a monolithic
10 structure for an optocoupler that provides improved
linearity.

BACKGROUND OF THE INVENTION

Optocouplers are used to electrically isolate an input
15 signal from a corresponding output signal. For example,
optocouplers may be used in data access arrangements
("DAAs"). A data access arrangement (DAA) is used for
interfacing a data terminal equipment ("DTE") (such as data
modems, facsimile machines, non-cellular portable
20 telephones, speaker phones, and message answering machines,
for example) with lines of the public switched telephone
network ("PSTN"). The network (PSTN) must be protected from
potential damage due to, for example, faulty data terminal
equipment (DTE) or inadvertent shorts through the data
25 terminal equipment (DTE) to its power line. Indeed, the
United States Federal Communications Commission ("FCC")
requires a 1500 volt isolation between data terminal
equipment (DTE) and the network (PSTN). In the past, data
access arrangements (DAAs) have used transformers to
30 provide such electrical isolation. However, due to their
relative expense and large size and weight, transformers
are disadvantageous, particularly for use in portable data
terminal equipment (DTE). Alternative isolation
components, such as optical isolators, must be used for
35 such reduced volume/weight applications.

Known optocouplers include an LED which is optically
coupleable with, but electrically isolated from, a

photodiode. The photodiode ("the output signal photodiode") generates an output signal based on the intensity of light emitted from the LED and detected by it.

Known optocouplers may also include an additional
5 photodiode ("the feedback control signal photodiode") for generating a servo-feedback signal based on the intensity of light emitted from the LED and detected by it. The feedback control signal photodiode allows the optocoupler to operate more linearly. In these known optocouplers, the
10 output signal photodiode and the feedback control signal photodiode are discrete elements. As such, a first direction defined between the LED and the output signal photodiode differs from a second direction defined between the LED and the feedback control signal diode.
15 Unfortunately, the LED may emit directionally non-uniform light. As a result, the intensity of light detected by the output signal diode usually varies from the intensity of light detected by the feedback control signal diode. Consequently, the output of the feedback control signal
20 photodiode will not accurately indicate the intensity of the light from the LED detected by the output signal photodiode, thereby preventing the full compensation of non-linearities in the operation of the optocoupler.

One solution to the problem of directionally
25 non-uniform light emission is to place the output signal photodiode in close proximity to the feedback control signal photodiode. Unfortunately, this offers only a partial solution because directionally non-uniform light will still cause non-linearities, although to a lesser
30 extent. In addition, the feedback control signal photodiode must be adequately isolated from the output signal photodiode to provide adequate electrical isolation. Such isolation is difficult when the two photodiodes are located close to one another.

35 In view of the above described problems with known optocoupler circuits, an optocoupler immune to directionally non-uniform light emission by the LED is

needed. Furthermore, any photodiodes of the optocoupler should be adequately electrically isolated from one another. Moreover, the optocoupler should be relatively simple and economical to manufacture. If possible, the
5 optocoupler should be integrated on a single chip.

SUMMARY OF THE INVENTION

Briefly, the present invention provides an LED, a first photodiode electrically isolated from the LED, and a
10 second photodiode electrically isolated from the LED and the first photodiode. The first photodiode is preferably a feedback control signal photodiode and the second photodiode is preferably an output signal photodiode. The first and second photodiodes are preferably integrated on
15 a single chip with the LED. The first and second photodiodes are configured and positioned relative to the LED so that any directional non-uniformities of the light emitted by the LED are compensated. Specifically, the emitted light detected by the photodiodes represents a
20 directional average of the light emitted in all directions.

The first and second photodiodes are preferably separated by an oxide, such as silicon dioxide for example, which electrically isolates the two photodiodes.

The first and second photodiodes preferably have the
25 same shape, thereby providing further compensation for any directional non-uniformities of the light emitted by the LED.

A reflective dome may be provided above the monolithic structure thereby forming an optical cavity. The
30 reflective dome increases the signal to noise ratio of the optocoupler.

BRIEF DESCRIPTION OF THE DRAWINGS

For a better understanding of the invention, reference
35 is made to the following description of an exemplary embodiment thereof, and to the accompanying drawings, wherein:

Figure 1 is a side view, through the cross-section defined by line I-I of Figure 2, of an optocoupler constructed in accordance with the present invention;

Figure 2 is a plan view, through a cross section defined by line II-II shown in Figure 1, of the optocoupler of Figure 1;

Figure 3 is a schematic diagram of a conventional optocoupler chip having an externally coupled feedback control loop; and

Figures 4a through 4d illustrate a method for fabricating the monolithic structure of an optocoupler of the present invention.

DETAILED DESCRIPTION

Figure 3 is a schematic diagram of a conventional optocoupler chip 390 having an externally coupled feedback control loop. The conventional optocoupler includes a LED 300, an output signal photodiode 310, and a feedback control signal photodiode 320. A differential (error) operational amplifier 330 is coupled from a control signal output terminal 394 to a LED input signal terminal 391.

The LED 300 emits a light having an intensity based on the voltage of a signal output from the differential amplifier 330. The differential amplifier 330 may either source or sink the LED current. The output signal photodiode 310 is electrically isolated from, and optically coupleable with, the LED 300. A first direction D1 is defined from the LED 300 to the output signal photodiode 310. The anode of the output signal photodiode 310 is coupled with a first voltage source V_{S1} via terminal 395 and the cathode of the output signal photodiode 310 is coupled with an output load via terminal 396. Alternatively, if a depletion layer photodiode is used, the cathode of the output signal photodiode 310 can be coupled with a voltage source and the anode of the output signal photodiode 310 can be coupled

with the output load, such that the reverse biased depletion layer photodiode operates below its breakdown voltage. In either case, the output signal photodiode 310 supplies an output signal to the output load (or to an output driver) based on the intensity of light detected by it.

The feedback control signal photodiode 320 is also optically coupleable with the LED 300. Although the feedback control signal photodiode 320 may also be electrically isolated from the LED 300, such electrical isolation is not required. A second direction D2 is defined from the LED 300 to the feedback control signal photodiode 320. The anode of the feedback control signal photodiode 320 is coupled with a second supply voltage V_{S2} via terminal 393 and the cathode of the feedback control signal photodiode 320 is coupled with a first (inverting) input of the differential amplifier 330. Alternatively, if a depletion layer photodiode is used, the cathode of the feedback control signal photodiode 320 can be coupled with a voltage source and the anode of the feedback control signal photodiode 320 can be coupled with the first input of the differential amplifier 330, such that the reverse biased depletion layer photodiode operates below its breakdown voltage. A second (non-inverting) input of the differential amplifier 330 is provided with an input signal V_{DRIVE} .

Unfortunately, the LED 300 does not emit light in a directionally uniform manner. Consequently, the intensity of the light emitted in direction D1 may vary from the intensity of the light emitted in the direction D2. This difference in detected intensity makes it difficult to properly control the voltage applied to the LED 300. As discussed above, if the output signal photodiode 310 is placed in close proximity to the feedback control signal photodiode 320 such that the direction D1 closely approaches the direction D2, electrically isolating the two photodiodes may become difficult.

Figure 1 is a cross-sectional side view of the monolithic structure 100 of an optocoupler of the present invention. The cross section is taken across a middle of the monolithic structure as shown by line I-I of Figure 2.

5 The monolithic structure 100 includes a silicon substrate 110, a layer of oxide 120 (such as silicon dioxide for example), an inner silicon section 160, an inner silicon tub 150, an outer silicon tub 140, a thick oxide (such as silicon dioxide) covering 130, and a light
10 emitting diode (LED) 190.

 The layer of oxide 120 is located above the silicon substrate 110 such that an interface is defined between an upper surface of the silicon substrate 110 and a lower surface of the oxide layer 120. The inner silicon section
15 160 is located on, and preferably in a center region of, an upper surface of the oxide layer 120. The inner silicon tub 150 is located on the upper surface of the oxide layer 120 and surrounds the inner silicon section 160. The inner edges 159 of the inner silicon tub 150 are spaced from the
20 outer edges 168 of the inner silicon section 160 thereby defining an first area 56 of the upper surface of the oxide layer 120 that is not in contact with a silicon structure. The outer silicon tub 140 is located on the upper surface of the oxide layer 120 and surrounds the inner silicon tub
25 150. The inner edges 149 of the outer silicon tub 140 are spaced from the outer edges 158 of the inner silicon tub 150 thereby defining a second area 45 of the upper surface of the oxide layer 120 that is not in contact with a silicon structure.

30 The inner silicon tub 150 includes a rectangular p-doped "tub" region 151. Alternatively, the silicon tub 150 may have an octagonal p-doped "tub" region 151. Providing an octagonal p-doped "tub" region 151, however, requires an additional step of etching corner regions
35 during fabrication. Indeed, the shape of the p-doped "tub" region 151 may assume any shape that surrounds the LED 190 and that can be fabricated from the crystalline structure.

However, symmetric shapes, and in particular, shapes symmetric with respect to two perpendicular lines intersecting at the center of the shape are preferred.

An n-doped region 152 is located within the p-doped tub 151 thereby forming a p-n junction 151-152. This p-n junction 151-152 defines a first diode; the p-doped tub 151 forming the anode of the first diode and the n-doped region 152 forming the cathode of the first diode. Similarly, the outer silicon tub 140 includes a rectangular p-doped "tub" region 141 but can assume other shapes (such as octagonal) as discussed above. An n-doped region 142 is located within the p-doped tub 141 thereby forming a p-n junction 141-142. This p-n junction 141-142 defines a second diode; the p-doped tub 141 forming the anode of the second diode and the n-doped region 142 forming the cathode of the second diode.

The thick oxide covering 130 covers the inner silicon region 160, the inner silicon tub 150, the outer silicon tub 140, and the first and second areas 56 and 45 of the upper surface of the oxide layer 120 not covered with silicon. However, voids for providing access to cathode (144,154) and anode (145,155) bond pads are formed in the thick oxide layer to permit anode and cathode connections to be formed to the first and second diodes. Specifically, as shown in Figures 1 and 2, the void 145 provides access to a cathode bond pad on the n-doped region 142 of the outer silicon tub 140. A similar void 144 provides access to an anode bond pad on the p-doped tub 141 of the outer silicon tub 140. Similarly, as shown in Figures 1 and 2, the void 155 provides access to a cathode bond pad on the n-doped region 152 of the inner silicon tub 150. A similar void 154 provides access to an anode bond pad on the p-doped tub 151 of the inner silicon tub 150.

The LED 190 is arranged on an upper exposed surface of the thick oxide covering 130. As shown in Figure 2, the LED 190 includes a cathode bonding pad 194 and an anode bonding pad 195. The thick oxide covering 130 should

permit light at the wavelength of the light (typically infra-red) emitted by the LED 190 to pass. That is, the thick oxide covering 130 should be substantially transparent to light emitted by the LED 190.

5 An optional reflective dome 200, covering the monolithic structure 100, may also be provided. The reflective dome 200 may be made from an optically transparent material having a reflective layer for example.

10 The first diode 151, 152 formed by the inner silicon tub 150 is a photodiode and may be used to either generate an output signal or a feedback control signal. However, given its close proximity to the LED 190, the first photodiode 151, 152 is preferably used for generating a feedback control signal for the LED 190. This is because
15 the feedback control signal photodiode does not need to be electrically isolated from the LED 190. The second diode 141, 142 formed by the outer silicon tub 140 is a photodiode and may be used to either generate a feedback control signal or an output signal. The second
20 photodiode 141, 142 should perform the function not being performed by the first photodiode 151, 152. Accordingly, the second photodiode is preferably used for generating an output signal. This provides even better electrical isolation between the output signal photodiode and the
25 LED 190.

 In operation, when the LED 190 emits light, some of the emitted light passes through the optically transparent thick oxide layer 130 to the first and second photodiodes. The first and second photodiodes produce electrical output
30 signals based on the intensity of the light that they receive. Since both the first and second photodiode structures surround the LED, any directional non-uniformities in the intensity of light emitted by the LED are compensated for because the intensities of light
35 emitted in all directions is averaged by the surrounding photodiodes.

If the optional reflective dome 200 is provided, an optical cavity between the upper surface of the monolithic structure 100 and the inner surface of the reflective dome 200 is defined. Consequently, some of the light emitted by the LED 190 is reflected by the reflective dome 200 to the first and second photodiodes. Thus, by increasing the net intensity of the light received by the first and second photodiodes, the reflective dome 200 improves the signal to noise ratio of the optocoupler.

10 The inner and outer silicon tubs 150 and 140, respectively (i.e., the first and second photodiodes) are surrounded by an oxide; in particular, a bottom surface of the inner and outer silicon tubs 150 and 140, respectively, rest on the oxide layer 120, and side and top surfaces
15 (except in the areas of the voids used for accessing anode and cathode bond pads) of the inner and outer silicon tubs 150 and 140, respectively, are surrounded by the thick oxide 130. As a result, the first and second photodiodes are electrically isolated from each other and from the LED
20 190.

Figures 4a through 4d illustrate an exemplary method of fabricating the monolithic 100 structure of Figures 1 and 2. As shown in Figures 4a and 4b, trenches 410 are formed in an n-type silicon wafer 400 in a known manner, e.g., etching, stamping, etc. Next, as shown in Figure 4c, an n-type material is introduced, e.g., by diffusion, into the wafer 400, thereby forming an N⁺ region 420. Silicon is then oxidized to form an SiO₂ layer 430. Then, as shown also in Figure 4c, poly silicon (or "amorphous silicon")
25 440 is deposited on the upperside of the resulting structure. The poly silicon fills the trenches 410 and provides mechanical stability. Next, as shown by the dashed line 500 of Figure 4c, the n-type silicon layer 400 is back-lapped to expose portions of the N⁺ layer 420. As
30 shown in Figure 4d, the resulting structure is then flipped and is processed using dielectrically isolated tubs, thereby forming P⁺ regions 450. The silicon oxide layer

120 and the silicon substrate 110 (See Figure 1) are then added. The resulting structure is then flipped. The poly silicon 440 (130) is then appropriately shaped, e.g., by etching, to form the voids 144, 145, 154, 155 (See Figures 1 and 2) that allow bond pads and wire leads to be applied to the cathodes and anodes of the two photodiodes.

The embodiments described herein are merely illustrative of the principles of the present invention. Various modifications may be made thereto by persons ordinarily skilled in the art, without departing from the scope or spirit of the invention.

WHAT IS CLAIMED IS:

1. A device, comprising:
 - (a) a light emitting diode for emitting light;
 - 5 (b) a first photodiode monolithically formed with the light emitting diode, for producing a current based on light sensed by it;
 - (c) a second photodiode monolithically formed with the light emitting diode, for producing a current based on
10 light sensed by it; and
 - (d) an insulator for electrically isolating the light emitting diode from the first photodiode, the insulator being substantially transparent to light emitted by the light emitting diode,
 - 15 wherein the first and second photodiodes are positioned with respect to the light emitting diode such that directional non-uniformities of light emitted by the light emitting diode are compensated.
- 20 2. The device of claim 1, wherein the first photodiode is an output signal photodiode for producing an output signal based on light detected by it and the second photodiode is a feedback control signal photodiode for producing a feedback control signal based on light detected by it.
- 25 3. The device of claim 1, wherein the second photodiode defines a plane in which the second photodiode surrounds a projection of the light emitting diode normal to the plane.
- 30 4. The device of claim 3, wherein the first photodiode surrounds the second photodiode in a predefined plane.
5. The device of claim 3, wherein the first photodiode surrounds the second photodiode.
- 35 6. The device of claim 1, wherein the insulator is a thick oxide layer.

7. The device of claim 6, wherein the insulator is a thick layer of silicon dioxide.

5 8. The device of claim 1, wherein the first photodiode is formed by a tub of a p-doped semiconductor filled with an n-doped semiconductor.

9. The device of claim 1, wherein the second photodiode
10 is formed by a tub of a p-doped semiconductor filled with an n-doped semiconductor.

10. The device of claim 1, further comprising:

(e) a substrate having an upper surface; and

15 (f) an electrically insulating layer formed on the upper surface of the substrate and having an upper surface, wherein the first photodiode is formed on the upper surface of the electrically insulating layer,

wherein the second photodiode is formed on the upper
20 surface of the electrically insulating layer, separated from the first photodiode, thereby defining an area of the upper surface of the electrically insulating layer not covered with the first or second photodiodes,

wherein the insulator is formed above the upper layer
25 of the electrically insulating layer and substantially covers each of the first and second photodiodes, thereby electrically isolating the first photodiode from the second photodiode, and

wherein the light emitting diode is located on an
30 upper exposed surface of the insulator, whereby it is electrically isolated from the first photodiode.

11. The device of claim 10, wherein the second photodiode defines a plane in which the second photodiode surrounds a
35 projection of the light emitting diode normal to the plane.

12. The device of claim 11, wherein the second photodiode is shaped as a rectangle.

13. The device of claim 11, wherein the second photodiode
5 is shaped as a square.

14. The device of claim 11, wherein the second photodiode is shaped as an octagon.

10 15. The device of claim 10, wherein the first photodiode is shaped to surround the second photodiode.

16. The device of claim 15, wherein the first photodiode is shaped as a rectangle.

15

17. The device of claim 15, wherein the first photodiode is shaped as a square.

18. The device of claim 15, wherein the first photodiode
20 is shaped as an octagon.

19. The device of claim 10, wherein the first photodiode is formed by a tub of a p-doped semiconductor filled with an n-doped semiconductor.

25

20. The device of claim 10, wherein the second photodiode is formed by a tub of a p-doped semiconductor filled with an n-doped semiconductor.

30 21. The device of claim 1, further comprising a reflective dome for reflecting light emitted by the light emitting diode, the reflective dome being arranged above the light emitting diode and the insulator, thereby defining an optical cavity.

35

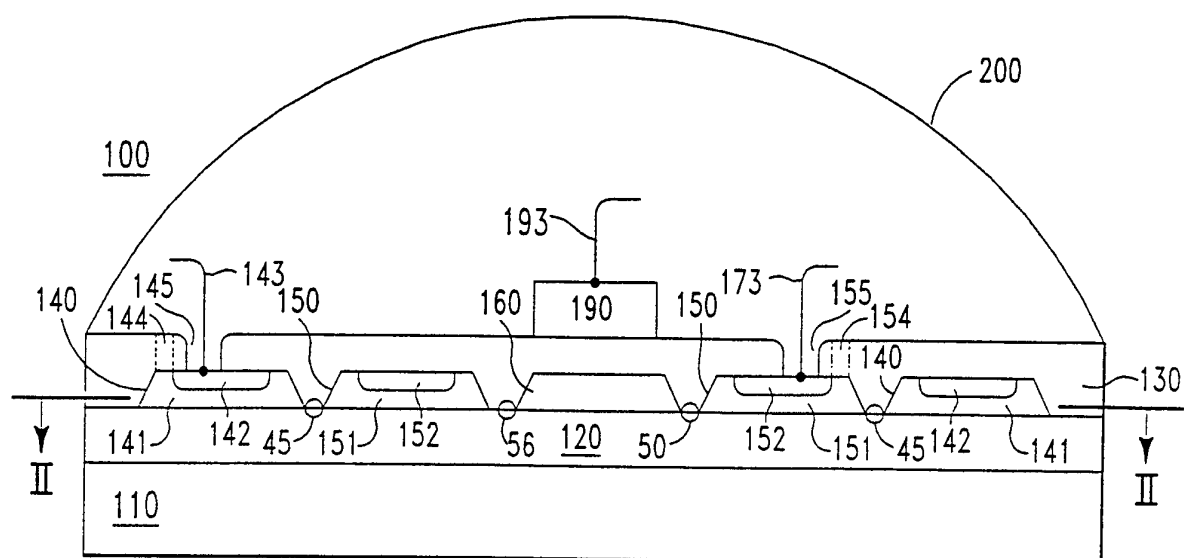


FIG. 1

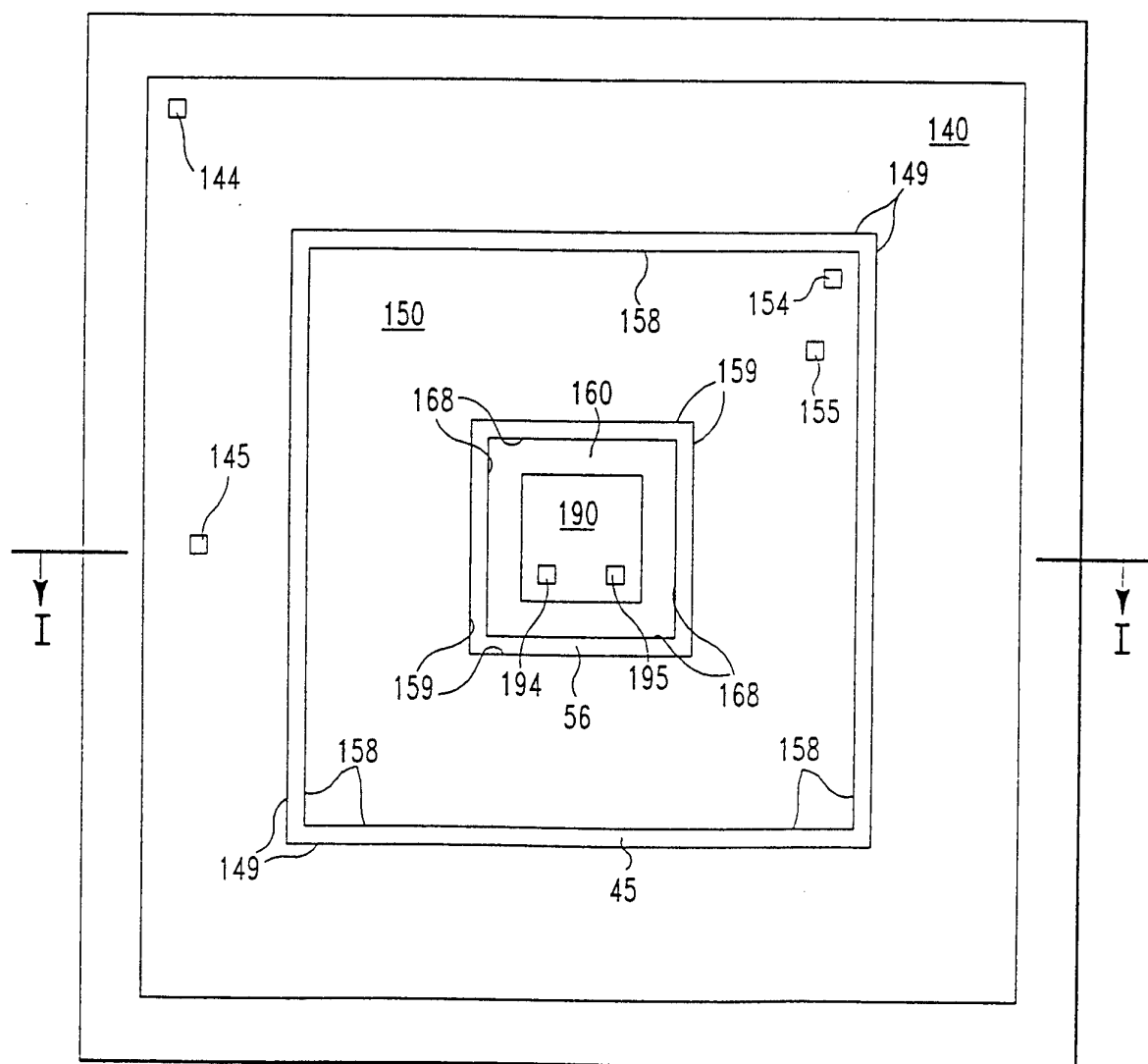


FIG. 2

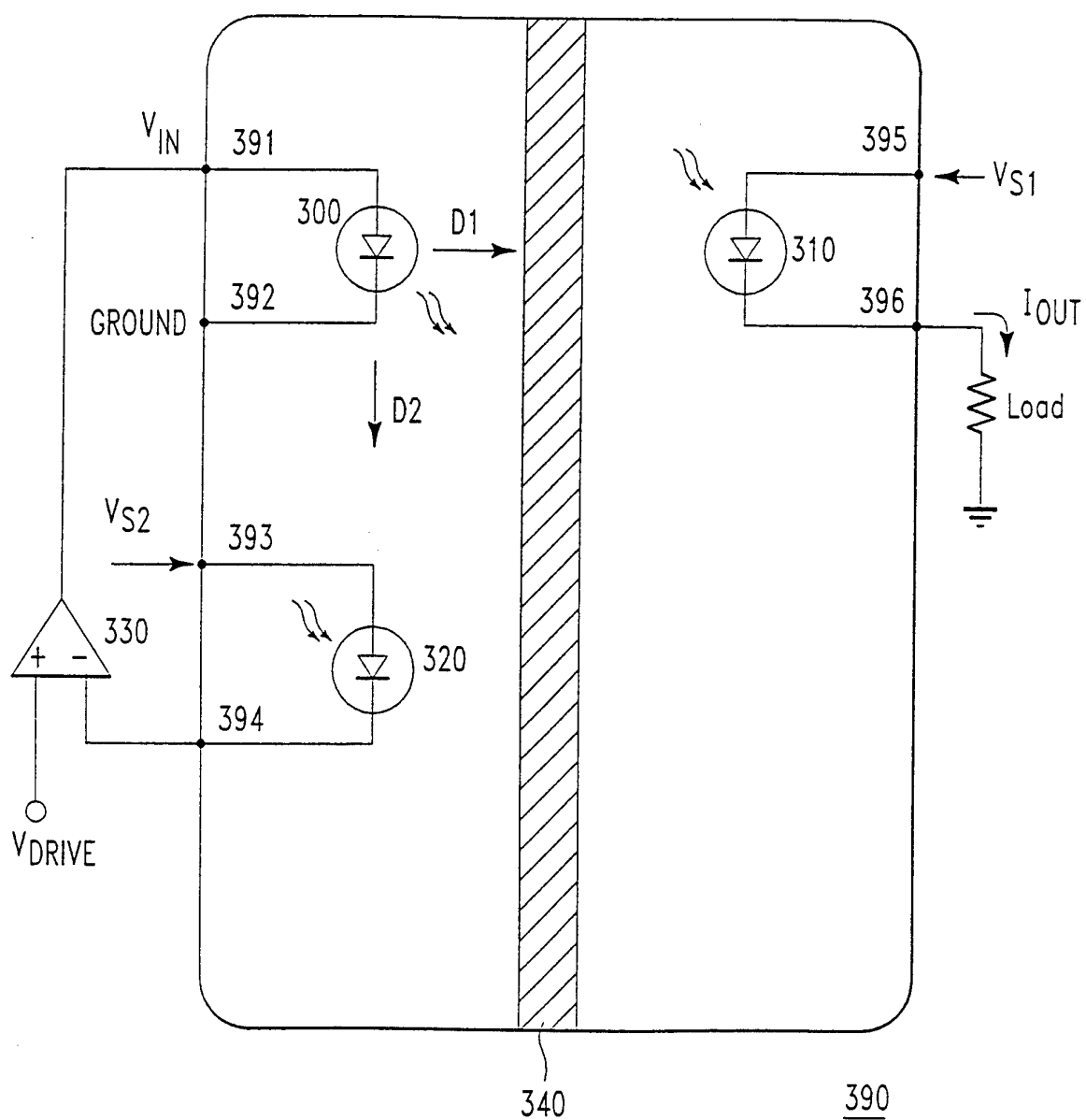


FIG. 3

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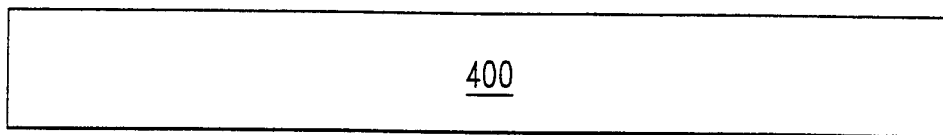


FIG. 4a

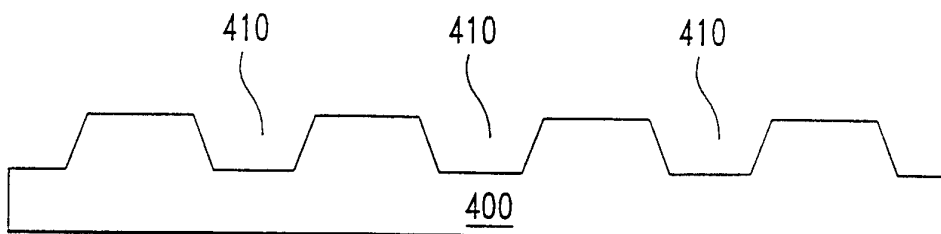


FIG. 4b

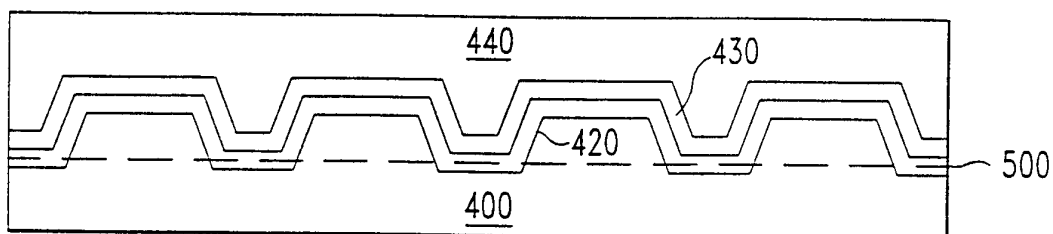


FIG. 4c

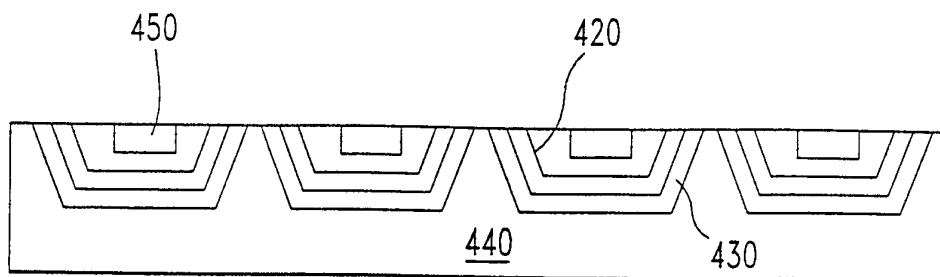


FIG. 4d

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 96/03735

A. CLASSIFICATION OF SUBJECT MATTER IPC 6 H01L31/173		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC 6 H01L		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practical, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP,A,0 084 621 (IBM) 3 August 1983 see page 2 - page 3; figure 1 ---	1,2,10
A	EP,A,0 632 509 (MOTOROLA INC) 4 January 1995 see column 3, line 1 - column 4, line 28; figure 1 ---	1,2,10
A	PATENT ABSTRACTS OF JAPAN vol. 010, no. 164 (E-410), 11 June 1986 & JP,A,61 014753 (TOSHIBA KK), 22 January 1986, see abstract ---	1,3-5,10
A	EP,A,0 645 827 (SIEMENS AG) 29 March 1995 see claims --- -/--	1,6,10
<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C. <input checked="" type="checkbox"/> Patent family members are listed in annex.		
* Special categories of cited documents : "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. "&" document member of the same patent family		
Date of the actual completion of the international search 5 July 1996		Date of mailing of the international search report 17. 07. 96
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+ 31-70) 340-2040, Tx. 31 651 epo nl, Fax (+ 31-70) 340-3016		Authorized officer Lina, F

INTERNATIONAL SEARCH REPORT

International Application No
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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>PATENT ABSTRACTS OF JAPAN vol. 010, no. 192 (E-417), 5 July 1986 & JP,A,61 036981 (TOSHIBA CORP), 21 February 1986, see abstract</p> <p>-----</p>	1,10

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US 96/03735

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP-A-0084621	03-08-83	US-A- 4477721	16-10-84
		JP-C- 1737180	26-02-93
		JP-B- 4015632	18-03-92
		JP-A- 58130580	04-08-83

EP-A-0632509	04-01-95	JP-A- 7015030	17-01-95

EP-A-0645827	29-03-95	NONE	
