

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization

International Bureau



(10) International Publication Number

WO 2015/036887 A1

(43) International Publication Date
19 March 2015 (19.03.2015)

WIPO | PCT

(51) International Patent Classification:

H01L 25/075 (2006.01) *H01L 33/48* (2010.01)
H01L 33/60 (2010.01) *H01L 33/50* (2010.01)
H01L 33/00 (2010.01) *H01L 33/58* (2010.01)

MARTYNOV, Yourii; High Tech Campus 5, NL-5656 AE Eindhoven (NL).

(21) International Application Number:

PCT/IB2014/064106

(74) Agents: **VAN EEUWIJK, Alexander** et al.; High Tech Campus 5, NL-5656 AE Eindhoven (NL).

(22) International Filing Date:

28 August 2014 (28.08.2014)

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(25) Filing Language:

English

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

(26) Publication Language:

English

(30) Priority Data:

61/877,434 13 September 2013 (13.09.2013) US
61/936,360 6 February 2014 (06.02.2014) US

(71) Applicant: **KONINKLIJKE PHILIPS N.V. [NL/NL]**; High Tech Campus 5, NL-5656 AE Eindhoven (NL).

(72) Inventors: **STOCKMAN, Stephen Andrew**; High Tech Campus 5, NL-5656 AE Eindhoven (NL). **DE SAMBER, Marc Andre**; High Tech Campus 5, NL-5656 AE Eindhoven (NL). **SHCHEKIN, Oleg Borisovich**; High Tech Campus 5, NL-5656 AE Eindhoven (NL). **SWEEGERS, Norbertus Antonius Maria**; High Tech Campus 5, NL-5656 AE Eindhoven (NL). **HAQUE, Ashim Shatil**; High Tech Campus 5, NL-5656 AE Eindhoven (NL).

[Continued on next page]

(54) Title: FRAME BASED PACKAGE FOR FLIP-CHIP LED

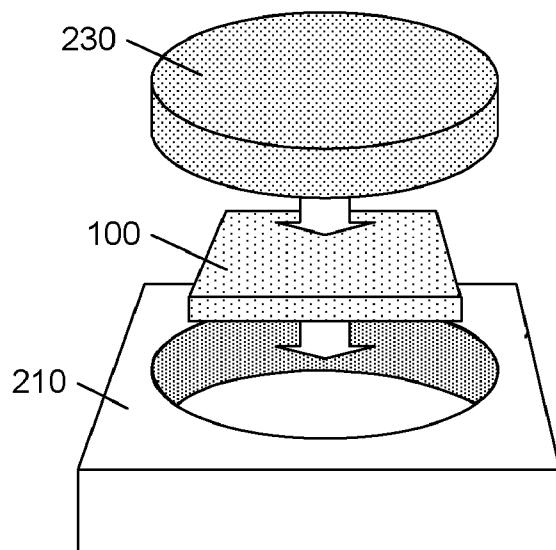


FIG. 3A

(57) **Abstract:** A hollow frame is configured to surround the periphery of a substantially self-supporting flip-chip light emitting device. The frame may be shaped to also contain a wavelength conversion element above the light emitting surface of the light emitting device. The lower surface of the light emitting device, which is exposed through the hollow frame, includes contact pads coupled to the light emitting element for surface mounting the light emitting module on a printed circuit board or other fixture. The flip-chip light emitting device may include a patterned sapphire substrate (PSS) upon which the light emitting element is grown, the patterned surface providing enhanced light extraction from the light emitting element, through the patterned sapphire substrate.



Declarations under Rule 4.17:

- *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))* — *with international search report (Art. 21(3))*
- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

Published:

FRAME-BASED PACKAGE FOR FLIP-CHIP LED

FIELD OF THE INVENTION

This invention relates to the field of light emitting devices, and in particular to a frame that provides a reflective enclosure about the periphery of the device.

5

BACKGROUND OF THE INVENTION

The ever expanding use of semiconductor light emitting devices has produced a highly competitive market for these devices. In this market, performance and price are often significant for providing product distinction among vendors. Accordingly, a common objective is to reduce the cost of production while providing equivalent or better performance

10 from the light emitting device.

One technique for providing relatively inexpensive packaging of a light emitting element is to place the light emitting element in a frame with leadframe contacts that facilitate external connections to the light emitting element. An LED leadframe generally comprises a pair of conductors that are shaped to provide for external connections to the

15 LED. A support frame may be molded about the leadframe such that the leadframe conductors provide conductive surfaces within the support frame to mount the LED, and conductive surfaces at the exterior of the support frame to mount the framed LED on a printed circuit board or other fixture.

USPA 2010/0207140, "COMPACT MOLDED LED MODULE", published 19 August 2010 for Serge L. Rudaz, Serge Bierhuizen, and Ashim S. Haque, and incorporated by reference herein, discloses an array of support frames within which light emitting devices are bonded to leadframe contacts within each support frame, as illustrated in FIGs. 7A-7B.

FIG. 7A illustrates two leadframes 16 within a molded frame array 40. Each leadframe 16 includes conductors 12 and 14. In this example a tub 10 is formed about each leadframe 16, and the conductors 12 and 14 of leadframe 16 are shaped to extend through the frame element 40 so as to provide contacts 12a, 14a within the tub 10 for mounting a light emitting device, and contacts 12b, 14b for subsequent mounting of the tub 10 with light emitting device onto a printed circuit board or other fixture. Although only two leadframes 16 and tubs 10 are illustrated, a molded frame array 40 may include hundreds of leadframes 16 and tubs 10.

FIG. 7B illustrates two light emitting modules 38. In this example embodiment, a light emitting device 20, which may be a thin-film device with minimal inherent structural support, is situated on a submount 30, which provides the necessary structural support to the light emitting device 20. Other devices, such as an ESD protection device 26 may also be 5 situated on the submount 30. A protective coating 28 may be provided upon the submount 30. Conductors 24 through the submount 30 couple the light emitting device 20 to the contacts 12 and 14 in the leadframe 16.

The tub 10 may be subsequently filled with an encapsulant. Optionally, the encapsulant, or the protective coating 28, or the light emitting device 20, may include a 10 wavelength conversion material, such as phosphor, that absorbs some or all of the emitted light and emits light at a different wavelength. The interior walls 15 of the tub 10 may be reflective, to redirect light toward the outside of the tub 10.

Upon completion, the individual light emitting modules 38 formed by the frame array 40 are singulated by slicing along the line 36. The individualized light emitting modules 38 15 include side surfaces 35 that facilitate the picking and placing of the light emitting module 38, but consume significantly more volume than the light-producing element 20, and introduces a substantially larger footprint compared to the surface area of the light-producing element 20. This substantial volume and footprint 'overhead' limits the use of such a light emitting module in applications such as flash or illumination elements for portable devices, 20 such as smart phones and the like.

SUMMARY OF THE INVENTION

It would be advantageous to provide a less complex process for providing a light emitting module with side surfaces that facilitate handling of the light emitting module and 25 contacts opposite the light emitting surface. It would also be advantageous to provide such a light emitting module with minimal volume and footprint overhead.

To better address one or more of these concerns, in an embodiment of this invention, a hollow frame is configured to surround the periphery of a substantially self-supporting flip-chip light emitting device. The frame may be shaped to also contain a wavelength conversion 30 element above the light emitting surface of the light emitting device. The lower surface of the light emitting device, which is exposed through the hollow frame, includes contact pads coupled to the light emitting element for surface mounting the light emitting module on a printed circuit board or other fixture. The flip-chip light emitting device may include a patterned sapphire substrate (PSS) upon which the light emitting element is grown, the

patterned surface providing enhanced light extraction from the light emitting element, through the patterned sapphire growth substrate.

BRIEF DESCRIPTION OF THE DRAWINGS

5 The invention is explained in further detail, and by way of example, with reference to the accompanying drawings wherein:

FIGs. 1A-1F illustrate an example light emitting module that includes a hollow frame that surrounds the periphery of a light emitting device with optional wavelength conversion element.

10 FIGs. 2A-2F illustrate other example interior profiles for the hollow frame.

FIGs. 3A-3D illustrate examples of other hollow frame structures.

FIG. 4 illustrates an example hollow frame array structure.

FIGs. 5A-5B illustrate an example light emitting module that includes a plurality of light emitting diodes within open cavities in a frame element.

15 FIG. 6 illustrates an example alternative assembly method.

FIGs. 7A-7B illustrate an example prior art light emitting module comprising a support frame with leadframe contacts.

FIGs 8A-8C illustrate an example self-supporting light emitting die.

Throughout the drawings, the same reference numerals indicate similar or corresponding features or functions. The drawings are included for illustrative purposes and are not intended to limit the scope of the invention.

DETAILED DESCRIPTION

In the following description, for purposes of explanation rather than limitation, 25 specific details are set forth such as the particular architecture, interfaces, techniques, etc., in order to provide a thorough understanding of the concepts of the invention. However, it will be apparent to those skilled in the art that the present invention may be practiced in other embodiments, which depart from these specific details. In like manner, the text of this description is directed to the example embodiments as illustrated in the Figures, and is not 30 intended to limit the claimed invention beyond the limits expressly included in the claims. For purposes of simplicity and clarity, detailed descriptions of well-known devices, circuits, and methods are omitted so as not to obscure the description of the present invention with unnecessary detail.

Thick, self-supporting light emitting devices are now commonly available. FIGs. 8A-8C illustrate an example of several self-supporting light emitting devices 100. FIG. 8A illustrates a substrate 110, upon which semiconductor layers are formed/grown to create multiple light emitting devices 100. The substrate 110 is commonly sapphire or glass, and the 5 light emitting element 120 of each device 100 commonly includes an active region sandwiched between an n-type semiconductor layer and a p-type semiconductor layer. Pads 130 provide contact to the n and p layers, such that when current flows between these layers, light is emitted from the active region. Gallium nitride (GaN), doped to provide the n-type and p-type semiconductors, is commonly used as the semiconductor forming the light 10 emitting element 120.

The substrate 110 may be sliced/diced (dashed lines in FIG. 8A) to provide singular light emitting chips 100. In operation, the individual light emitting chips are typically oriented in a 'flip-chip' configuration as illustrated in FIG. 8B, with the contact pads 130 on the nominal 'bottom' of the chip 100, as illustrated in FIG. 8C. Light is emitted primarily 15 from the 'top' surface 125 of the light emitting element 120, into the substrate 110, and subsequently emitted from the top 115 and side 116 surfaces of the substrate 110. Some light may also be emitted from the side surface 126 of the light emitting element 120.

To increase the light extraction efficiency from the light emitting element 120 into the substrate 110, the interface between the substrate 110 and light emitting element 120 may be 20 'roughened' to reduce the likelihood of total internal reflection (TIR) at the interface. Commonly, the substrate 110 is a Patterned Sapphire Substrate (PSS) upon which the light emitting element 120 is grown, the pattern on the sapphire substrate creating the aforementioned 'roughened' interface between the substrate 110 and the light emitting element 120.

25 Although the light emitting device 100 is substantially self-supporting, it generally may be 'packaged' to withstand subsequent handling as it undergoes the various processes during assembly of the final product, such as the assembly of a printed circuit board and other fixtures. As in the example of FIGs. 7A-7B, the chips 100 may be placed on a strip or array of leadframes, with the pads 130 soldered to conductors in the leadframe that enable 30 external connection to each of the light emitting chip 100. The array of leadframes is processed to encapsulate each of the light emitting chips, or sets of light emitting chips, then singulated to provide individual encapsulated ('packaged') light emitting devices.

The encapsulation serves multiple purposes; the encapsulant protects the light emitting chip from the environment, and may be formed to provide one or more particular optical functions. For example, the encapsulant may include wavelength conversion material, such as phosphor, that converts some or all of the light emitted from the light emitting chip 5 100 into light of a different wavelength. The encapsulant may also be shaped to form a lens that provides a desired light output pattern. Optionally, the encapsulant may be placed within a well formed by reflective walls, such as the tub 10 of the light emitting module 38 in FIG. 7B.

10 FIGs. 1A-1F illustrate a packaging process that eliminates the need for a leadframe and provides for minimal volume and footprint overhead.

15 FIG. 1A illustrates an example hollow frame 210, within which a light emitting chip may be placed. Optionally, the interior walls 215 of the frame 210 may be reflective, using, for example, a coating of silver (Ag) or other reflective material. The shape and size of the opening in the frame 210 may correspond to the shape and size of the light emitting chip 100, as illustrated in FIG. 1B. Preferably, the light emitting chip 100 is pre-tested, so that only operational chips 100 are packaged.

20 Although FIGs. 1A-1F illustrate a single frame 210, an array 400 of frames 210, such as illustrated in FIG. 4, may be provided to enable simultaneous assembly and testing of multiple frames 210.

25 Before assembly, the array 400 may be processed to apply a reflective material upon the walls 215 within each frame 210. Depending on the intended application for the packaged device, the entire array 400 may be spray coated with a reflective material, or the upper surface of the frames 210 may be masked while the reflective material is applied to the interior walls. Other techniques may be used to apply the reflective coating, including dipping the array 400 in a vat of reflective material. In some embodiments, the material of the frame 210 may be inherently reflective, eliminating the need to apply a separate reflective coating.

After assembly, the array of frames is sliced/diced along cutting lines 410 to provide singulated packaged light emitting devices 200, as illustrated in FIGs. 1A-1F. Although not illustrated, the array 400 may include features between the frames 210 that facilitate such slicing/dicing. Preferably, the material selected for the array 400 is easily sliced using 5 mechanical or laser cutting processes. One of skill in the art will recognize that the frame may be sliced to provide modules that include multiple light emitting chips, as detailed further below with respect to FIGs. 5A-5B.

As shown in FIG. 1B, a removable sheet 280, such as an adhesive strip, may cover the lower surface of the frame 210, to provide a surface upon which the chip 100 may be 10 supported during the assembly process. As illustrated in FIG. 1C, the sheet 280 may be flexible, allowing the chip 100 to be placed within the frame 210 such that the contact pads 130 extend slightly below the frame 210, if desired. Optionally, the pads 130 may be flush with the lower surface of the frame 210.

The chip 100 may be secured within the frame 210 using any of a variety of 15 techniques. If the frame 210 is pliable, the opening in the frame may be sized to provide a friction fit. Alternatively, the opening in the frame may be slightly larger than the chip 100, and the walls 215 on the frame 210 or the sidewalls of the chip 100 may be coated with an adhesive before the chip 100 is inserted into the frame 210. If the adhesive is reflective, the need to provide a reflective surface on the walls 215 may be eliminated.

20 In some embodiments, the frame 210 may be a shrinkable material, such as a heat-shrink material that is shrunk after the light emitting chip 100 (and optional cap 230, discussed below) is inserted into the frame 210. In some embodiments, laser slicing of the frame 400 (in FIG. 4) into individual packaged devices 200 may provide the energy to cause the material to shrink. Other techniques for securing the chip 100 into the frame 210 will be 25 evident to one of skill in the art in view of this disclosure.

As shown in FIG. 1D, a cap 230 may be provided to cover the light emitting chip 100. This cap receives the light from the light emitting surface 115 of the chip, and may provide for a desired optical effect before the light is emitted from the surface 235 of the cap 230. For example, the cap may include wavelength conversion material that converts some or all of 30 the light from the light emitting chip 100 into light of one or more different wavelength to achieve a desired color point. Similarly, although illustrated as a parallelepiped, the cap 230 may be shaped to form a lens that provides a desired light output pattern, such as a collimating lens.

The light emitting surface 115 of the light emitting chip 100, and the light emitting surface 235 of the cap 230 may be roughened or patterned to enhance the light emission efficiency by reducing the likelihood of total internal reflection (TIR) at these surfaces. Optionally, an interface material may be applied at the interface between the light emitting chip 100 and the cap 230, or between these elements and the reflective walls 215 to enhance the optical coupling between these elements.

As shown in FIG. 1E, frame 210 may be sized to contain the cap 230 as well as the chip 100, and the reflective walls 215 serve to redirect light that may escape from the sides of the cap 230. The placement of the cap 230 in the frame 210 provides for a fine alignment of the cap 230 and the light emitting chip 100, which is often required to achieve a particular light output pattern that is consistent among the packaged devices 200. The cap 230 may be attached to the frame 210 using any of the attachment methods used for attaching the chip 100 to the frame 210.

After attaching the chip 100 and optional cap 230 to the frame 210, the packaged device 200 will have a light emitting surface 235 (or surface 115 if the optional cap 230 is not included) as the 'upper' surface, and contact pads 130 on the 'lower' surface of the device 200. In this manner, a packaged light emitting device 200 is formed without the use of a leadframe or a submount.

As shown in FIG. 1E and FIG. 1F, the packaged light emitting device 200 has a minimal amount of volume and footprint overhead, as well as a low profile, and is particularly well suited for use in small, handheld devices, such as phones and the like. The reduced volume and footprint overhead may also be beneficial for other applications, such as automotive, illumination, and display applications.

One of skill in the art will recognize that the frame 210 may include other features that facilitate the assembly process. For example, the opening in the frame 210 may include features that allow air bubbles to escape the frame 210 when the chip 100 and cap 230 are inserted, common in the art. In like manner, the frame 210 may include a material such as TiO₂ or silicone between the cap 230 and the reflective walls 215 to reduce or eliminate "blue ring", which may be caused when blue light exits the edges of the light emitting chip 100, without passing through a cap that includes wavelength converting material.

One of skill in the art will also recognize that the frame 210 may provide other functions in addition to protecting the light emitting chip 100 and providing a package with minimal volume and footprint overhead. For example, the material for frame 210 may be selected to provide high thermal conductivity, thereby extending the life of the device 200 by 5 efficiently dissipating the heat generated by the light emitting chip 100, and the cap 230 if it includes a wavelength conversion material.

Although the invention is presented above using an example process wherein the light emitting chip 100 is inserted first, then the cap 230, one of skill in the art will recognize that the cap 230 may be inserted into the frame 210 first, followed by the light emitting chip; or, 10 the cap 230 and light emitting chip 100 may be attached to each other, then inserted into the frame 210.

FIGs. 2A-2F illustrate other example interior profiles for hollow frames 201-206.

As shown in FIG. 2A, example frame 201 includes a 'step' feature 211 that serves to 15 control the vertical arrangement of the light emitting chip 100 and the cap 230. In particular, the step 211 may be situated to control the extent to which the contacts of the light emitting chip 100 extend beyond the lower surface of the frame 201 when the cap 230 is inserted.

As shown in FIG. 2B, frame 202 includes a sloped wall segment 212 that facilitates the insertion of the light emitting chip 100 into the frame 210, and also serves to re-direct any 20 side emitted light from the cap 230 away from the light emitting chip 100.

As shown in FIG. 2C, frame 203 the sloped wall segment 212 extends to the lower surface of the frame 210. Also illustrated in FIG. 2C, the cap 230' is created by dispensing a material in liquid or paste form into the cavity formed by the opening in the frame 210, then solidifying the material. For example, the material may be silicone with optional wavelength 25 conversion material that is applied in liquid form then cured to form a rigid cap 230'. In such an embodiment, the liquid or paste material may serve as the adhesive that attaches the chip 100 to the frame 210.

As shown in FIG. 2D, frame 204 also includes a cap 230' that is dispensed into the cavity, but in this example, the walls 213 are not sloped.

As shown in FIG. 2E, frame 205 includes an intermediate optical element 240 situated between the chip 100 and the cap 230. This optical element 240 may provide a particular optical effect, or it may enhance the light extraction efficiency by providing an index of refraction that is between the indices of refraction of the chip 100 and the cap 230.

5 This optical element 240 is illustrated as a pre-formed parallelepiped, but it may be of any shape, and may be dispensed in liquid or paste form then solidified (not illustrated). As in the example of a cap 230' that is dispensed within the cavity of the frame 210, the dispensed optical element 240 may serve to attach the chip 100 to the frame 210. There may be a small gap 238 between the elements 230, 240, 100 and the frame 210 or one or more of the
10 elements 230, 240, 100 may fit tightly inside frame 210.

One of skill in the art will recognize that a variety of forming and assembling techniques may be used to achieve a desired packaging construct. Some or all of the techniques shown in FIGs 2A-2E may be combined. As shown in FIG. 2F, example frame 206, includes sloped wall segments 212, a pre-formed intermediate optical element 240, and
15 a cap 230' that is dispensed in liquid or paste form and serves to attach the chip 100 and optical element 240 to the frame 210.

Although the opening in the frame 210 is preferably shaped to correspond to the shape of the light emitting chip, which is typically rectilinear, to minimize the footprint of the
20 resultant device 200 (or 201-206), other shaped openings may be provided. For example, because it is generally simpler to drill or bore circular holes through a material, cylindric or conic holes may be provided as illustrated in FIGs. 3A-3D.

As shown in FIG. 3A and in cross section in FIG. 3B, example device 301 includes frame 210 and a cylindrical hole in which a rectilinear light emitting chip 100 and a circular cap 230 are placed. In this example embodiment, the size of the hole is such that it circumscribes the light emitting chip 100 i.e. the diagonal of the light emitting chip 100 is less than or equal to the diameter of the cylindrical hole. In this manner, the extra footprint overhead caused by a cylindrical hole instead of a rectilinear hole is minimized. In some embodiments, the light emitting chip 100 is pre-attached to the circular cap 230, then the pair
25 of elements 100, 230 are placed within the cylindrical frame 210. The space between the chip 100 and the cylindrical walls 315 of the frame 210 may be filled with a reflective material, such as a dielectric paste that is subsequently hardened.
30

As shown in FIG. 3C and in cross section in FIG. 3D, example device 302, the frame 210 includes a conic hole, with the size of the opening at the bottom surface of the frame 210 circumscribing the chip 100. In this example, the cap 230' is dispensed within the conic cavity in liquid or paste form, then hardened, and may serve to attach the chip 100 to the 5 frame 210. Optionally, the walls 325 may be reflective.

As noted above, the frame 400 of FIG. 4 may be configured and sliced/diced such that each singulated module includes a plurality of light emitting chips. Multiple chip modules are commonly used for high power applications, such as automotive lighting modules.

FIGs. 5A-5B illustrates example light emitting modules that includes a plurality of light emitting diodes within the light emitting module. In these examples, the light emitting modules 501, 502 comprise one or more rows of four light emitting chips 100 with caps 230. That is, the modules 501, 502 may be a 'linear' light strip comprising four chips 100, or an 'MxN' array of chips 100. The variables M or N may be 4 or any other suitable integer.

In the example automotive lighting applications, the placement of the chips 100 within defined cavities in the frame provides for a simple method of achieving the high alignment precision that may be required for certain automotive lamps.

FIG. 5A illustrates a frame 510 in which each light emitting chip 100 is inserted first, followed by a corresponding cap 230.

FIG. 5B illustrates the same frame 510 in which the cap 230 is inserted first, followed by the 'face-down' insertion of the light emitting chip 100. This method provides an advantage in that the caps 230 provide a level surface with the frame 510, as contrast to the protruding contacts of the light emitting chip 100. This may simplify the assembly process, and provide a wider range of options with regard to the choice of materials used as the 25 removable sheet (280 in FIG. 1B) that provides a surface upon which the first inserted element is supported during the assembly process. That is, the requirement that the sheet 280 be stretchable to accommodate the contacts 130, as illustrated in FIG. 1C, is eliminated.

FIG. 6 illustrates an example alternative assembly method. In this example, the caps 30 230 are placed in a frame 610, and the light emitting chips 100 are attached to a substrate 650, such as a printed circuit board. After attaching the light emitting chips 100 to the substrate 650, the frame 610 with caps 230 is placed upon the substrate.

Although FIG. 6 illustrates a multi-chip module 601, one of skill in the art will recognize that single chip modules may be assembled in the same way.

While the invention has been illustrated and described in detail in the drawings and foregoing description, such illustration and description are to be considered illustrative or exemplary and not restrictive; the invention is not limited to the disclosed embodiments.

5 For example, although the figures illustrate a frame 210 that encloses all of the elements 100, 230, 240 it is possible to operate the invention in an embodiment wherein some or all of the cap 230 extends above the frame 210 and is shaped to provide a particular optical effect. For example, the cap 230 may be mushroom-shaped, the upper portion providing a wider distribution of the emitted light. In like manner, some or all of the optical 10 element 240 may extend above the frame 210, and the cap 230 may encapsulate the portion of the optical element 240 extending beyond the frame 210. The cap 230 may also be omitted and the optical element 240 may be shaped to provide the aforementioned desired optical effect.

Other variations to the disclosed embodiments can be understood and effected by 15 those skilled in the art in practicing the claimed invention, from a study of the drawings, the disclosure, and the appended claims. In the claims, the word "comprising" does not exclude other elements or steps, and the indefinite article "a" or "an" does not exclude a plurality. The mere fact that certain measures are recited in mutually different dependent claims does not indicate that a combination of these measures cannot be used to advantage. Any reference 20 signs in the claims should not be construed as limiting the scope.

CLAIMS:

1. A light emitting device comprising:

a substrate upon which a light emitting element is situated, wherein:

the light emitting element comprises an active region that is sandwiched

between an n-type semiconductor and a p-type semiconductor, and

5 light emitted from a first light emitting surface of the light emitting element is emitted from at least one light emitting surface of the substrate;

contact pads that are formed on a second surface of the light emitting element that is opposite the first light emitting surface, wherein at least a first contact pad is connected to the n-type semiconductor and a second contact pad is connected to the p-type semiconductor;

10 and

a frame arranged to surround the substrate and light emitting element such that at least a portion of the contact pads and at least a portion of the substrate are not obstructed by the frame.

15 2. The device of claim 1, wherein the substrate is a growth substrate upon which the light emitting element is formed.

3. The device of claim 1, wherein the substrate comprises a patterned sapphire substrate.

20 4. The device of claim 1, including a cap that is surrounded by the frame, receives the light from the substrate, and emits light from a light emitting surface of the cap.

25 5. The device of claim 4, wherein the cap includes a wavelength conversion material that absorbs some or all of the light from the light emitting element and converts it to light of a different wavelength.

6. The device of claim 4, wherein the frame includes reflective inner walls adjacent the substrate and the cap.

5 7. The device of claim 1, wherein the frame surrounds a plurality of light emitting elements.

8. A frame structure comprising:

a frame that includes a plurality of openings,

a plurality of light emitting chips that are situated in the plurality of openings,

10 each of the light emitting chips including:

a substrate upon which a light emitting element is situated and through which light from the light emitting element is emitted, and

contact pads that are formed on a first surface of the light emitting element that is opposite the substrate;

15 each of the openings including walls that surround the light emitting chip within the opening and allow external contact directly to the contact pads and allow light from the substrate to exit the frame structure.

9. The frame structure of claim 8, including a plurality of caps that are situated in the

20 plurality of openings, each cap being optically coupled to the substrate of the light emitting chip within at least one of the openings.

10. The frame structure of claim 9, wherein each cap includes a wavelength conversion material.

25

11. The frame structure of claim 10, wherein each cap is a pre-formed element that is placed within at least one of the openings.

12. The frame structure of claim 11, wherein each opening includes a step feature that 30 introduces a restriction in the opening, wherein the restriction is arranged to control a location of the cap and the light emitting chip within the opening.

13. A method of forming light emitting devices comprising:

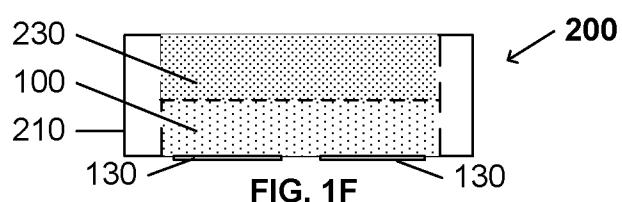
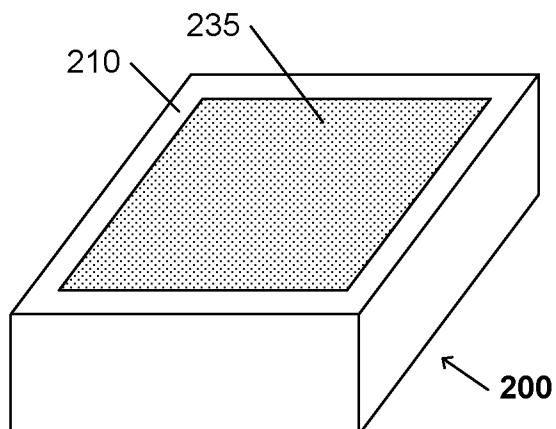
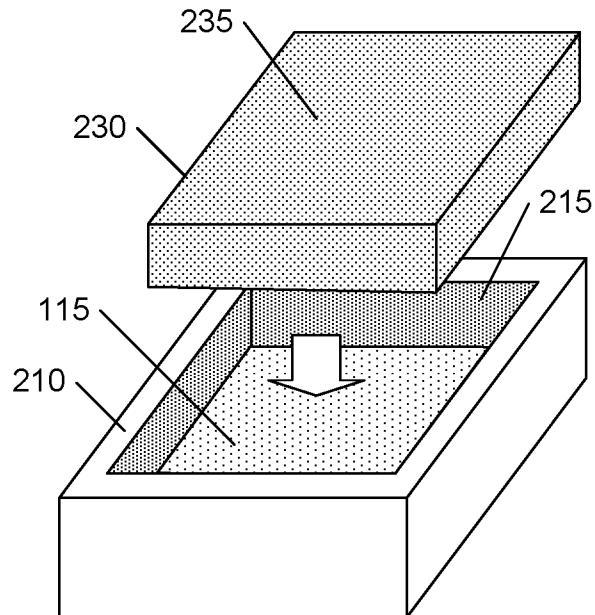
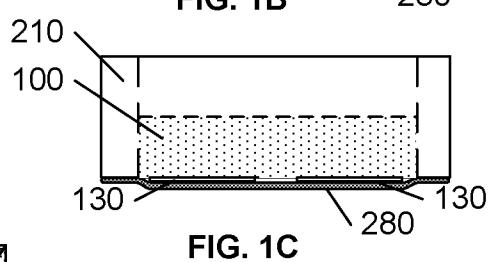
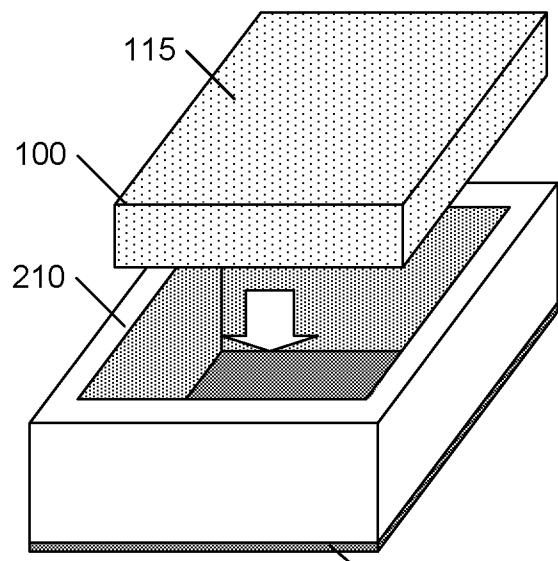
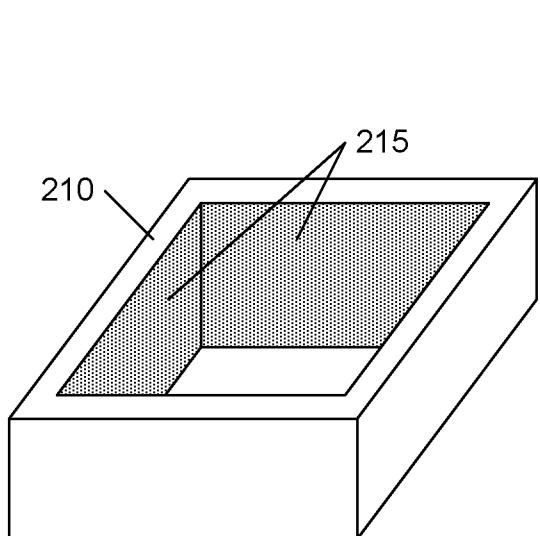
providing a frame structure that includes a plurality of openings;
placing a light emitting chip within each of the openings;
each of the light emitting chips including:

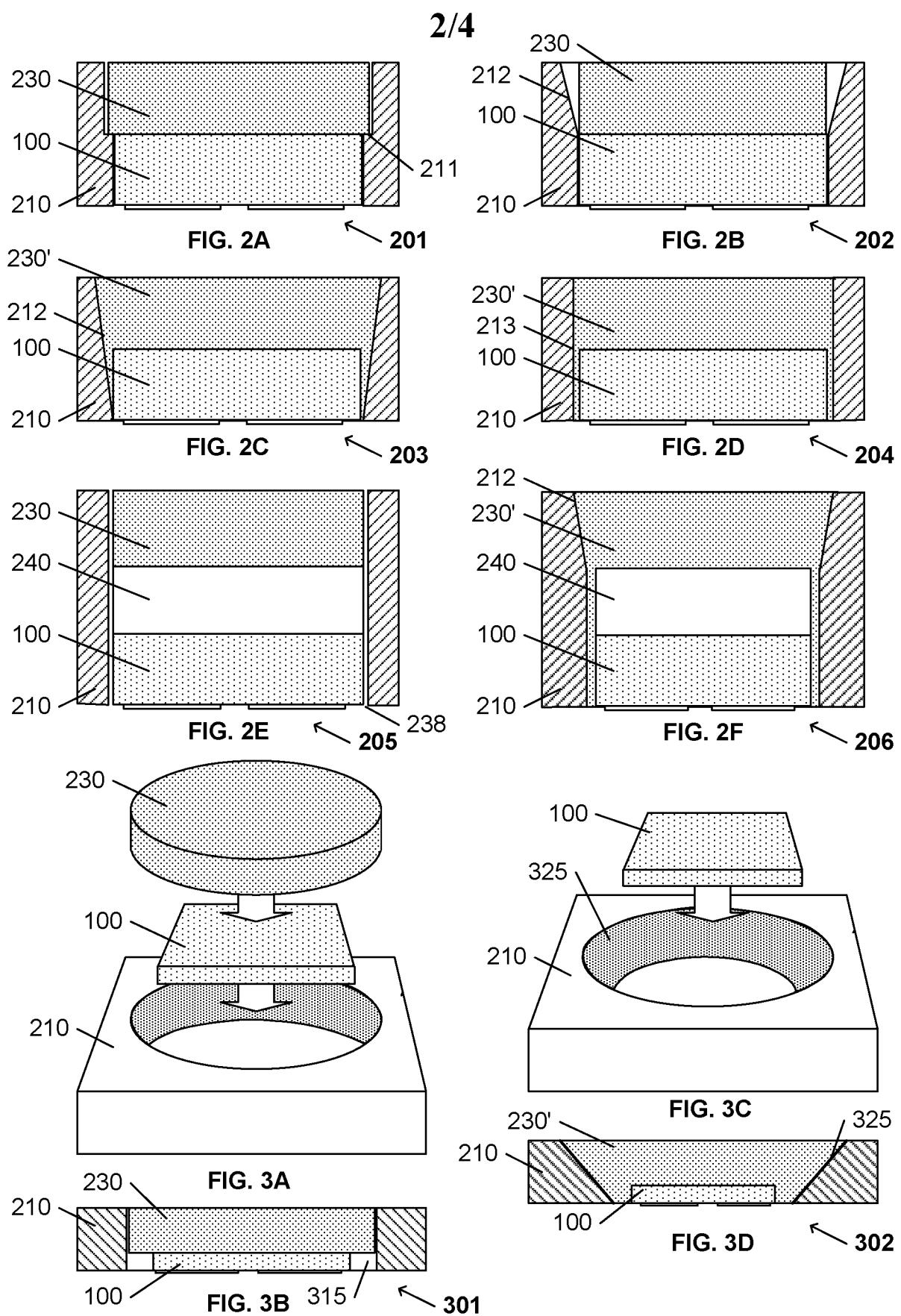
5 a substrate upon which a light emitting element is situated and through which
light from the light emitting element is emitted, and
 contact pads that are formed on a surface of the light emitting element that is
 opposite the substrate; and
 slicing the frame structure to provide individual light emitting modules that include
10 one or more light emitting devices that are enclosed within walls of each opening that
 surround the light emitting chip within the opening and allow external contact directly to the
 contact pads and allow light from the substrate to exit the frame structure.

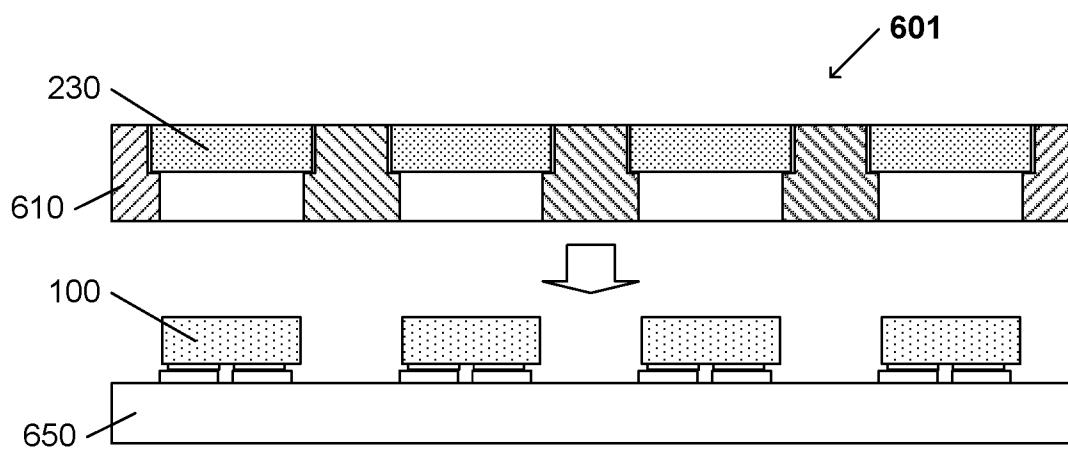
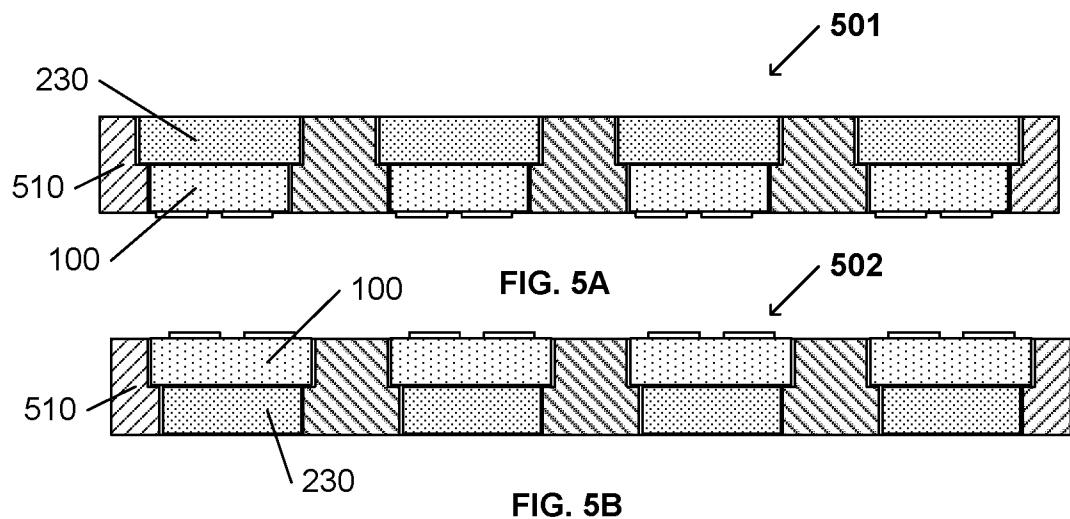
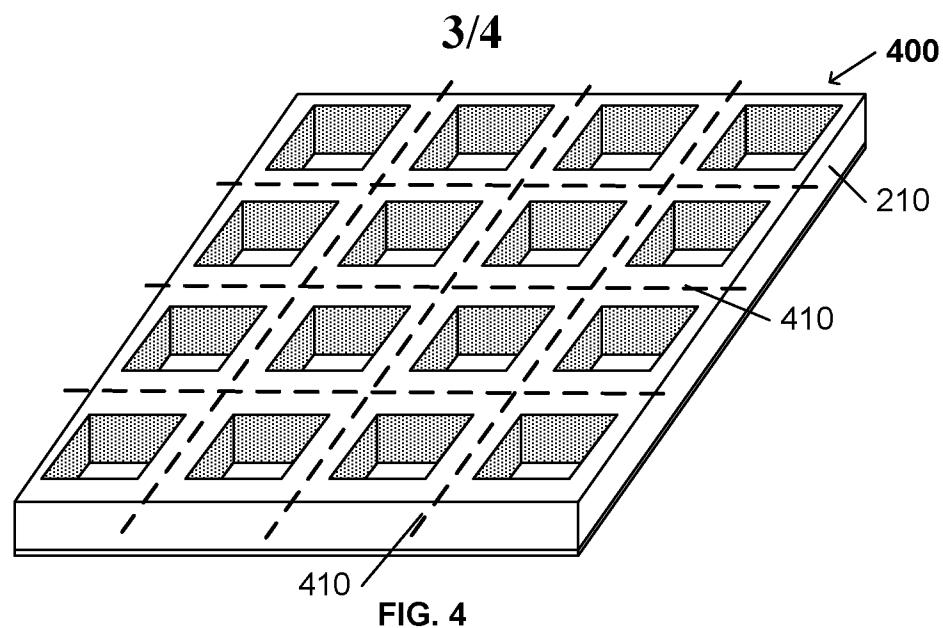
14. The method of claim 13, wherein the substrate is a growth substrate upon which each of
15 the light emitting elements is formed.

15. The method of claim 13, including placing a wavelength conversion element within at
 least one of the openings.

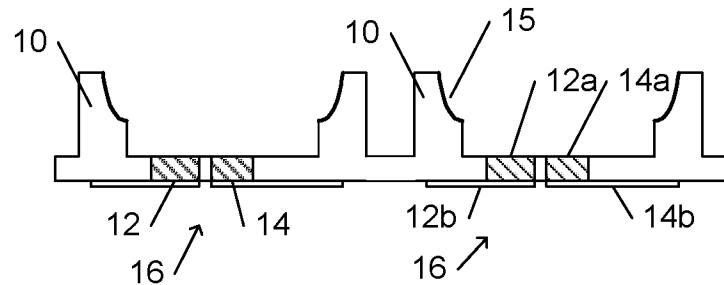
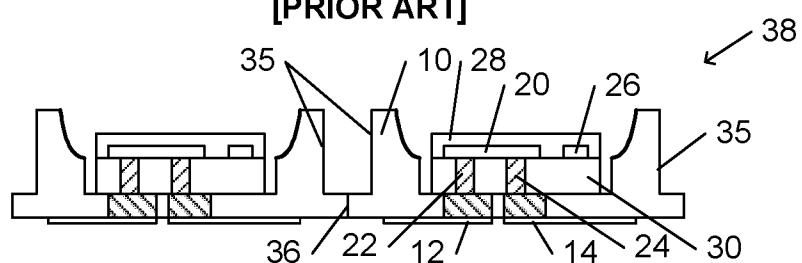
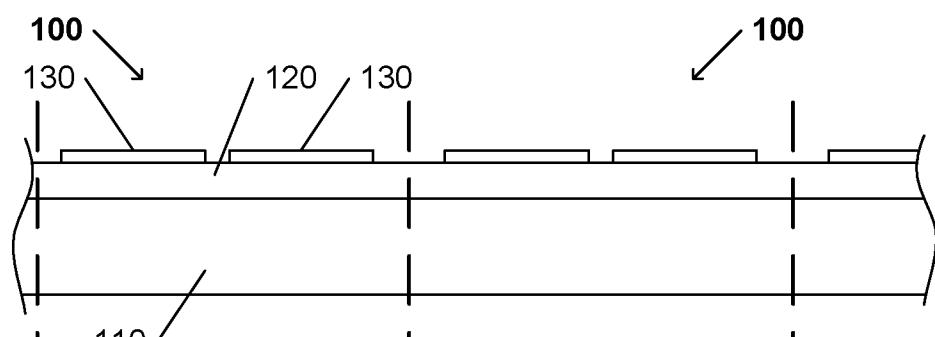
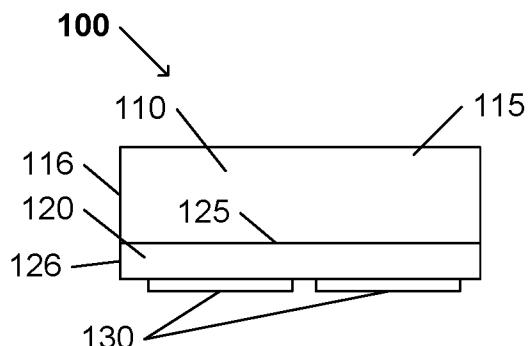
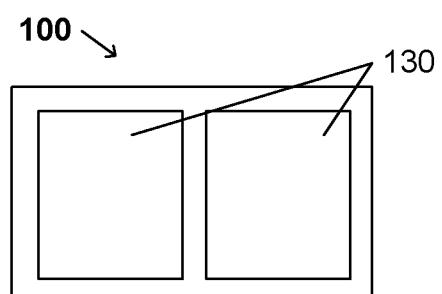
1/4







4/4

FIG. 7A
[PRIOR ART]FIG. 7B
[PRIOR ART]FIG. 8A
[PRIOR ART]FIG. 8B
[PRIOR ART]FIG. 8C
[PRIOR ART]

INTERNATIONAL SEARCH REPORT

International application No
PCT/IB2014/064106

A. CLASSIFICATION OF SUBJECT MATTER

INV.	H01L25/075	H01L33/60	H01L33/00
ADD.	H01L33/50	H01L33/58	H01L33/48

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2013/032842 A1 (PARK JONG KIL [KR] ET AL) 7 February 2013 (2013-02-07)	1-3,7,8, 13-15
Y	paragraph [0041] - paragraph [0093]; figures 1A-15	4-6,9-12
X	----- DE 10 2009 036621 A1 (OSRAM OPTO SEMICONDUCTORS GMBH [DE]) 10 February 2011 (2011-02-10)	1-3,7
A	paragraph [0005] - paragraph [0055]; figures 1A-5	4-6,8-15
X	----- WO 2007/147278 A2 (STAUFERT GERHARD [CH]) 27 December 2007 (2007-12-27)	1,2
A	page 23, line 5 - page 32, line 2; figures 1a-3	3-15
	----- -/-	

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier application or patent but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
9 December 2014	16/12/2014
Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Authorized officer Albrecht, Claus

INTERNATIONAL SEARCH REPORT

International application No
PCT/IB2014/064106

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	EP 1 408 559 A2 (CITIZEN ELECTRONICS [JP]) 14 April 2004 (2004-04-14)	4-6,9-12
A	paragraph [0032] - paragraph [0075]; figures 1-24 -----	1-3,7,8, 13-15
A	US 2013/187178 A1 (TISCHLER MICHAEL A [US]) 25 July 2013 (2013-07-25) paragraph [0101] - paragraph [0255]; figures 1-62 -----	1-15
A	EP 0 747 948 A2 (UNIV CALIFORNIA [US]) 11 December 1996 (1996-12-11) column 6, line 43 - column 27, line 12; figures 1-20 -----	1-15
A	WO 02/17405 A1 (OSRAM OPTO SEMICONDUCTORS GMBH [DE]; BOGNER GEORG [DE]; BRUNNER HERBER) 28 February 2002 (2002-02-28) page 3, line 32 - page 10, line 23; figures 1-6 -----	1-15
A	WO 2013/056927 A1 (OSRAM GMBH [DE]; KRAEUTER GERTRUD [DE]; BARCHMANN BERND [DE]; BIEBERSD) 25 April 2013 (2013-04-25) page 7, line 5 - page 9, line 9; figures 1-2 -----	1-15
A	DE 10 2010 025319 A1 (OSRAM OPTO SEMICONDUCTORS GMBH [DE]) 29 December 2011 (2011-12-29) paragraph [0003] - paragraph [0065]; figures 1A-3B -----	1-15
A	US 2005/269587 A1 (LOH BAN P [US] ET AL) 8 December 2005 (2005-12-08) paragraph [0018] - paragraph [0049]; figures 1A-4 -----	1-15

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/IB2014/064106

Patent document cited in search report	Publication date	Patent family member(s)		Publication date
US 2013032842	A1 07-02-2013	DE 102012213581	A1	07-02-2013
		KR 101219106	B1	11-01-2013
		US 2013032842	A1	07-02-2013
DE 102009036621	A1 10-02-2011	CN 102473814	A	23-05-2012
		DE 102009036621	A1	10-02-2011
		EP 2462633	A1	13-06-2012
		JP 2013501368	A	10-01-2013
		KR 20120056269	A	01-06-2012
		TW 201115792	A	01-05-2011
		US 2012119233	A1	17-05-2012
		US 2014284645	A1	25-09-2014
		WO 2011015449	A1	10-02-2011
WO 2007147278	A2 27-12-2007	NONE		
EP 1408559	A2 14-04-2004	CN 1497747	A	19-05-2004
		EP 1408559	A2	14-04-2004
		JP 4280050	B2	17-06-2009
		JP 2004128424	A	22-04-2004
		KR 20040031661	A	13-04-2004
		TW 1236159	B	11-07-2005
		US 2004070338	A1	15-04-2004
US 2013187178	A1 25-07-2013	EP 2807673	A2	03-12-2014
		US 8785960	B1	22-07-2014
		US 2013187174	A1	25-07-2013
		US 2013187178	A1	25-07-2013
		US 2013187540	A1	25-07-2013
		US 2014061705	A1	06-03-2014
		US 2014077686	A1	20-03-2014
		US 2014227812	A1	14-08-2014
		US 2014246747	A1	04-09-2014
		WO 2013112435	A1	01-08-2013
		WO 2013112691	A2	01-08-2013
EP 0747948	A2 11-12-1996	AU 708552	B2	05-08-1999
		AU 5591796	A	19-12-1996
		CA 2177219	A1	08-12-1996
		CN 1147153	A	09-04-1997
		CN 1244027	A	09-02-2000
		EP 0747948	A2	11-12-1996
		JP H09120943	A	06-05-1997
		US 5824186	A	20-10-1998
WO 0217405	A1 28-02-2002	CN 1471734	A	28-01-2004
		DE 10041328	A1	14-03-2002
		EP 1312124	A1	21-05-2003
		JP 5064639	B2	31-10-2012
		JP 2004507114	A	04-03-2004
		TW 501276	B	01-09-2002
		US 2004047151	A1	11-03-2004
		WO 0217405	A1	28-02-2002
WO 2013056927	A1 25-04-2013	NONE		
DE 102010025319	A1 29-12-2011	DE 102010025319	A1	29-12-2011
		JP 2013536568	A	19-09-2013

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/IB2014/064106

Patent document cited in search report	Publication date	Patent family member(s)		Publication date
		US	2013240935 A1	19-09-2013
		WO	2012000943 A1	05-01-2012

US 2005269587	A1	08-12-2005	AT 420464 T	15-01-2009
			EP 1756879 A2	28-02-2007
			EP 2075856 A2	01-07-2009
			EP 2287926 A2	23-02-2011
			EP 2287927 A2	23-02-2011
			EP 2287928 A2	23-02-2011
			EP 2287929 A2	23-02-2011
			EP 2290713 A1	02-03-2011
			JP 5596901 B2	24-09-2014
			JP 2008502159 A	24-01-2008
			JP 2011176347 A	08-09-2011
			MY 139974 A	30-11-2009
			TW I382561 B	11-01-2013
			TW 201234644 A	16-08-2012
			US 2005269587 A1	08-12-2005
			US 2008283861 A1	20-11-2008
			US 2013323865 A1	05-12-2013
			WO 2005119707 A2	15-12-2005
