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- (71) Applicant: SANDISK ENTERPRISE IP LLC [US/US];
951 Sandisk Drive, Milpitas, CA 95035 (US).
- (72) Inventors: DEAN, David; 951 Sandisk Drive, Milpitas,
CA 95035 (US). ELLIS, Robert, W.; 951 Sandisk Drive,
Milpitas, CA 95035 (US).
- (74) Agents: WILLIAMS, Gary S. et al.; Morgan Lewis &
Bockius LLP, 2 Palo Alto Square, 3000 El Camino Real,
Suite 700, Palo Alto, CA 94306 (US).

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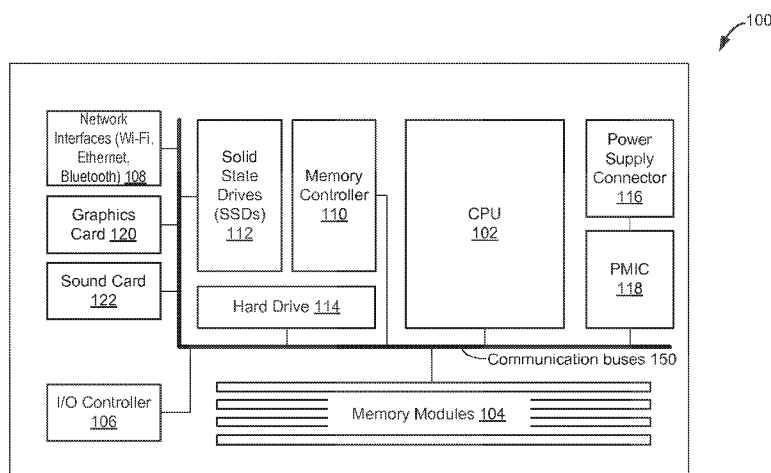


FIG. 1

(57) **Abstract:** Various embodiments described herein include systems, methods and/or devices used to dissipate heat generated by electronic components in an electronic system (e.g., a memory system that includes closely spaced memory modules). In one aspect, an electronic assembly includes a first circuit board with one or more heat generating components coupled thereto. The electronic assembly further includes a second circuit board with one or more heat sensitive components coupled thereto. The electronic assembly also includes a thermal barrier interconnect. The thermal barrier interconnect electrically couples the first circuit board to the second circuit board. In some embodiments, thermal barrier interconnect is a flexible interconnect with a lower thermal conductivity than the first circuit board and the second circuit board. The thermal barrier interconnect forms a thermal barrier between the first and second circuit boards which protects the heat sensitive components from the heat generating components.

THERMAL ISOLATION TECHNIQUES

TECHNICAL FIELD

[0001] The disclosed embodiments relate generally to heat management, and in particular, to dissipating heat generated by electronic components in electronic systems.

BACKGROUND

[0002] Electronics, such as processors or memory, generate heat during operation. If left unchecked, this heat can reduce system performance and even lead to partial or complete system failure. As such, many existing technologies attempt to remove or dissipate heat through the use of heat sinks, cooling fans, etc.

[0003] While these technologies may be effective for cooling a single electronic component that is not located near other sources of heat, these technologies fall short when it comes to more complex systems and higher density systems, such as solid state drives (SSDs), dual in-line memory modules (DIMMs), and small outline-DIMMs, all of which utilize memory cells to store data as an electrical charge or voltage.

[0004] Existing cooling systems for such systems typically include multiple heat sinks and high-speed fans. These cooling systems are noisy; add significant expense to the system; increase the overall energy consumption of these systems; and decrease system efficiency. Moreover, existing cooling systems do not always alleviate localized hot-spots that form within the systems, which in turn shortens the life of the individual components within the system.

[0005] In the absence of efficient heat dissipation mechanisms, the increased heat can ultimately lead to reduced performance or failure of either individual memory cells of a memory module or the entire memory module.

[0006] Some systems include circuitry that is also sensitive to high temperatures. For example, Solid State Disk (SSD) drives typically contain support circuitry that during normal operation generates a large amount of heat. However, the NAND flash memory used in these designs has a temperature sensitivity that reduces the life of the drive.

[0007] A common technique for removing heat from the NAND in an SSD is to integrate a heat sink design in the case design for the drive. However, this presents a problem when the form factor for the SSD does not include a case, e.g., PCIe or embedded SSDs that

use a PCBA (printed circuit board assembly) and no case. In other words, many of the heat sinks used for SSDs are an integral part of the case design, and when there is no case, the heat removal is handled by passing large volumes of air over the heat generating components. However, as mentioned above, these cooling systems are noisy; add significant expense to the system; increase the overall energy consumption of these systems; decrease system efficiency, and do not always alleviate localized hot-spots that form within the systems.

[0008] In light of these and other issues, it would be desirable to provide a system and method for more effectively cooling electronic components, especially those found in systems that contain multiple heat generating components.

SUMMARY

[0009] According to some embodiments there is provided an electronic assembly for dissipating heat. The electronic assembly includes a first circuit board with one or more heat generating components coupled thereto. The electronic assembly further includes a second circuit board with one or more heat sensitive components coupled thereto. The electronic assembly also includes a thermal barrier interconnect. The thermal barrier interconnect electrically couples the first circuit board to the second circuit board. In some embodiments, thermal barrier interconnect is a flexible interconnect with a lower thermal conductivity than the first circuit board and the second circuit board. The thermal barrier interconnect forms a thermal barrier between the first and second circuit boards which protects the heat sensitive components from the heat generating components.

[0010] Other embodiments include a method of manufacturing an electronic assembly for dissipating heat. A first circuit board with one or more heat generating components coupled thereto is provided. A second circuit board with one or more heat sensitive components coupled thereto is also provided. A thermal barrier interconnect is provided and it is coupled to the first circuit board to the second circuit board such that the first and second circuit board are electrically coupled to one another.

[0011] Some advantages of the embodiments describe herein are as follows. These electronic assemblies provide a means of differential temperature control at design time. They also help to reduce thermal issues on selected device in a single assembly. The flexible interconnect allows for variable form factors, shapes, designs, and/or assemblies. These electronic assemblies allow for segregation of devices or components that have different

temperature sensitivities. They increase the life of a solid state drive by increasing data retention from lower device temperatures. For instance, a NAND flash running at a lower temperature will have better data retention and therefore require less recycling of data and lengthen the drive's life. These electronic assemblies are also better at compensating for thermal expansion and contractions over a wide temperature range.

[0012] Other embodiments and advantages may be apparent to those skilled in the art in light of the descriptions and drawings in this specification.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] So that the present disclosure can be understood in greater detail, a more particular description may be had by reference to the features of various embodiments, some of which are illustrated in the appended drawings. The appended drawings, however, merely illustrate the more pertinent features of the present disclosure and are therefore not to be considered limiting, for the description may admit to other effective features.

[0014] Figure 1 is a block diagram of an exemplary system module in a typical computational device in accordance with some embodiments.

[0015] Figure 2 is a side view of an exemplary electronic assembly, in accordance with some embodiments.

[0016] Figure 3A is a side view of another exemplary electronic assembly, in accordance with some embodiments.

[0017] Figures 3B is an oblique view of the exemplary electronic assembly of Figure 3A, in accordance with some embodiments.

[0018] Figure 4 is a side view of another exemplary electronic assembly, in accordance with some embodiments.

[0019] Figure 5 illustrates an exemplary flow chart of a method for assembling and using an electronic assembly, in accordance with some embodiments.

[0020] In accordance with common practice the various features illustrated in the drawings may not be drawn to scale. Accordingly, the dimensions of the various features may be arbitrarily expanded or reduced for clarity. In addition, some of the drawings may not depict all of the components of a given system, method or device. Finally, like reference numerals may be used to denote like features throughout the specification and figures.

DETAILED DESCRIPTION

[0021] The various embodiments described herein include systems, methods and/or devices used by, or integrated into, electronic assemblies. In particular, the electronic systems, the heat sinks, and the heat dissipation method described herein facilitate dissipation of heat generated by electronic components in the electronic systems.

[0022] The embodiments described herein allow for the isolation of heat sensitive devices from heat generating devices on a PCBA (printed circuit board assembly). In some embodiments, by using a thermal barrier interconnect such as a flexible circuit board between two rigid printed circuit boards, a thermal barrier is formed. This thermal barrier is used to allow one board to operate at a reduced temperature in relation to the other board. For instance, a board with NAND flash can operate at a reduced temperature in relation to the board which contains the NAND flash controller and power supply electronics. The difference in temperatures between the boards can be as much as 20 degrees C. This means the NAND flash running at a lower temperature will have better data retention and therefore require less recycling of data (which is Write Amplification), thereby lengthening the drive life.

[0023] A thin flexible board which carries the signals does not contain the thermal mass of the rigid FR4 (or other types) board material. As such, the thin flexible board produces in a thermal barrier. In some embodiments, the thermal barrier is further enhanced from the reduction in copper planes, which are found in the rigid boards to supply power and grounds. In some embodiments, the flexible material is made from a kapton or other polyimide materials which has a lower thermal conductivity.

[0024] One example of such an electronic system is a memory system that is commonly integrated in many computers and consumer electronic devices. Memory systems frequently include closely placed memory modules that require efficient heat dissipation in order to ensure proper function and life span. Some embodiments are described herein in the context of generic electronic systems. However, one of skill in the art will recognize that the embodiments described herein are suitable for use in a memory system as well as any other appropriate electronic system that includes two or more electronic modules integrated in a limited space and which requires efficient dissipation of generated heat.

[0025] More specifically, according to some embodiments, an electronic assembly for dissipating heat is provided. The electronic assembly includes a first circuit board with one

or more heat generating components coupled thereto. The electronic assembly further includes a second circuit board with one or more heat sensitive components coupled thereto. The electronic assembly also includes a thermal barrier interconnect. The thermal barrier interconnect electrically couples the first circuit board to the second circuit board.

[0026] In some embodiments, the thermal barrier interconnect has a lower thermal conductivity than the first circuit board and the second circuit board.

[0027] In some embodiments, the thermal barrier interconnect is a flexible interconnect. In some embodiments, the flexible interconnect is made from a kapton material. In some embodiments, the flexible interconnect is made from a polyimide material.

[0028] In some embodiments, the thermal barrier interconnect has a lower thermal mass than the first and circuit boards.

[0029] In some embodiments, the thermal barrier interconnect includes power planes for CORE voltage and the I/O voltage.

[0030] In some embodiments, the thermal barrier interconnect does not include a plurality of power planes found in the first and second circuit boards.

[0031] In some embodiments, the one or more heat generating components include one or more power supply electronic components.

[0032] In some embodiments, the one or more heat generating components include a NAND flash controller.

[0033] In some embodiments, the one or more heat sensitive components include NAND flash memory.

[0034] In some embodiments, the first circuit board, the second circuit board are next to one another in a substantially common plane.

[0035] In some embodiments, first circuit board is on a first plane and the second circuit board is on a second plane substantially parallel to the first plane and separated by a space.

[0036] Some embodiments also include a fastener configured to couple the first circuit board to the second circuit board. The fastener, first circuit board, second circuit board, and thermal barrier interconnect surround the space and form an air flow channel.

[0037] In some embodiments, in operation the first circuit board has a first temperature and the second circuit board has a second temperature lower than the first temperature. In some embodiments, in operation, the second temperature is up to 20 degrees Celsius lower than the first temperature.

[0038] In some embodiments, the electronic assembly is a solid state disk drive.

[0039] In some embodiments, the electronic assembly does not include a case.

[0040] In some embodiments, the electronic assembly does not include a heat sink.

[0041] Another aspect of the invention includes a method of manufacturing an electronic assembly for dissipating heat. A first circuit board with one or more heat generating components coupled thereto is provided. A second circuit board with one or more heat sensitive components coupled thereto is also provided. A thermal barrier interconnect is provided and it is coupled to the first circuit board to the second circuit board such that the first and second circuit board are electrically coupled to one another.

[0042] Numerous details are described herein in order to provide a thorough understanding of the exemplary embodiments illustrated in the accompanying drawings. However, some embodiments may be practiced without many of the specific details, and the scope of the claims is only limited by those features and aspects specifically recited in the claims. Furthermore, well-known methods, components, and circuits have not been described in exhaustive detail so as not to unnecessarily obscure more pertinent aspects of the embodiments described herein.

[0043] FIG. 1 is a block diagram of an exemplary system module 100 in a typical computational device in accordance with some embodiments. The system module 100 in this computational device includes at least a central processing unit (CPU) 102, memory modules 104 for storing programs, instructions and data, an input/output (I/O) controller 106, one or more communication interfaces such as network interfaces 108, and one or more communication buses 150 for interconnecting these components. In some embodiments, the I/O controller 106 allows the CPU 102 to communicate with an I/O device (e.g., a keyboard, a mouse, a track-pad, etc.) via a universal serial bus interface (or any other suitable wired or wireless interface). In some embodiments, the network interfaces 108 include one or more interfaces for Wi-Fi, Ethernet, and/or Bluetooth networks, each allowing the computational device 100 to exchange data with an external source, such as a server or another computational device. In some embodiments, the communication buses 150 include circuitry

(sometimes called a chipset) that interconnects and controls communications among various system components included in the system module.

[0044] In some embodiments, the memory modules 104 include volatile memory devices, such as dynamic random access memory (“DRAM”) or static random access memory (“SRAM”) devices, non-volatile memory devices, such as resistive random access memory (“ReRAM”), electrically erasable programmable read only memory (“EEPROM”), flash memory (which can also be considered a subset of EEPROM), ferroelectric random access memory (“FRAM”), and magnetoresistive random access memory (“MRAM”), and other semiconductor elements capable of storing information. Furthermore, each type of memory device may have different configurations. For example, flash memory devices may be configured in a NAND or a NOR configuration.

[0045] The memory devices can be formed from passive elements, active elements, or both. By way of non-limiting example, passive semiconductor memory elements include ReRAM device elements, which in some embodiments include a resistivity switching storage element, such as an anti-fuse, phase change material, etc., and optionally a steering element, such as a diode, etc. Further by way of non-limiting example, active semiconductor memory elements include EEPROM and flash memory device elements, which in some embodiments include elements containing a charge storage region, such as a floating gate, conductive nanoparticles or a charge storage dielectric material.

[0046] Multiple memory elements may be configured so that they are connected in series or such that each element is individually accessible. By way of non-limiting example, NAND devices contain memory elements (e.g., devices containing a charge storage region) connected in series. For example, a NAND memory array may be configured so that the array is composed of multiple strings of memory in which each string is composed of multiple memory elements sharing a single bit line and accessed as a group. In contrast, memory elements may be configured so that each element is individually accessible, e.g., a NOR memory array. One of skill in the art will recognize that the NAND and NOR memory configurations are exemplary, and memory elements may be otherwise configured.

[0047] The semiconductor memory elements included in a single device, such as memory elements located within and/or over the same substrate or in a single die, may be distributed in a two- or three- dimensional manner (such as a two dimensional (2D) memory array structure or a three dimensional (3D) memory array structure).

[0048] In a two dimensional memory structure, the semiconductor memory elements are arranged in a single plane or single memory device level. Typically, in a two dimensional memory structure, memory elements are located in a plane (e.g., in an x-z direction plane) which extends substantially parallel to a major surface of a substrate that supports the memory elements. The substrate may be a wafer on which the material layers of the memory elements are deposited and/or in which memory elements are formed or it may be a carrier substrate which is attached to the memory elements after they are formed.

[0049] The memory elements may be arranged in the single memory device level in an ordered array, such as in a plurality of rows and/or columns. However, the memory elements may be arranged in non-regular or non-orthogonal configurations as understood by one of skill in the art. The memory elements may each have two or more electrodes or contact lines, including a bit line and a word line.

[0050] A three dimensional memory array is organized so that memory elements occupy multiple planes or multiple device levels, forming a structure in three dimensions (i.e., in the x, y and z directions, where the y direction is substantially perpendicular and the x and z directions are substantially parallel to the major surface of the substrate).

[0051] As a non-limiting example, each plane in a three dimensional memory array structure may be physically located in two dimensions (one memory level) with multiple two dimensional memory levels to form a three dimensional memory array structure. As another non-limiting example, a three dimensional memory array may be physically structured as multiple vertical columns (e.g., columns extending substantially perpendicular to the major surface of the substrate in the y direction) having multiple elements in each column and therefore having elements spanning several vertically stacked planes of memory devices. The columns may be arranged in a two dimensional configuration, e.g., in an x-z plane, thereby resulting in a three dimensional arrangement of memory elements. One of skill in the art will understand that other configurations of memory elements in three dimensions will also constitute a three dimensional memory array.

[0052] By way of non-limiting example, in a three dimensional NAND memory array, the memory elements may be connected together to form a NAND string within a single plane, sometimes called a horizontal (e.g., x-z) plane for ease of discussion. Alternatively, the memory elements may be connected together to extend through multiple parallel planes. Other three dimensional configurations can be envisioned wherein some

NAND strings contain memory elements in a single plane of memory elements (sometimes called a memory level) while other strings contain memory elements which extend through multiple parallel planes (sometimes called parallel memory levels). Three dimensional memory arrays may also be designed in a NOR configuration and in a ReRAM configuration.

[0053] A monolithic three dimensional memory array is one in which multiple planes of memory elements (also called multiple memory levels) are formed above and/or within a single substrate, such as a semiconductor wafer, according to a sequence of manufacturing operations. In a monolithic 3D memory array, the material layers forming a respective memory level, such as the topmost memory level, are located on top of the material layers forming an underlying memory level, but on the same single substrate. In some embodiments, adjacent memory levels of a monolithic 3D memory array optionally share at least one material layer, while in other embodiments adjacent memory levels have intervening material layers separating them.

[0054] In contrast, two dimensional memory arrays may be formed separately and then integrated together to form a non-monolithic 3D memory device in a hybrid manner. For example, stacked memories have been constructed by forming 2D memory levels on separate substrates and integrating the formed 2D memory levels atop each other. The substrate of each 2D memory level may be thinned or removed prior to integrating it into a 3D memory device. As the individual memory levels are formed on separate substrates, the resulting 3D memory arrays are not monolithic three dimensional memory arrays.

[0055] Further, more than one memory array selected from 2D memory arrays and 3D memory arrays (monolithic or hybrid) may be formed separately and then packaged together to form a stacked-chip memory device. A stacked-chip memory device includes multiple planes or layers of memory devices, sometimes called memory levels.

[0056] The term “three-dimensional memory device” (or 3D memory device) is herein defined to mean a memory device having multiple layers or multiple levels (e.g., sometimes called multiple memory levels) of memory elements, including any of the following: a memory device having a monolithic or non-monolithic 3D memory array, some non-limiting examples of which are described above; or two or more 2D and/or 3D memory devices, packaged together to form a stacked-chip memory device, some non-limiting examples of which are described above.

[0057] A person skilled in the art will recognize that the invention or inventions described and claimed herein are not limited to the two dimensional and three dimensional exemplary structures described here, and instead cover all relevant memory structures suitable for implementing the invention or inventions as described herein and as understood by one skilled in the art.

[0058] In some embodiments, the memory modules 104 include non-volatile memory, such as one or more magnetic disk storage devices, optical disk storage devices, flash memory devices, or other non-volatile solid state storage devices. In some embodiments, the memory modules 104, or alternatively the non-volatile memory device(s) within memory modules 104, include a non-transitory computer readable storage medium. In some embodiments, memory slots are reserved on the system module 100 for receiving the memory modules 104. Once inserted into the memory slots, the memory modules 104 are integrated into the system module 100.

[0059] In many embodiments, the system module 100 further includes one or more components selected from:

- a memory controller 110 that controls communication between the CPU 102 and memory components, including the memory modules 104, in the computational device;
- solid state drives (SSDs) 112 that apply integrated circuit assemblies to store data in the computational device, and in some embodiments, are based on NAND or NOR memory configurations;
- a hard drive 114 that is a conventional data storage device used for storing and retrieving digital information based on electromechanical magnetic disks;
- a power supply connector 116 that is electrically coupled to receive an external power supply;
- power management integrated circuit (PMIC) 118 that modulates the received external power supply to other desired DC voltage levels, e.g., 5V, 3.3V or 1.8V, as required by various components or circuits within the computational device;
- a graphics card 120 that generates a feed of output images to one or more display devices according to their desirable image/video formats; and

- a sound card 122 that facilitates the input and output of audio signals to and from the computational device under control of computer programs.

[0060] It is noted that the one or more communication buses 150 also interconnect and control communications among various system components, including components 110-122 (as well as other components not listed).

[0061] Further, one of skill in the art will understand that other non-transitory computer readable storage media can be used. In particular, as new data storage technologies are developed, those new data storage technologies may be used in the memory modules described herein. These new non-transitory computer readable storage media include, but are not limited to, those manufactured from biological materials, nanowires, carbon nanotubes, and individual molecules, even though the respective data storage technologies are currently under development and are yet to be commercialized.

[0062] Some of the aforementioned components (or other components not mentioned) generate heat during normal operation. In some instances, they may be integrated with heat sinks in order to reduce the temperatures of the corresponding components. For example, the solid state drives 112 used in a blade server may have heat sinks mounted on the top of each individual dual in-line memory module (DIMM) or on an electronic assembly containing the DIMMs. Heat generated from electronic components in the DIMMs is conducted to the heat sinks, and dissipated by airflow generated by fans. However, as the data workload in these blade servers increases and the form factor of the DIMMs decreases (e.g., closely placed memory slots in the memory modules 104), it becomes more difficult for conventional heat sinks and high-speed fans to conduct and dissipate the heat effectively. More generally, as the size of electronic components decreases, and more and more electronic components are being placed in close proximity to one another on circuit boards, it becomes more difficult to keep the electronic components sufficiently cool. Furthermore, some form factors of solid state disk drives do not include a case and thus heat sinks cannot be integrated into the design of the case.

[0063] To address this issue, the various embodiments described herein describe electronic assemblies that separate heat generating components from heat sensitive components by using a thermal barrier interconnect. In some embodiments, by using a thermal barrier interconnect such as a flexible circuit board between two rigid printed circuit boards, a thermal barrier is formed. This thermal barrier is used to allow one board to operate

at a reduced temperature in relation to the other board. For instance, a board with NAND flash can be at a reduced temperature in relation to the board which contains the NAND flash controller and power supply electronics. The difference can be as much as 20 degrees C. This means the NAND flash running at a lower temperature will have better data retention and therefore require less recycling of data (which is Write Amplification) and lengthen the drive life.

[0064] Furthermore, in some embodiments, electronic assemblies form a self-supporting tube or channel for directing airflow over electronic components of the assembly. In particular, a channel structure helps confine air flow to the space within the channel. Thus, by passing air through the channel (e.g., either by convection, fans, or any other technique), heat can be effectively removed from components that are within the channel, or that are otherwise thermally coupled to the channel (e.g., components that are mounted outside the channel but are thermally coupled to a heat sink within the channel). Because the channel structures confine the airflow to the space within the channel, better cooling performance can be achieved for a given amount of airflow than would otherwise be possible.

[0065] Figure 2 is a side view of an exemplary electronic assembly, in accordance with some embodiments. Figure 2 illustrates an embodiment of an electronic assembly 200 (“assembly 200”). In particular, the assembly 200 comprises a first circuit board 202 and a second circuit board 204. (Circuit boards are sometimes referred to herein simply as “boards.”)

[0066] The first circuit board 202 includes with one or more heat generating components 206 coupled to it. The second circuit board 204 includes one or more heat sensitive components 210 coupled to it. In some embodiments, one or more heat sensitive components 210 are, or include, memory modules. In some embodiments, the one or more heat generating components 206 are, or include, processors, power supply electronics, and/or flash controllers.

[0067] In some embodiments, at least one of the first circuit board 202 and second circuit board 204 include one or more solid state drives (SSDs). In some embodiments, at least one of first circuit board 202 and second circuit board 204 include one or more three-dimensional (3D) memory devices.

[0068] The first board 202 and second board 204 are mechanically and/or electrically coupled via a thermal barrier interconnect 211. The thermal barrier interconnect 211 can

carry electrical signals between two boards (e.g., the first board 202 and the second board 204), or between other electronic components or sub-assemblies. For example, the assembly 200 includes a thermal barrier interconnect 211 that mechanically and/or electrically couples the first board 202 to the second board 204.

[0069] In various embodiments, the thermal barrier interconnect 211 is a flexible board, flexible wire array, flexible PCB, flexible flat cable, ribbon cable (e.g., a flexible flat ribbon cable), or a combination thereof. In some embodiments, the flexible material is made from a kapton or other polyimide materials which has a lower thermal conductivity.

[0070] The thermal barrier interconnect 211 does not contain the thermal mass of the rigid FR4 (or other types) board material. As such, thermal barrier interconnect 211 produces in a thermal barrier. In some embodiments, the thermal barrier is further enhanced from the reduction in copper planes which are found in the rigid boards to supply power and grounds.

[0071] It will be understood that more or fewer flexible interconnects can be used to couple the boards of an electronic assembly in accordance with the ideas described in the instant application. For example, an electronic assembly may also include a fastener interconnect. One example of such an assembly is described herein with reference to Figure 4.

[0072] Specifically, Figure 2 shows an solid state disk constructed in such a way as to thermally isolate the heat generating components 206 such as power supply and NAND controller electronics (temperature zone 1) from the more temperature sensitive components 210, such as NAND flash (temperature zone 2). The thermal barrier interconnect 211 such as a flexible circuit board which connects the two boards provides an electrical connection between them, such that for instance it supplies the NAND power and data and controller signals (from the first board 202) for the NAND (on the second board 204).

[0073] It is further noted, that typically the NAND uses only two power supply connections, one for the CORE voltage and one for the I/O voltage. This means other power planes used by the NAND flash controller and support circuitry from the NAND PCBA are not needed in the thermal barrier interconnect 211. By removing the unused power planes from the NAND printed circuit board assembly you in turn remove some amount of thermal conduction to the main controller boards.

[0074] Figures 3A and 3B show a solid state disk drive form factor. In some embodiments, this form factor is used as a daughter board assembly on a larger SSD

integration platform. In some embodiments, all of the NAND flash is on the outer board (e.g. the second board 204). This allows better exposure to airflow for lower temperature on the NAND in comparison to the main controller board.

[0075] This configuration along with the proper airflow has been shown to be 20 degree C lower than the NAND temperature from the main controller board. Because a flexible circuit board is a standard impedance controlled PCB, there is no loss in signal strength of speed to the NAND flash board when a flexible circuit board is employed as the thermal barrier interconnect. From an electrical standpoint, this assembly is all one board with no schematic restrictions.

[0076] Figure 3A and 3B illustrate another embodiment of an electronic assembly 200 ("assembly 200"). In particular, the assembly 200 comprises a first circuit board 202 and a second circuit board 204.

[0077] The first circuit board 202 includes with one or more heat generating components 206 coupled to it. The second circuit board 204 includes with one or more heat sensitive components 210 coupled to it. In some embodiments, one or more heat sensitive components 210 are, or include, memory modules. In some embodiments, the one or more heat generating components 206, or include processors, power supply electronics, and/or flash controllers. In this embodiment, the first board is on a first plane and the second boards is on a second plane substantially parallel to the first plane and is separated by a space 214.

[0078] In some embodiments, the assembly 200 is configured to be mechanically and/or electrically coupled to a base board (e.g., base board 208). In some embodiments, the base board is a mother board of a computer. In some embodiments, the base board is any circuit board to which the assembly 200 is configured to be coupled. In some embodiments, the assembly 200 is a daughter board assembly.

[0079] The first board 202 and second board 204 are mechanically and/or electrically coupled via a thermal barrier interconnect 211. For example, the assembly 200 includes a thermal barrier interconnect 211 that mechanically and/or electrically couples the first board 202 to the second board 204. In various embodiments, the thermal barrier interconnect 211 is a flexible board, flexible wire array, flexible PCB, flexible flat cable, ribbon cable (e.g., a flexible flat ribbon cable), or a combination thereof. The thermal barrier interconnect 211 can carry electrical signals between two boards (e.g., the first board 202 and the second board 204), or between other electronic components or sub-assemblies.

[0080] It will be understood that more or fewer flexible interconnects can be used to couple the boards of an electronic assembly in accordance with the ideas described in the instant application. For example, an electronic assembly may also include a fastener interconnect. One example of such an assembly is described herein with reference to Figure 4.

[0081] Returning to Figure 3A and 3B, the boards 202, 204 have been configured such that a space 214 is formed between the first board 202, the second board 204. Furthermore, the second board 204 is located further away from the base board 208. Both of these features allow for better exposure to airflow and thus for lower temperature on the second board 204 in comparison to the first board 202.

[0082] Figure 4 is side view of another exemplary electronic assembly, in accordance with some embodiments. Figure 4 is similar to the embodiment of Figures 3A and 3B and includes the same elements described therein. It additionally includes at least one fastener 224. The fastener(s) are configured to couple the first circuit board 202 to the second circuit board 204, such that the fastener 224, first circuit board 202, second circuit board 204, and thermal barrier interconnect 211 surround the space 214 and form an air flow channel 216.

[0083] Air can then pass through this channel 216 (e.g., with a fan, compressed air, convection, etc.) in order to remove heat from the electronic components (such as the one or more heat generating components 206 and the one or more heat sensitive components 210). The one or more fastener(s) 224 include any structure(s), component(s), or device(s) that mechanically and/or electrically couple the first board 202 and the second board 204. For example, the one or more fastener(s) 224 can be any of the group consisting of: a clip, a screw, a bolt, a nut, a solder tab and/or soldered connection, adhesive, a slot, a hole, a peg, a protrusion, or the like (or any combination of the foregoing items). In some embodiments, the fastener(s) have low or no thermal conductivity. For instance, they may be made of an electrostatic (ESD) discharge plastic, a static dissipative plastic, a composite material, or the like.

[0084] It is noted that, in some embodiments the channel 216 is formed without the need for external supports, rails, brackets, or other hardware, reducing the cost and complexity of the assembly 200 while still providing the increased cooling capacity that is made possible by such a channel. In particular, because the channel 216 can be formed simply by joining the first board 202 and the second board 204, the assembly 200 can be

more easily manufactured than structures that require more complicated mounting hardware. Moreover, because the entire assembly 200 can be soldered together the resulting assembly is more robust than an assembly that uses less secure mounting or attachment techniques. Specifically, the soldered connections may be more resistant to separation or loosening caused by the heating and cooling cycles that are frequently present in computer hardware.

[0085] As shown in Figure 4, the channel 216 is supported on one side by the thermal barrier interconnect 211 (typically a flexible interconnect such as a flexible circuit board). For example, the thermal barrier interconnect 211 joins the first board 202 and the second board 204, and, when the assembly is formed into a channel 216, the thermal barrier interconnect 211 provides structural support to keep the first board 202 separate from the second board 204. Although the thermal barrier interconnect 211 is rigid enough to maintain separation of the first board 202 and the second board 204 when the assembly is completed, it is flexible enough to provide a pliable joint so that the boards can be moved with respect to one another without damaging the connections. This allows the boards to be manipulated with respect to one another for ease of installation and assembly. For example, the assembly 200 may be mounted to a base board (e.g., base board 208, Figure 3A). Then the second board 204 is coupled to the first board 202 by means of one or more fasteners 224 to form the channel 216. Thus, in some embodiments, the thermal barrier interconnect 211 is flexible enough to allow the boards to be moved relative to one another, but are rigid enough to support the channel 216 under normal operating conditions and orientations (e.g., so that the channel 216 does not collapse).

[0086] In some embodiments, multiple assemblies 200 are combined to form a substantially continuous channel 216. For example, in some embodiments, several assemblies 200 are coupled to one or more base boards such that the respective channels 216 are substantially aligned, thus allowing air to pass through the respective channels. In some embodiments, where multiple assemblies 200 form a single channel, the assemblies 200 are coupled to one another so as to form a substantially continuous channel. For example, in some embodiments, gaskets, seals, rails, or any other appropriate components, are positioned between two adjacent assemblies in order to prevent air from escaping the channel.

[0087] Figure 5 illustrates an exemplary flow chart of a method 500 for manufacturing and using an electronic assembly for dissipating heat, according to some embodiments. A first electronic assembly is provided (502). The electronic assembly includes a first circuit board (e.g., the first circuit board 202), a second circuit board (e.g., the

second circuit board 204), and a thermal barrier interconnect (e.g., thermal barrier interconnect 211). In some embodiments, the electronic assembly also includes one or more fasteners (e.g., the one or more fastener(s) 224) (504).

[0088] Then the first circuit board is coupled to the second circuit board with the thermal barrier interconnect (506). In some embodiments, the first circuit board, the second circuit board, and the thermal barrier interconnect are next to one another in a substantially common plane as illustrated in Figure 2.

[0089] In some embodiments, the second circuit board is coupled to a base circuit board (e.g., base board 208) (508).

[0090] In some embodiments, the electronic assembly (e.g., the second circuit board) is then manipulated (e.g., by machine or by hand) so as to cause the second circuit board to be positioned in a separate plane substantially parallel to the first circuit board (510). Manipulating the second circuit board so as to cause the second circuit board to be positioned substantially parallel to the first circuit board causes at least a portion of the thermal barrier interconnect 211 coupling the first circuit board to the second circuit board to be deformed as illustrated in Figures 3A and 3B.

[0091] In some embodiments, the first circuit board is coupled to the second circuit board with a fastener (e.g., the one or more fastener(s) 224), such that the first circuit board and the second circuit board are substantially parallel and are separated by a space 214, wherein the space forms at least part of a channel (e.g., channel 216) that is configured to direct airflow through the space between the first and second circuit boards as illustrated in Figure 4 (512).

[0092] Thereafter, airflow is passed or directed (514) through the channel to dissipate heat, as described above.

[0093] It will be understood that, although the terms “first,” “second,” etc. may be used herein to describe various elements, these elements should not be limited by these terms. These terms are only used to distinguish one element from another. For example, a first contact could be termed a second contact, and, similarly, a second contact could be termed a first contact, which changing the meaning of the description, so long as all occurrences of the “first contact” are renamed consistently and all occurrences of the second contact are renamed consistently. The first contact and the second contact are both contacts, but they are not the same contact.

[0094] The terminology used herein is for the purpose of describing particular embodiments only and is not intended to be limiting of the claims. As used in the description of the embodiments and the appended claims, the singular forms “a”, “an” and “the” are intended to include the plural forms as well, unless the context clearly indicates otherwise. It will also be understood that the term “and/or” as used herein refers to and encompasses any and all possible combinations of one or more of the associated listed items. It will be further understood that the terms “comprises” and/or “comprising,” when used in this specification, specify the presence of stated features, integers, steps, operations, elements, and/or components, but do not preclude the presence or addition of one or more other features, integers, steps, operations, elements, components, and/or groups thereof.

[0095] As used herein, the term “if” may be construed to mean “when” or “upon” or “in response to determining” or “in accordance with a determination” or “in response to detecting,” that a stated condition precedent is true, depending on the context. Similarly, the phrase “if it is determined [that a stated condition precedent is true]” or “if [a stated condition precedent is true]” or “when [a stated condition precedent is true]” may be construed to mean “upon determining” or “in response to determining” or “in accordance with a determination” or “upon detecting” or “in response to detecting” that the stated condition precedent is true, depending on the context.

[0096] The foregoing description, for purpose of explanation, has been described with reference to specific embodiments. However, the illustrative discussions above are not intended to be exhaustive or to limit the claims to the precise forms disclosed. Many modifications and variations are possible in view of the above teachings. The embodiments were chosen and described in order to best explain principles of operation and practical applications, to thereby enable others skilled in the art.

What is claimed is:

1. An electronic assembly for dissipating heat, comprising:
a first circuit board with one or more heat generating components coupled thereto;
a second circuit board with one or more heat sensitive components coupled thereto;
and
a thermal barrier interconnect electrically coupling the first circuit board to the second circuit board.
2. The electronic assembly of claim 1, wherein the thermal barrier interconnect has a lower thermal conductivity than the first circuit board and the second circuit board.
3. The electronic assembly of any of claims 1-2, wherein the thermal barrier interconnect is a flexible interconnect.
4. The electronic assembly of claim 3, wherein the flexible interconnect is made from a kapton material.
5. The electronic assembly of claim 3, wherein the flexible interconnect is made from a polyimide material.
6. The electronic assembly of any of claims 1-5, wherein the thermal barrier interconnect has a lower thermal mass than the first and second circuit boards.
7. The electronic assembly of any of claims 1-6, wherein the thermal barrier interconnect includes power planes for CORE voltage and the I/O voltage.
8. The electronic assembly of claim 7, wherein the thermal barrier interconnect does not include a plurality of power planes found in the first and second circuit boards.
9. The electronic assembly of any of claims 1-8, wherein the one or more heat generating components include one or more power supply electronic components.
10. The electronic assembly of any of claims 1-9, wherein the one or more heat generating components include a NAND flash controller.
11. The electronic assembly of any of claims 1-10, wherein the one or more heat sensitive components include NAND flash memory.

12. The electronic assembly of any of claims 1-11, wherein the first circuit board, the second circuit board are disposed next to one another in a substantially common plane.
13. The electronic assembly of any of claims 1-11, wherein the first circuit board is on a first plane and the second circuit board is on a second plane substantially parallel to the first plane and the first and second circuit boards are separated by a space.
14. The electronic assembly of claim 13, further comprising a fastener configured to couple the first circuit board to the second circuit board, wherein the fastener, first circuit board, second circuit board, and thermal barrier interconnect surround the space and form an air flow channel.
15. The electronic assembly of any of claims 1-14, wherein, in operation:
the first circuit board has a first temperature; and
the second circuit board has a second temperature lower than the first temperature.
16. The electronic assembly of claim 15, wherein, in operation, the second temperature is 5 to 20 degrees Celsius lower than the first temperature.
17. The electronic assembly of any of claims 1-16, wherein the electronic assembly does not include a case or a heat sink.
18. The electronic assembly of any of claims 1-17, wherein the electronic assembly is a solid state disk drive.
19. The electronic assembly of any of claims 1-18, wherein at least one of the first circuit board and second circuit board comprises one or more solid state drives (SSDs) or one or more three-dimensional (3D) memory devices.
20. A method of manufacturing an electronic assembly for dissipating heat, comprising:
providing an electronic assembly, comprising:
a first circuit board with one or more heat generating components coupled thereto;
a second circuit board with one or more heat sensitive components coupled thereto; and
a thermal barrier interconnect; and

coupling the first circuit board to the second circuit board with the thermal barrier interconnect.

21. The method of claim 20, wherein the electronic assembly is configured to operate so that the second circuit board operates at a temperature that is 5 to 20 degrees Celsius lower than the temperature of the first board in a steady state operation.

22. The method of any of claims 20-21, wherein at least one of the first circuit board and the second circuit board comprises one or more solid state drives (SSDs) or one or more three-dimensional (3D) memory devices.

23. A method of dissipating heat from an electronic assembly, comprising:
providing an electronic assembly, comprising:

a first circuit board with one or more heat generating components coupled thereto;

a second circuit board with one or more heat sensitive components coupled thereto; and

a thermal barrier interconnect, wherein the first circuit board is coupled to the second circuit board with the thermal barrier interconnect; and

directing airflow through a space formed between the first circuit board and the second circuit board to dissipate heat.

100

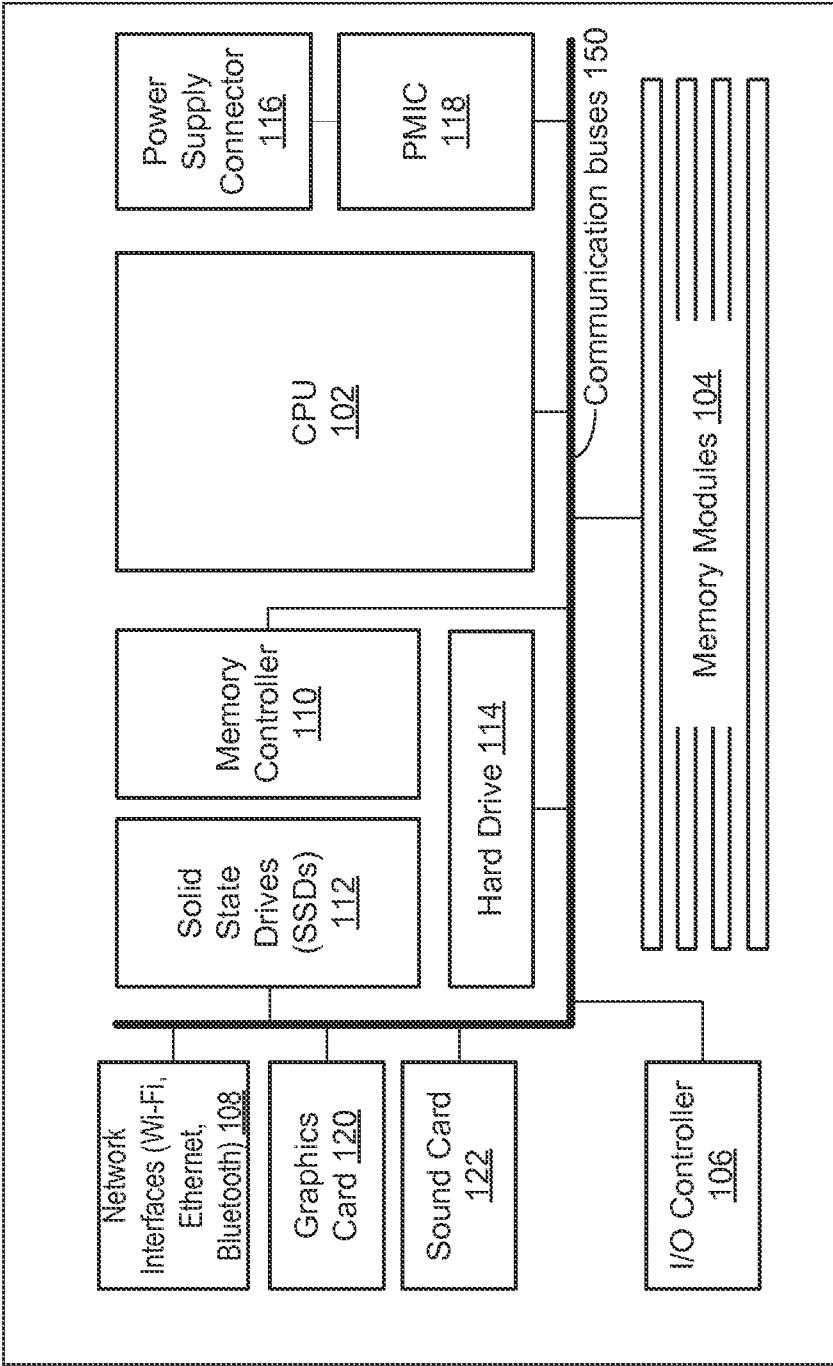


FIG. 1

200

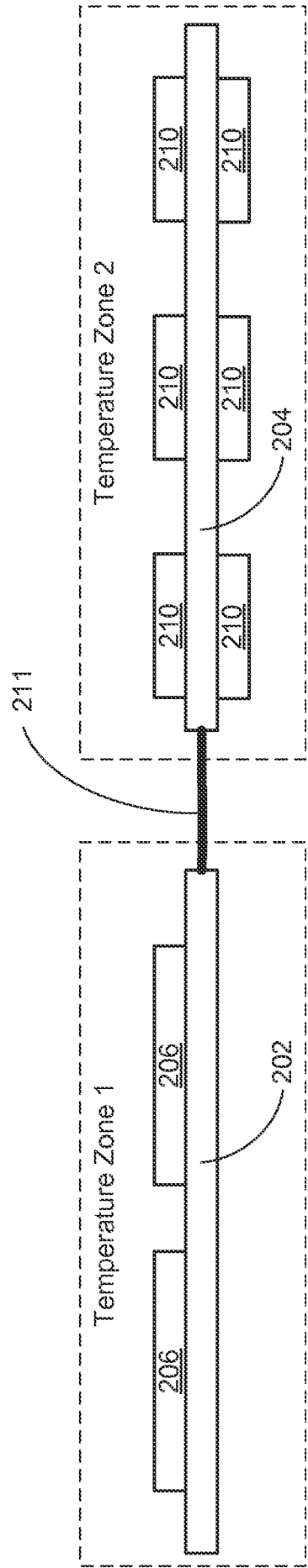
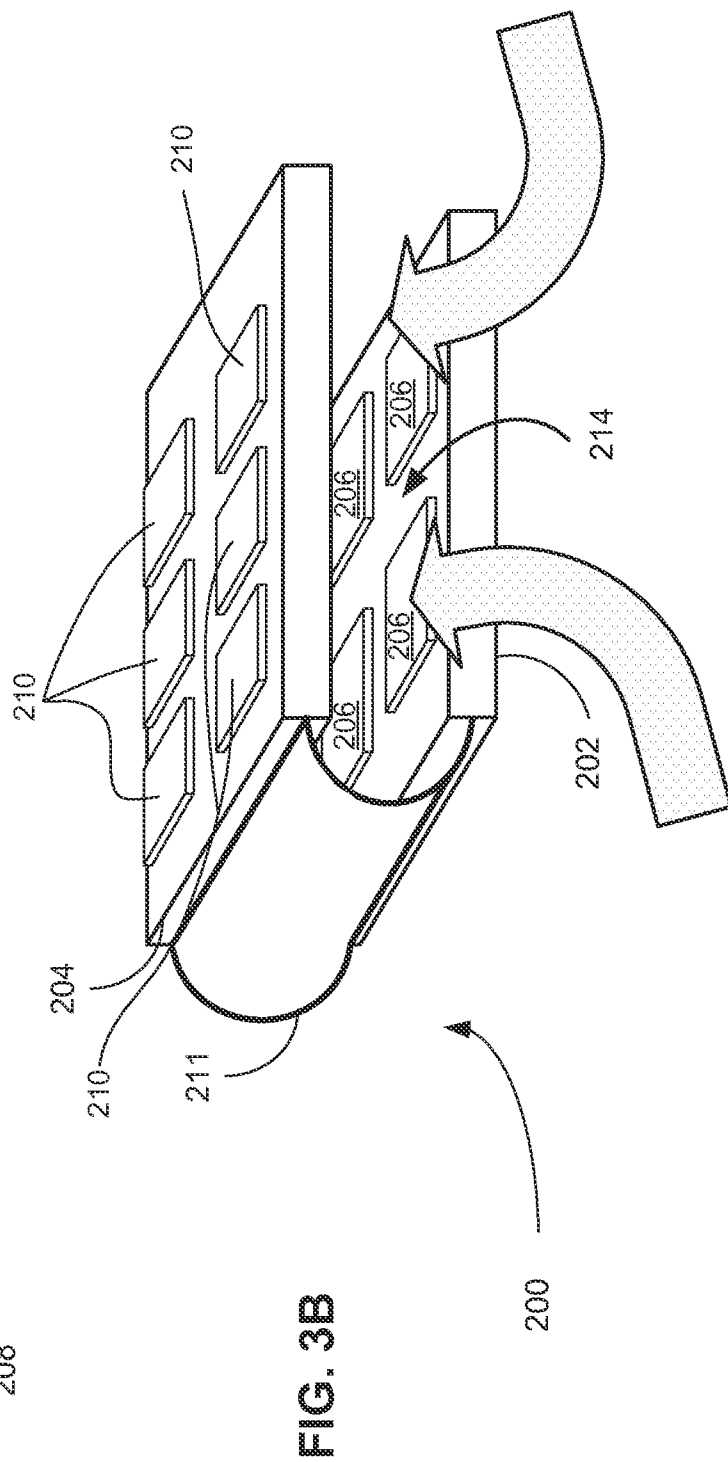
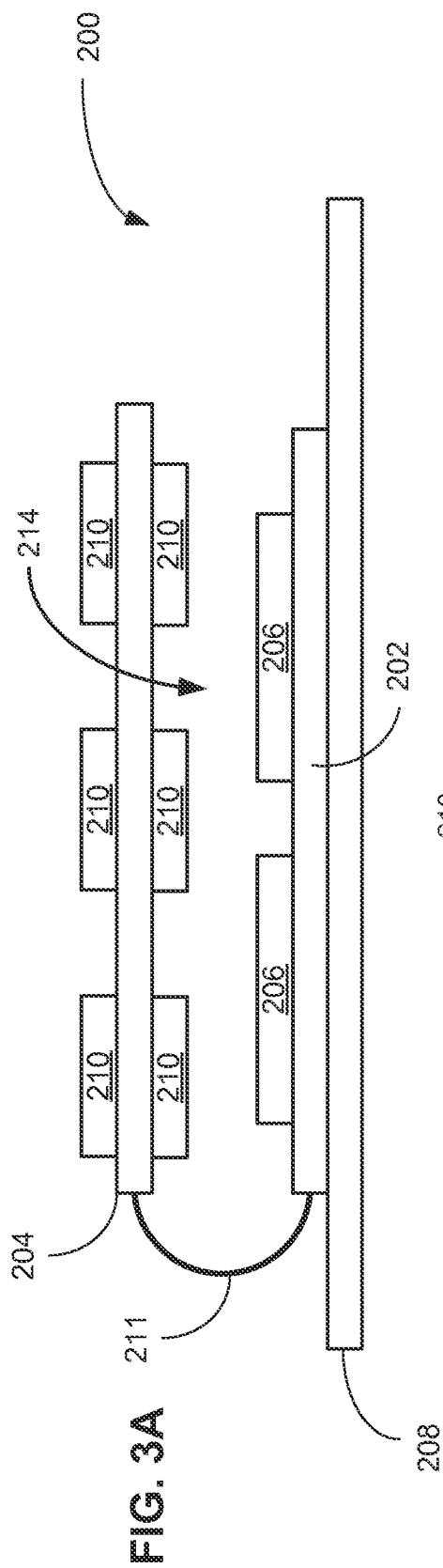


FIG. 2



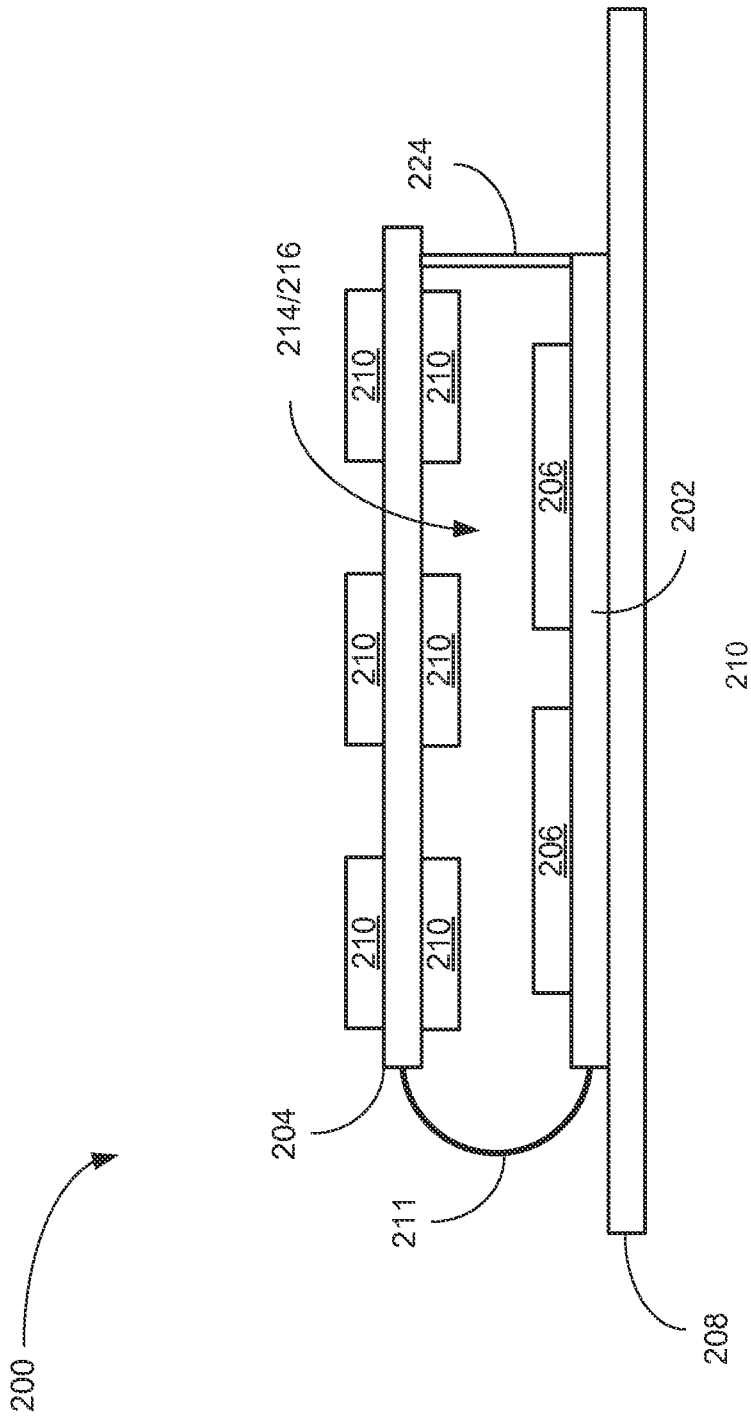
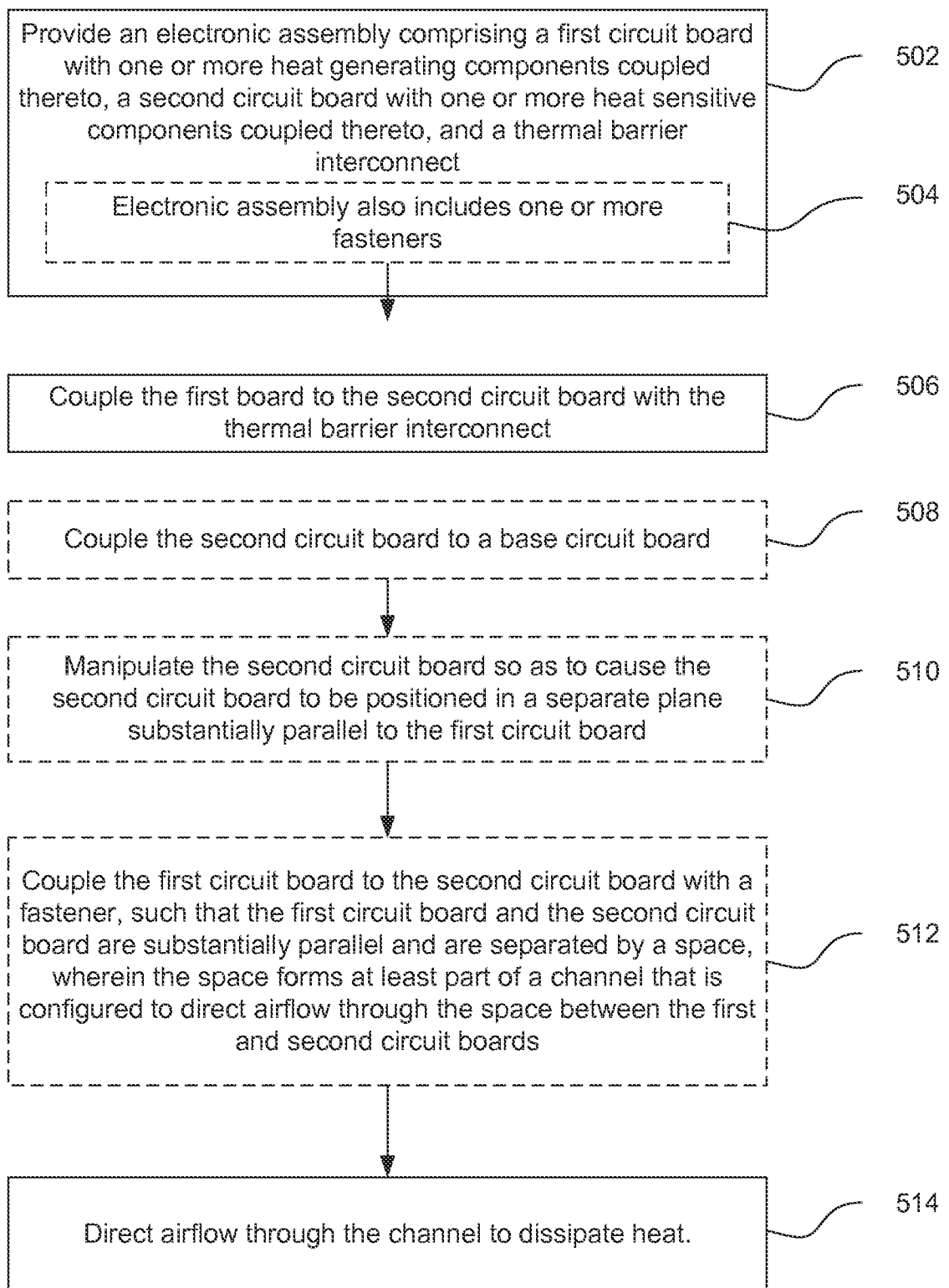


FIG. 4

500**FIG. 5**

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2015/017724

A. CLASSIFICATION OF SUBJECT MATTER
INV. G06F1/20
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2008/158818 A1 (CLIDARAS JIMMY [US] ET AL) 3 July 2008 (2008-07-03) paragraph [0020] - paragraph [0033] figure 1	1-23
A	----- WO 88/01793 A1 (HUGHES AIRCRAFT CO [US]) 10 March 1988 (1988-03-10) page 1, line 2 - line 14 page 3, line 9 - line 16	1,20,23
A	----- US 5 628 031 A (KIKINIS DAN [US] ET AL) 6 May 1997 (1997-05-06) column 21, line 44 - line 60 figure 25c	4
A	----- US 2005/152112 A1 (HOLMES STEVEN [US] ET AL) 14 July 2005 (2005-07-14) paragraph [0021] - paragraph [0029] figures 1-3	1,20,23



Further documents are listed in the continuation of Box C.



See patent family annex.

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"P" document published prior to the international filing date but later than the priority date claimed

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Date of the actual completion of the international search

6 May 2015

Date of mailing of the international search report

13/05/2015

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Baldan, Marco

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2015/017724

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