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(12) **United States Design Patent**
Kulkarni et al.

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(54) **DUAL ARM ARTICULATING MOUNT FOR AN ELECTRONIC DISPLAY**

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(**) Term: **14 Years**

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(51) **LOC (10) Cl.** **08-07**

(52) **U.S. Cl.**

USPC **D14/452**

(58) **Field of Classification Search**

USPC D14/371-382, 125-129, 336, 337,
D14/447-452, 492, 335, 239, 457, 439-441,
D14/432, 251-253; D8/349, 354, 363, 373,
D8/376, 380; D21/333; D12/415; D3/218;
348/180, 184, 325, 739, 825
CPC G06F 3/0412; G06F 3/016; G06F 3/0488;
G06F 3/011; G06F 3/038; G06F 3/03543;
G06F 3/0338; G06F 3/0202; G06F 3/0219;
G06F 3/0213; G06F 1/1616; G06F 3/023;
G06F 3/04883; G02F 1/13338; G02F 1/1313;
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B41J 2/465; G03F 7/70291; G02B 27/0172;
G02B 5/30; G02B 2027/0118; G02B 27/0101;
F16M 13/02; F16M 13/00; F16M 11/10;
F16M 11/04; F16M 2200/08; F16M 11/2021;
A47B 21/0314; A47B 88/044; A47B
2021/0335; H02G 3/126; F16B 47/00; F16B
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See application file for complete search history.

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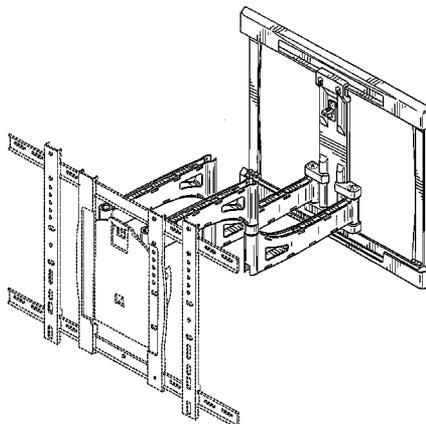
(57) **CLAIM**

The ornamental design for a dual arm articulating mount for
an electronic display, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view illustrating a dual arm
articulating mount for an electronic display, with a connected
display plate illustrated in broken lines;
FIG. 2 is a front perspective view illustrating the dual arm
articulating mount for an electronic display, with the display
plate omitted;
FIG. 3 is a front view thereof;
FIG. 4 is a side view thereof;
FIG. 5 is an opposing side view thereof;
FIG. 6 is a top view thereof; and,
FIG. 7 is a bottom view thereof.
The broken lines in FIG. 1 illustrating the connected display
plate represent environment that forms no part of the claimed
design, while the additional broken lines in FIGS. 1-3, 6 and
7 illustrate portions of the dual arm articulating mount for an
electronic display that form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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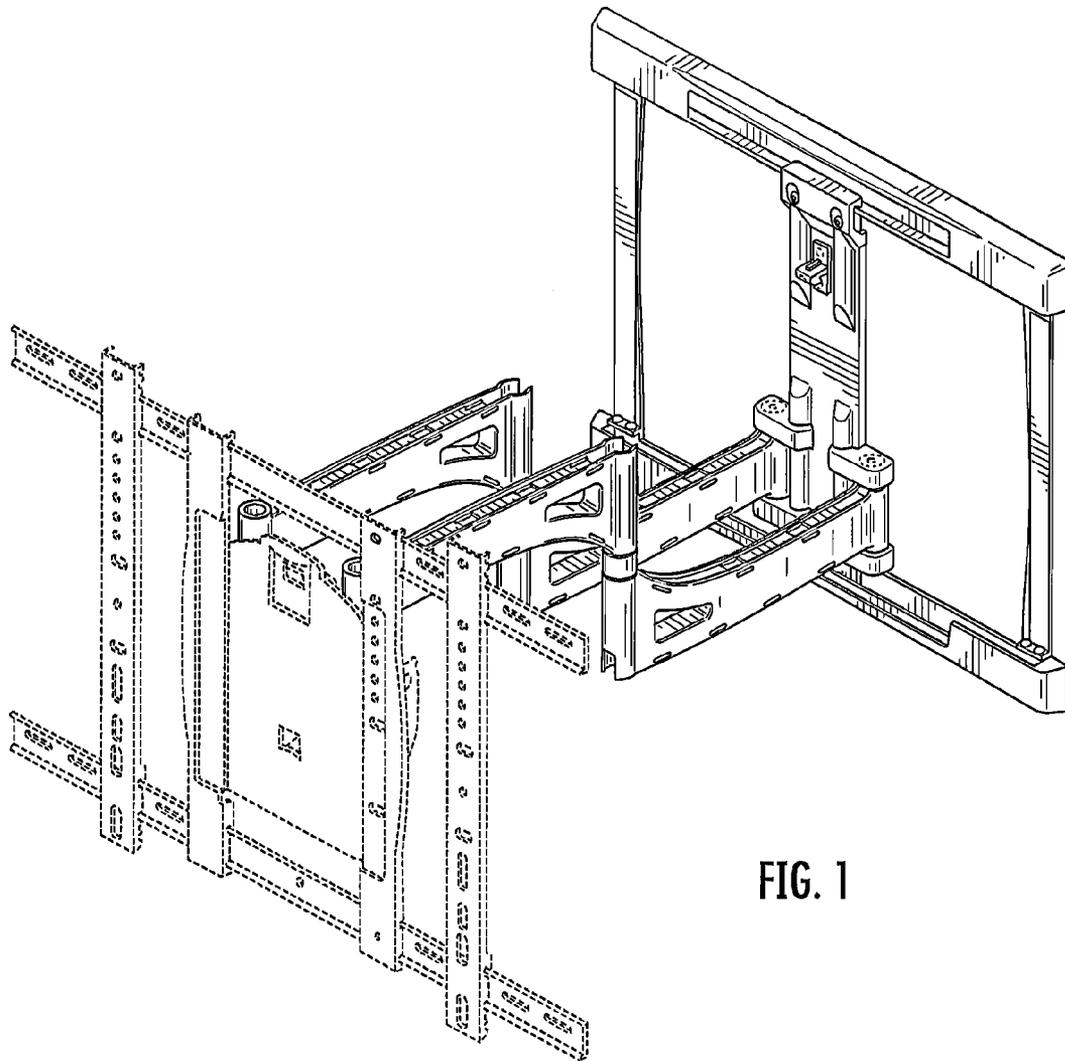


FIG. 1

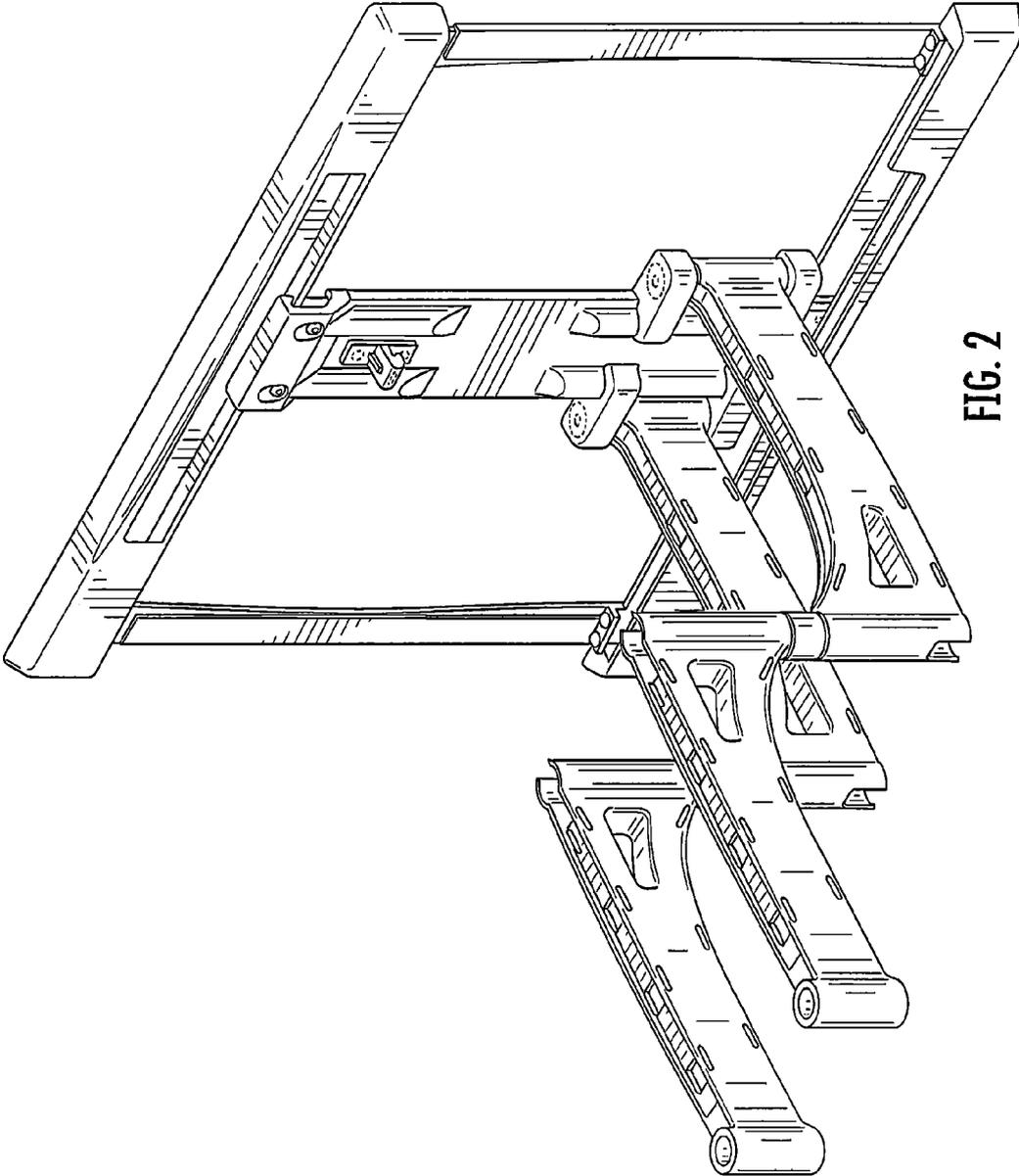


FIG. 2

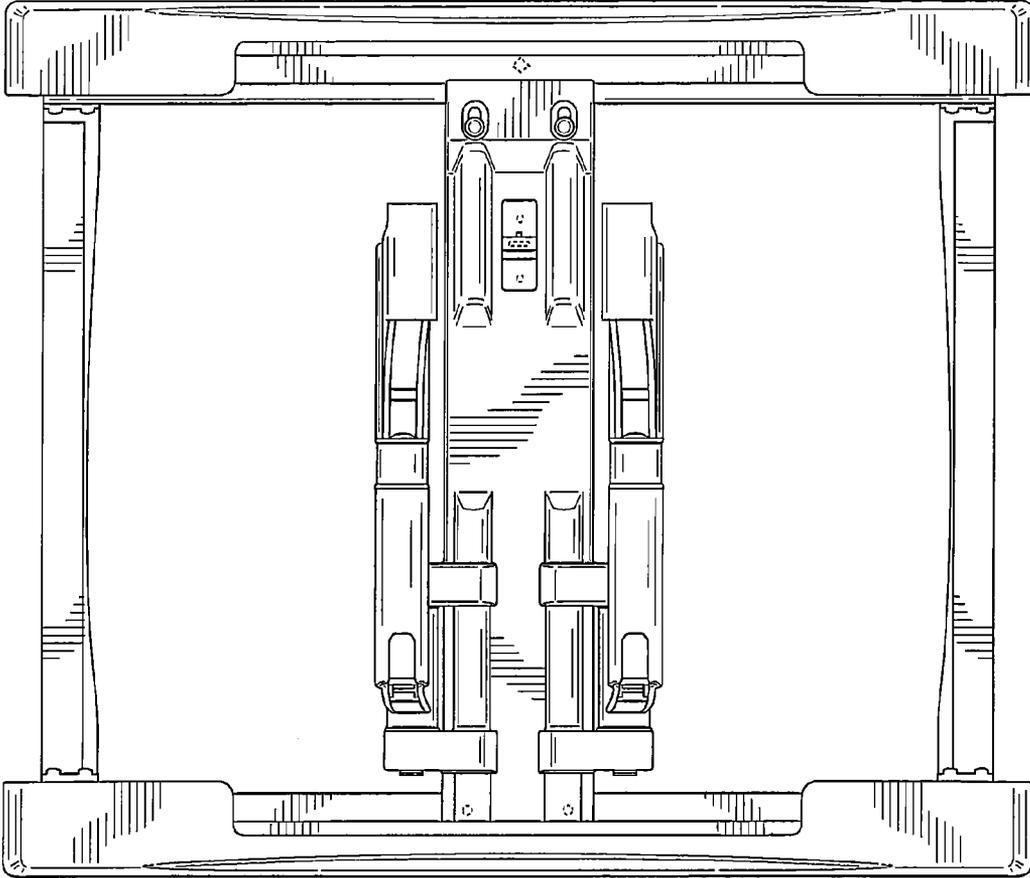


FIG. 3

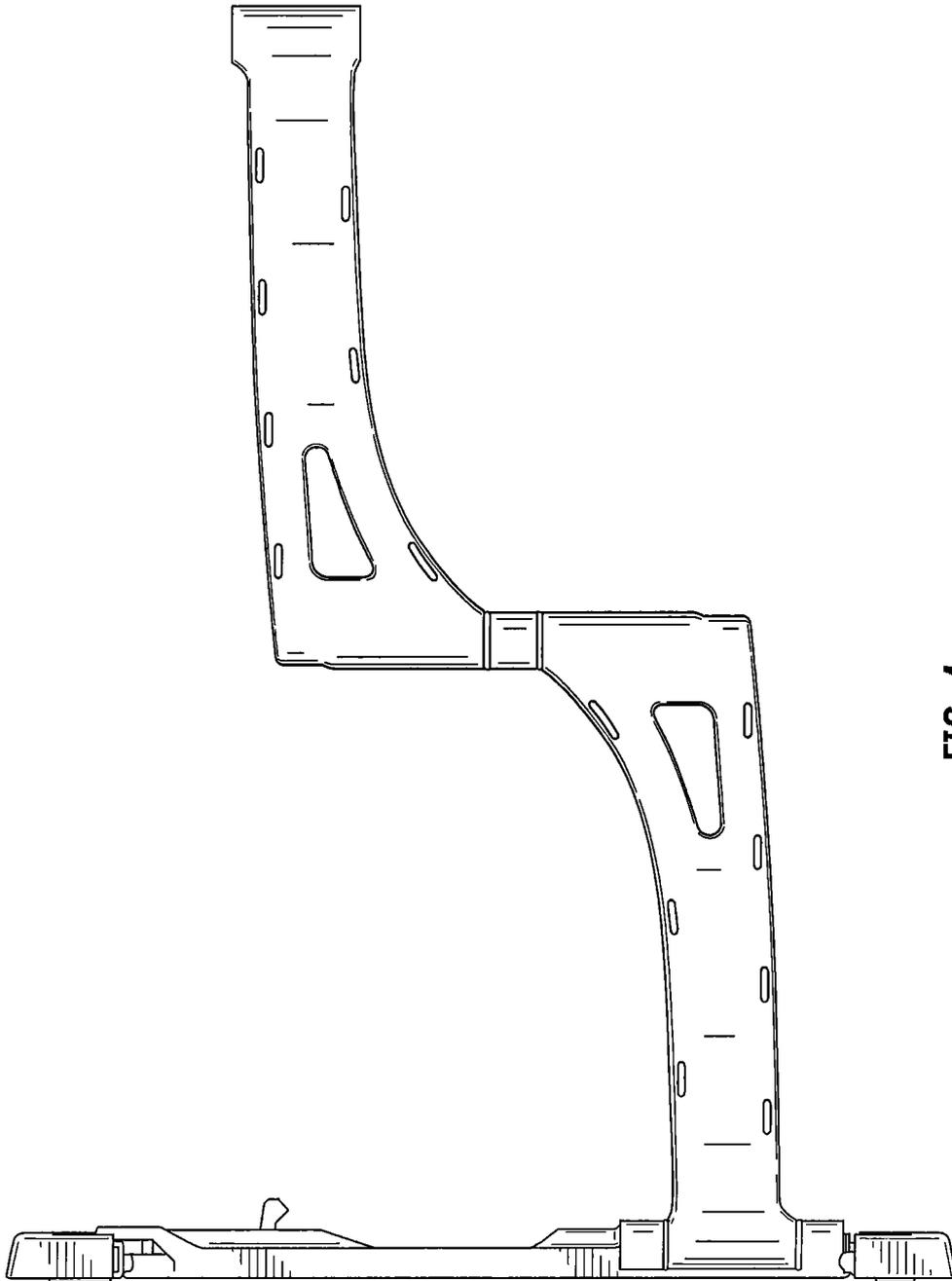


FIG. 4

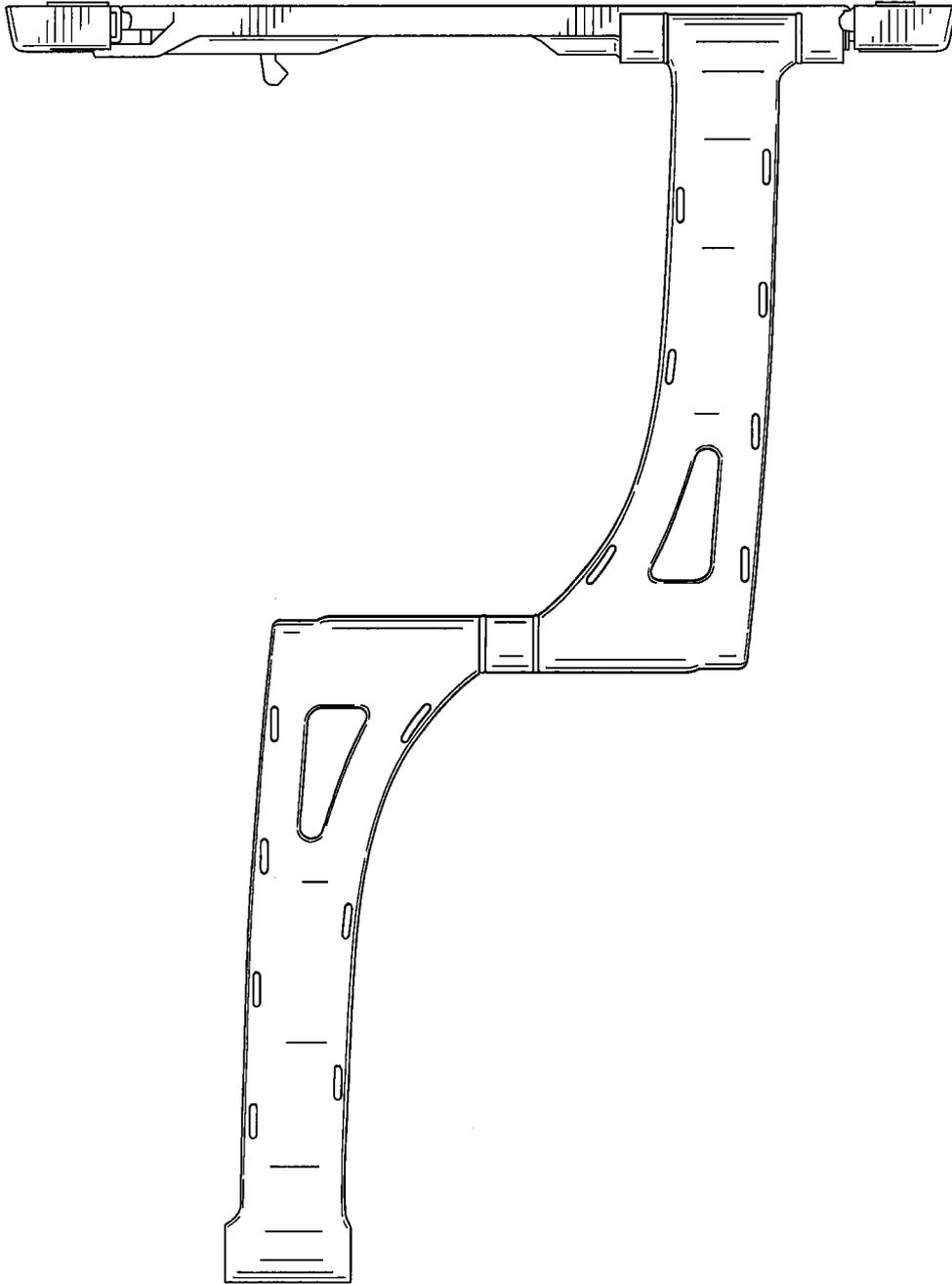


FIG. 5

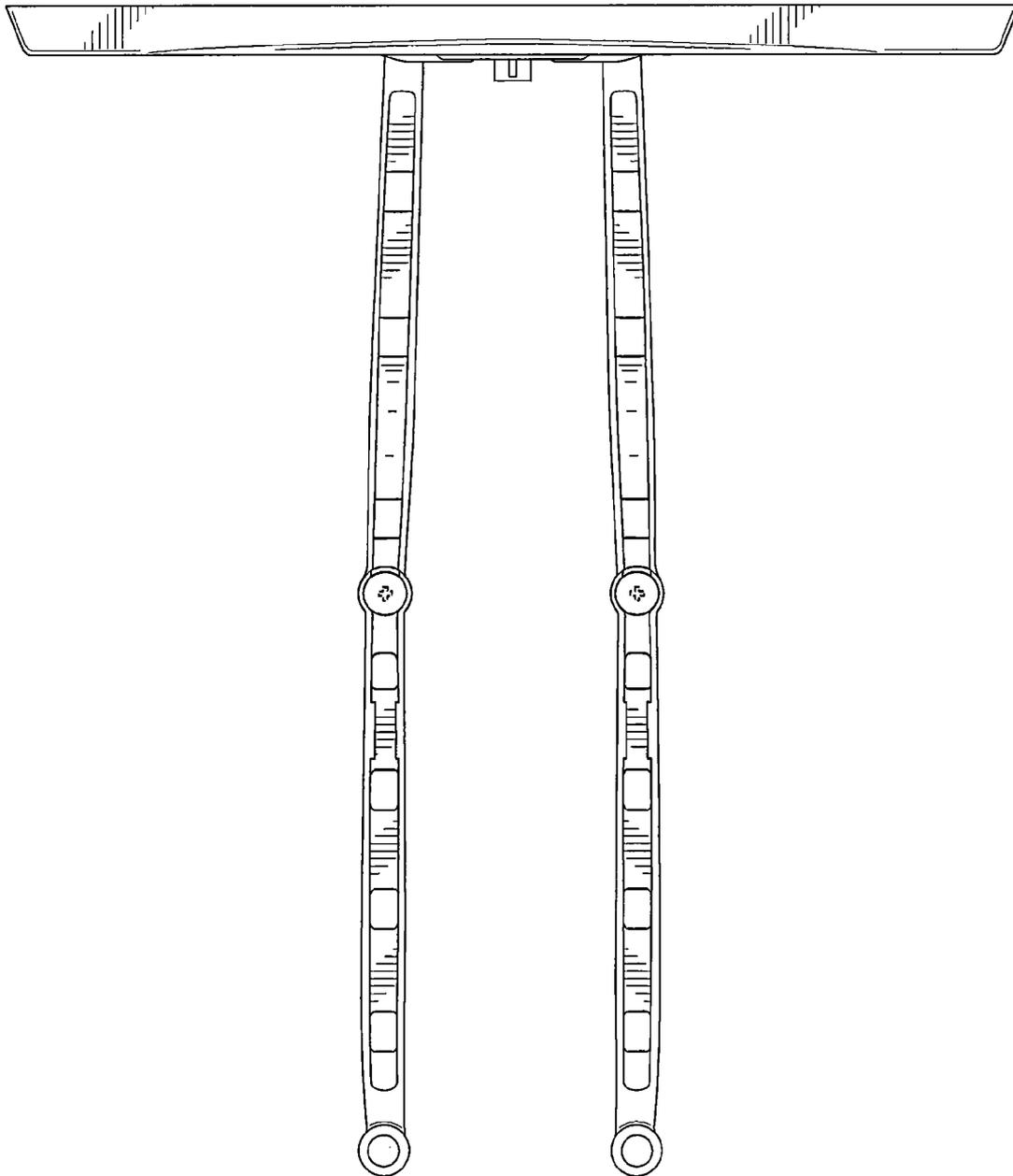


FIG. 6

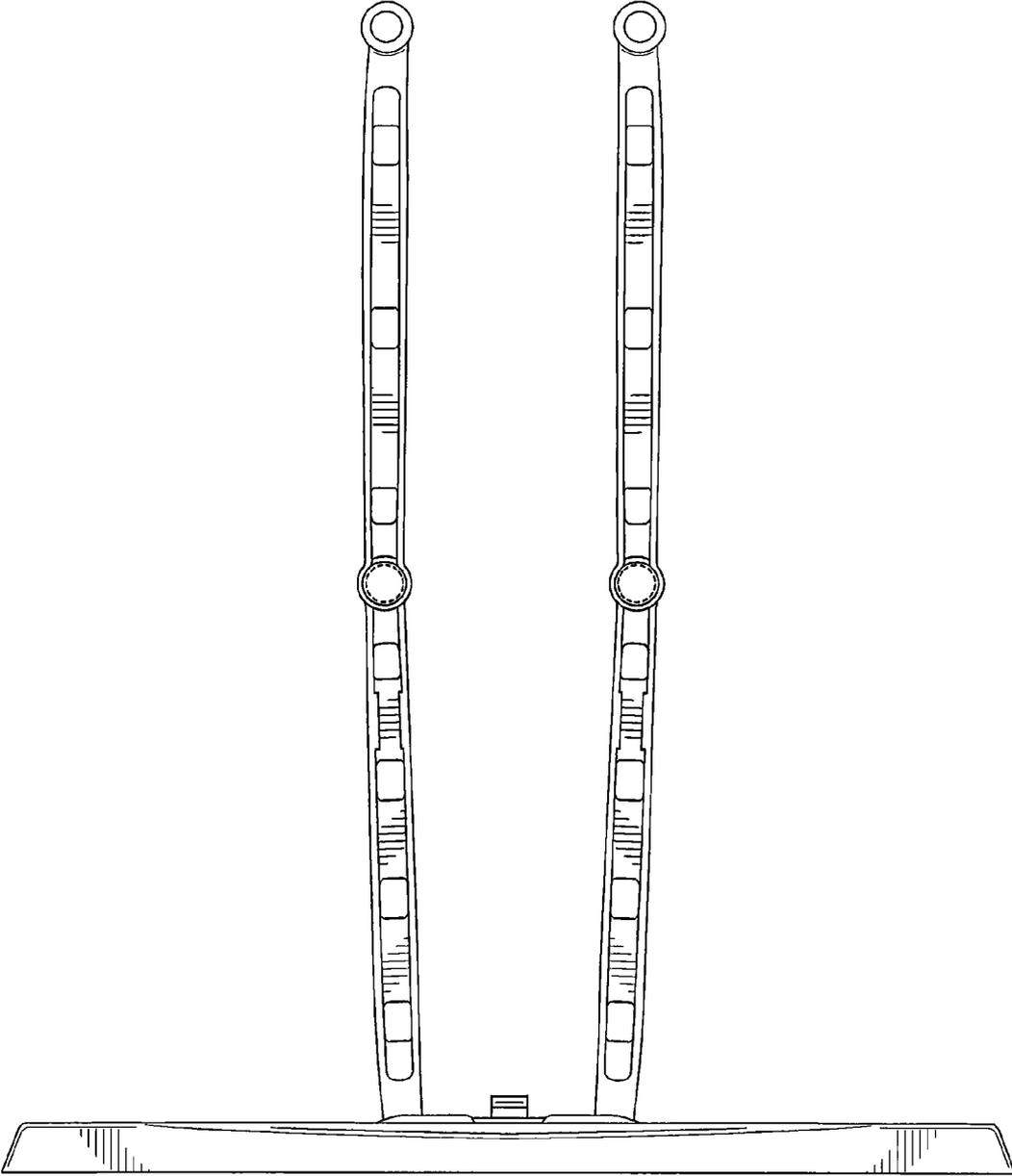


FIG. 7

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D747,724 S
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INVENTOR(S) : Kulkarni et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Title Page:

Item (71), Applicant: Please correct “WirePath Home Systems, LLC Charlotte, NC (US)”
to read -- WirePath Home Systems, LLC d/b/a SnapAV Charlotte, NC (US) --

Item (73), Assignee: Please correct “WirePath Home Systems, LLC Charlotte, NC (US)”
to read -- WirePath Home Systems, LLC d/b/a SnapAV Charlotte, NC (US) --

Signed and Sealed this
Ninth Day of August, 2016



Michelle K. Lee
Director of the United States Patent and Trademark Office