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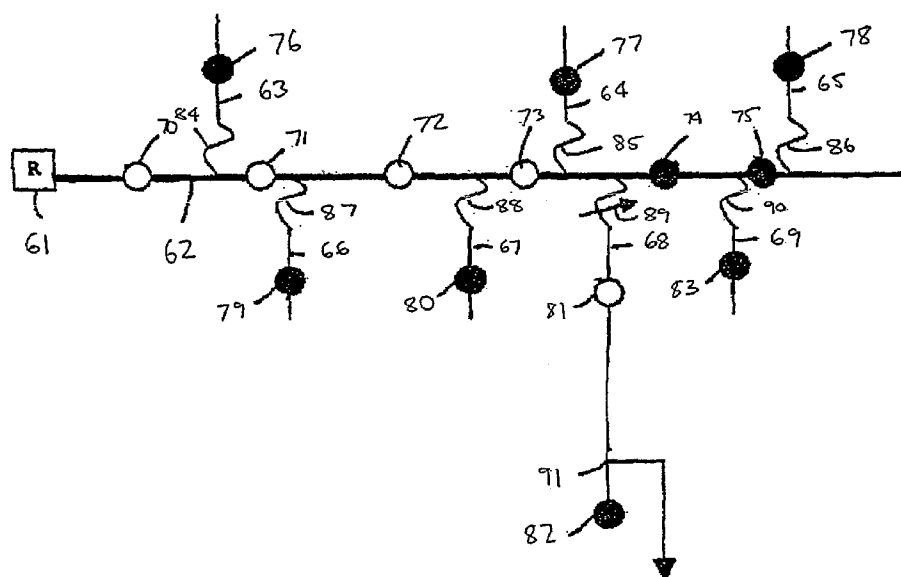
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(54) Title: METHOD OF DETECTING FAULTS USING GRADUATED FAULT DETECTION LEVELS



(57) Abstract: A faulted circuit indicator utilizes digitized fault indication curves to determine if a fault has occurred in a monitored conductor. Fault indication curves are intended to be similar to trip curves of common power protection devices, and consist of a plurality of associated fault current levels and fault times. The faulted circuit indicator only displays a fault when the current within the monitored conductor exceeds one of the fault current levels for its associated fault time. This results in more accurate fault detection through the ability to distinguish between small and large amplitude faults coupled with the flexibility of a fault indicator suitable for use on many different power systems.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER IPC (8) - H02H 03/00 (2007.01) USPC - 361/87 According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) 361/87 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched 361/1, 3, 5, 62, 63, 78, 79 Text search (see terms below) Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) PubWEST (USPT, PGPB, EPAB, JPAB); DialogPRO (Engineering); Google Scholar Search terms: faulted indicator, housing, current acquisition circuit, monitor, detect, sensor, average circuit, processor, controller, de-energization, re-energize				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
X	US 6,949,921 B1 (Feight et al.) 27 September 2005 (27.09.2005), entire document, figure 14	1-11		
☐ Further documents are listed in the continuation of Box C. ☐				
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