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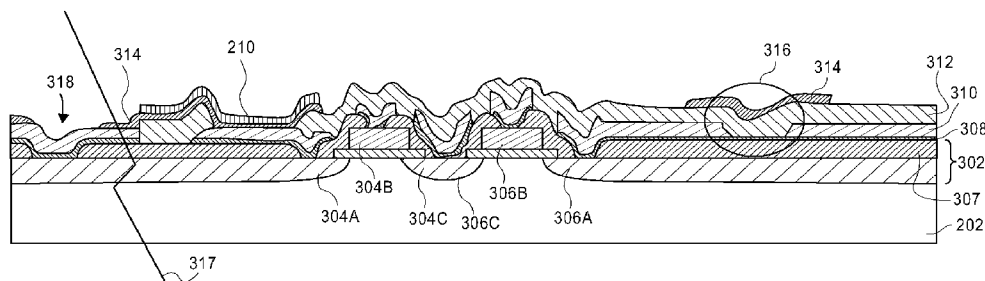
- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
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**FIG. 3**



(57) Abstract: A printhead assembly for an inkjet-printing device includes a printhead die and a flexible circuit connected to the printhead die. The printhead die includes a substrate, a first ground network electrically connected to the substrate, a device layer, and a second ground network electrically connected to the device layer. The first ground network and the second ground network are electrically isolated from one another within the printhead die. The first ground network and the second ground network are electrically connected to one another at the flexible circuit.



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## BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a diagram of a representative inkjet-printing device printhead assembly, according to an embodiment of the present disclosure.

FIG. 2 is a diagram of an inkjet-printing device printhead assembly schematically showing a first ground network and a second ground network that are electrically isolated from one another within a printhead die, and that are electrically connected to one another at a flexible circuit, according to an embodiment of the present disclosure.

FIG. 3 is a cross-sectional diagram depicting the layers of an inkjet-printing device printhead die in detail, according to an embodiment of the present disclosure.

FIG. 4 is a flowchart of a method for at least partially fabricating an inkjet-printing device printhead assembly, according to an embodiment of the present disclosure.

FIG. 5 is a block diagram of a rudimentary inkjet-printing device, according to an embodiment of the present disclosure.

## DETAILED DESCRIPTION OF THE DRAWINGS

FIG. 1 shows a representative inkjet-printing device printhead assembly 100, according to an embodiment of the present disclosure. The printhead assembly 100 includes an enclosure cartridge 102. The enclosure cartridge 102, and thus the printhead assembly 100, is insertable into a corresponding slot of an inkjet-printing device, so that the device can eject ink on a medium like paper to form an image on the medium.

The printhead assembly 100 includes a printhead die 104 that is electrically connected to a flexible circuit 106 of the assembly 100. The printhead die 104 is typically a small semiconductor die, which is depicted in FIG. 1 as being proportionally larger than it actually is in relation to the flexible circuit 106 and the enclosure cartridge 102 for illustrative clarity. The flexible circuit 106 electrically mates to a corresponding electrical connector of an inkjet-printing device upon the enclosure cartridge 102 being removably inserted or installed

into the inkjet-printing device. The flexible circuit 106 specifically can include conductor traces from the printhead die 104 so that the die 104 can be electrically coupled to the inkjet-printing device. The circuit 106 is flexible so that it can bend around one or more edges of the enclosure cartridge 102, as depicted in FIG. 1.

In the embodiment of FIG. 1, the printhead assembly 100 also includes a supply of ink 108, which is contained within the interior of the enclosure cartridge 102. However, in another embodiment, the supply of ink 108 may be contained in an assembly that is separate from the printhead assembly 100. In general, the inkjet-printing device into which the printhead assembly 100 has been installed causes the printhead die 104 to eject droplets of the ink 108 through the die to form an image on a medium like paper.

FIG. 2 shows a schematic view of a portion of the inkjet-printing device printhead assembly 100, according to an embodiment of the present disclosure. Specifically, the printhead die 104 and the flexible circuit 106 of the printhead assembly 100 are shown in FIG. 2. The printhead die 104 is depicted as including a substrate 202, such as a silicon substrate. The substrate 202 is the substrate of the printhead die 104 on which various devices, such as transistors and a heating resistor, of the die 104 are fabricated. The substrate 202 is electrically connected to what is referred to as a first ground network 206. That is, the first ground network 206 is electrically connected to a number of portions of the substrate 202.

The printhead die 104 is also depicted as including device grounds 208 and a surface metal layer 210. The device grounds 208 are the ground connections for the devices fabricated on the printhead die 104, such as the grounds of the various transistors that may be fabricated on the printhead die 104. The surface metal layer 210 may specifically be a layer of gold. The surface metal layer 210 in one embodiment provides a low-resistance conductor for power and ground signals within the printhead die 104. The device grounds 208 and the surface metal layer 210 are electrically connected to what is referred to as a second ground network 212.

The second ground network 212 can be considered a primary ground network, while the first ground network 206 can be considered a secondary or a “quiet” ground network, in that during operation of the printhead die 104, significantly more current flows through the second ground network 212 than through the first ground network 206. It is noted that within the printhead die 104 itself, the first ground network 206 and the second ground network 212 are electrically isolated from one another. This is advantageous, because in some processes employed during fabrication of the printhead die 104, such as etching, the second ground network 212 is desirably at a different electrical potential than the first ground network 206. As such, having the ground networks 206 and 212 electrically isolated from one another within the printhead die 104 is advantageous during fabrication of the die 104.

However, during operation of the printhead die 104, the first ground network 206 and the second ground network 212 are desirably both maintained at the same electrical potential, specifically common or ground, such as earth ground. The embodiment of FIG. 2 electrically connects the ground networks 206 and 212 with each other at the flexible circuit 106. Specifically, the ground networks 206 and 212 are shorted together at one or more points 214 within the flexible circuit 106. The points 214 may be implemented as an inkjet-printing device connector pin, for instance, that electrically connects the printhead die 104 to the inkjet-printing device in which the printhead assembly 100 is inserted or installed.

Thus, the embodiment of FIG. 2 provides for the at least substantially optimal electrical potentials at the ground networks 206 and 212 both during fabrication of the printhead die 104 and during operation of the printhead die 104. During fabrication of the printhead die 104, the ground networks 206 and 212 are electrically isolated, and therefore can be at different electrical potentials. During operation of the printhead die 104, the ground networks 206 and 212 are electrically connected with one another at the flexible circuit 106, and therefore are maintained at the same ground or common electrical potential.

FIG. 3 shows a cross section of a portion of the printhead die 104, according to an embodiment of the invention. Disposed over the printhead die 104 is a device layer 302. The device layer 302 includes a number of thin-film transistors. For instance, one transistor includes a source 304A, a polysilicon gate 304B, and a drain 304C, where there is a small layer of gate oxide (which is not specifically called out in FIG. 3) between the gate 304B and the source 304A and the drain 304C. Another transistor includes a source 306A, a polysilicon gate 306B, and a drain 306C, where there is a small layer of gate oxide between the gate 306B and the source 306A and the drain 306C. The drain 304C is the same as the drain 306C.

The device layer 302 can also be said to include a heating resistor 316, although in FIG. 3 the heating resistor 316 is depicted as being over the demarcated device layer 302 for illustrative convenience. As can be appreciated by those of ordinary skill within the art, when current is provided to the heating resistor 316, the resistor 316 is said to be "fired." As such, the resistor 316 causes a bubble to form within ink situated on the top side of the printhead die 104. This bubble ejects a droplet of the ink from the die 104. Thereafter, the bubble collapses. The device layer 302 can further be said to include an insulating layer 307 in one embodiment, which may be phosphosilicate glass (PSG) in one embodiment.

Disposed over the device layer 302 is a thin resistive layer 308, over which a first metal layer 310 is disposed. The first metal layer 310 may, for instance, be aluminum and/or a tantalum-aluminum alloy, such that the layer 310 has two sub-layers, one of aluminum and one of a tantalum-aluminum alloy. Disposed over the first metal layer 310 is a passivation and/or insulating layer 312, which protects the printhead die 104 from the ink. The layer 312 may, for instance, be silicon carbide or silicon nitride. The heating resistor 316 can be said to include a portion of the insulating layer 307, a portion of the resistive layer 308, a portion of the first metal layer 310, a portion of the layer 312, and/or a portion of an additional protecting layer 314 disposed over the passivation layer 312.

Disposed over the device layer 302 – specifically over the first metal layer 310 – is the surface metal layer 210, which can be a sub-layer of a second metal layer that also includes a tantalum layer. The surface metal layer 210 is separated and electrically insulated from the first metal layer 310 by a portion of the layer 312. The surface metal layer 210 is electrically connected to the grounds of the transistors within the device layer 302, and may also be electrically connected to the main power ground as well as other grounds, for instance, although none of these electrical connections are visible in the cross-sectional profile of FIG. 3. However, the flexible circuit 106 of FIGS. 1 and 2 is electrically connected to the second ground network 212 of FIG. 2 via the surface metal layer 210. It can also be said that the second ground network 212 is implemented at the second metal layer that includes the surface metal layer 210. It can further be said that the second ground network 212 is not primarily implemented at the first metal layer 310.

A breakaway line 317 indicates that the portions to the left of the line 317 in FIG. 3 are located farther away on the printhead die 104 from the portions to the right of the line 317 in FIG. 3 than is specifically shown in FIG. 3. The portions to the left of the line 317 include a substrate contact 318. The contact 318 exposes a portion of the first metal layer 310, and there is none of the passivation layer 312, the protecting layer 314, and the insulating layer 307 at this location. The first metal layer 310 at the contact 318 thus electrically exposes the substrate 202, since the two layers above the substrate 202 at this location – the thin resistive layer 308 and the first metal layer 310 – are both electrically conductive. The flexible circuit of FIGS. 1 and 2 is electrically connected to the first ground network 206 of FIG. 2 via the first metal layer 310. It can also be said that the first ground network 206 is primarily implemented at the first metal layer 310.

Therefore, FIG. 3 shows how the ground networks 206 and 212 of FIG. 2 are electrically isolated from one another within the printhead die 104 itself. The surface metal layer 210, for instance, is electrically isolated from the portion of the first metal layer 310 at which the contact 318 is located. As such, insofar as

the second ground network 212 is implemented at the second metal layer that includes the surface metal layer 210, and the first ground network 206 is primarily implemented at the first metal layer 310, the ground networks 206 and 212 are electrically isolated from one another within the printhead die 104 itself.

5           FIG. 4 shows a method 400 for at least partially fabricating the inkjet-printing device printhead assembly 100, according to an embodiment of the present disclosure. It is noted that just some parts of the fabrication process are particularly depicted in FIG. 4 and described herein. Those of ordinary skill within the art can thus appreciate that other parts may be performed to complete  
10 the fabrication of the printhead assembly 100. In particular, just the parts relevant to embodiments of the present disclosure are depicted in FIG. 4 and described herein.

The substrate 202 for the printhead die 104 of the printhead assembly 100 is provided (402). Thereafter, the device layer 302, including the thin-film  
15 transistors and/or the heating resistor 316, may be formed over the substrate (404). The first metal layer 310 at some time thereafter is formed over the device layer 302 (406), where the first ground network 206 is primarily implemented at the first metal layer 310 as has been described. Ultimately, the surface metal layer 210 is formed over the first metal layer 310 (408), where the second ground  
20 network 212 is implemented at the second metal layer that includes the surface metal layer 210 as has been described.

The substrate 202 can be etched such that the first ground network 206 and the second ground network 212 are at different potentials (410). For instance, the substrate 202 may be wet-etched using tetramethylammonium  
25 hydroxide (TMAH). It has been found that TMAH etching the substrate 202 is optimally performed when the surface metal layer 210 (i.e., the second ground network 212) is at a potential in relation to the substrate 202 (i.e., the first ground network 206). Otherwise, the substrate 202 may be etched improperly. The substrate 202 may be etched to create a hole for feeding ink through the  
30 printhead die 104, and/or to create a clean and smooth edge near the heating resistor 316, as can be appreciated by those of ordinary skill within the art.

Embodiments of the invention permit the surface metal layer 210 to be at a potential in relation to the substrate 202, insofar as the substrate 202 and the surface metal layer 210 (i.e., the first ground network 206 and the second ground network 212) are electrically isolated from one another within the printhead die 104 itself, prior to the flexible circuit 106 being attached to the die 104.

Once etching has been completed, the flexible circuit 106 may be connected to the printhead die 104 (412), such that the first ground network 206 and the second ground network 212 become electrically connected to one another. As such, when the printhead assembly 100 is being used, the ground networks 206 and 212 (i.e., the surface metal layer 210 and the substrate 202 or the first metal layer 310) can be maintained at the same ground or other common potential, which has been found to result in optimal operation of the assembly 100. Thus, during usage of the printhead assembly 100, the ground networks 206 and 212 remain electrically connected to one another due to their being electrically connected to each other at the flexible circuit 106.

In conclusion, FIG. 5 shows a rudimentary inkjet-printing device 500, according to an embodiment of the present disclosure. The inkjet-printing device 500 may be an inkjet printer, or a multifunction device (MFD) or an all-in-one (AIO) that can include other functionality in addition to inkjet-printing functionality. The inkjet-printing device 500 is depicted in FIG. 5 as including the printhead assembly 100 that has been described and an inkjet-printing mechanism 502. Those of ordinary skill within the art can appreciate that the inkjet-printing device 500 can and typically will include other components, in addition to those depicted in FIG. 5.

The inkjet-printing mechanism 502 includes those components by which the inkjet-printing device 500 forms images on media such as paper by, for instance, thermally ejecting ink onto the media. The printhead assembly 100 may thus share components with the inkjet-printing mechanism 502. That is, the printhead assembly 100 includes the printhead die 104 that actually causes ink to be ejected. To this extent, the inkjet-printing mechanism 502 can be said to share the printhead die 104 with the printhead assembly 100. Other components

that the inkjet-printing mechanism 502 can include are firmware, media advancement motors, and so on, as can be appreciated by those of ordinary skill within the art.

We claim:

1. A printhead assembly for an inkjet-printing device, comprising:  
a printhead die comprising:  
a substrate;  
5 a first ground network electrically connected to the substrate;  
a device layer;  
a second ground network electrically connected to the device layer,  
wherein the first ground network and the second ground network are electrically  
isolated from one another within the printhead die; and,  
10 a flexible circuit connected to the printhead die, wherein the first ground  
network and the second ground network are electrically connected to one  
another at the flexible circuit.
2. The printhead assembly of claim 1, wherein during fabrication of the  
15 printhead die, the first ground network is temporarily at a different electrical  
potential than the second ground network.
3. The printhead assembly of claim 2, wherein the first ground network is  
temporarily at a different electrical potential than the second ground network  
during etching of the substrate.
4. The printhead assembly of claim 1, wherein the printhead die further  
20 comprises a first metal layer in which the first ground network is primarily  
implemented.
5. The printhead assembly of claim 4, wherein the first metal layer is one or  
more of a tantalum-aluminum alloy layer and an aluminum layer.

6. The printhead assembly of claim 4, wherein the printhead die further comprises a second metal layer in which the second ground network is implemented.
7. The printhead assembly of claim 6, wherein the second metal layer  
5 comprises a surface metal layer of the printhead die.
8. The printhead assembly of claim 6, wherein the second metal layer comprises a gold layer.
9. The printhead assembly of claim 1, wherein the substrate is a silicon substrate.
- 10 10. The printhead assembly of claim 1, wherein the device layer comprises one or more transistors, and a heating resistor to cause ink to be ejected from the printhead assembly.
11. The printhead assembly of claim 1, wherein the flexible circuit electrically connects the printhead die to the inkjet-printing device.
- 15 12. A method comprising:  
providing a substrate for a printhead die for a printhead assembly for an inkjet-printing device;  
forming a device layer over the substrate, the device layer including one or more transistors, and a heating resistor to cause ink to be ejected from the  
20 printhead assembly;  
forming a first metal layer over the device layer, the first metal layer providing a first ground network electrically connected to the substrate;  
forming a second metal layer over the first metal layer, the second metal layer providing a second ground network electrically connected to the device

layer such that the second ground network is electrically isolated from the first ground network within the printhead die; and,

etching the substrate such that the first ground network is maintained at a different electrical potential than the second ground network during etching.

5 13. The method of claim 12, further comprising attaching a flexible circuit to the printhead die, such that the first ground network and the second ground network become electrically connected to one another at the flexible circuit.

14. The method of claim 13, wherein the flexible circuit is to electrically connect the printhead die to the inkjet-printing device.

10 15. The method of claim 13, wherein during usage of the inkjet-printing device, the first ground network and the second ground network remain electrically connected to one another due to the first ground network and the second ground network being electrically connected to one another at the flexible circuit.

15 16. An inkjet-printing device comprising:

an inkjet-printing mechanism; and,

a cartridge installed within the inkjet-printing device, the cartridge comprising:

a supply of ink;

20 a printhead die comprising:

a substrate;

a first ground network electrically connected to the substrate;

a device layer;

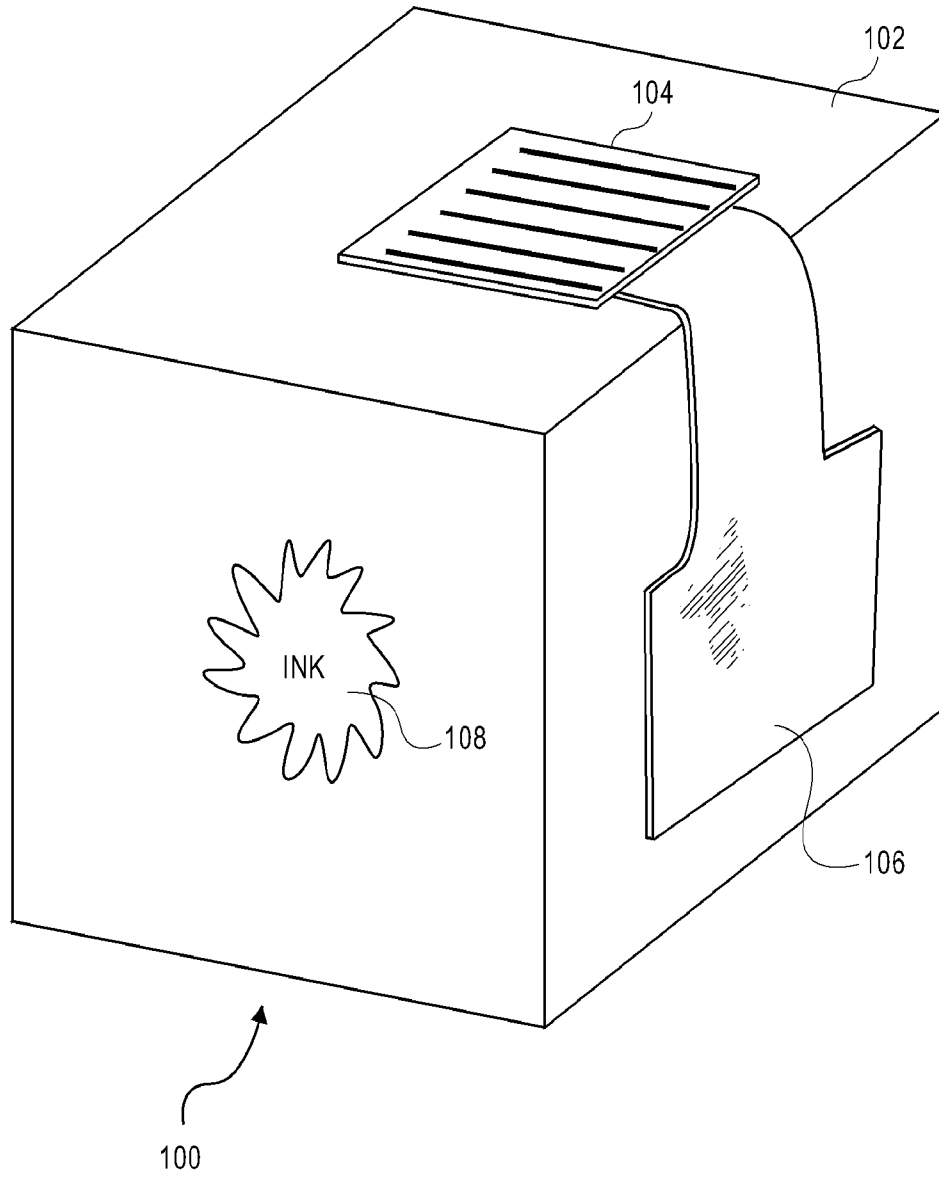
a second ground network electrically connected to the

25 device layer, wherein the first ground network and the second ground network are electrically isolated from one another within the printhead die; and,

a flexible circuit electrically connecting the printhead die to the

inkjet-printing mechanism, wherein the first ground network and the second ground network are electrically connected to one another at the flexible circuit, wherein the inkjet-printing mechanism causes the ink to be ejected via the printhead die onto a medium to form an image on the medium.

- 5 17. The inkjet-printing device of claim 16, wherein during fabrication of the printhead die, the first ground network is temporarily at a different electrical potential than the second ground network.
18. The inkjet-printing device of claim 17, wherein the first ground network is temporarily at a different electrical potential than the second ground network  
10 during etching of the substrate.
19. The inkjet-printing device of claim 16, wherein the printhead die further comprises:  
a first metal layer in which the first ground network is primarily implemented; and,  
15 a second metal layer in which the second ground network is implemented and in which the first metal layer is not primarily implemented,  
wherein the second metal layer is a surface metal layer of the printhead die.
20. The inkjet-printing device of claim 16, wherein the device layer comprises  
20 one or more transistors, and a heating resistor.



**FIG. 1**

FIG. 2

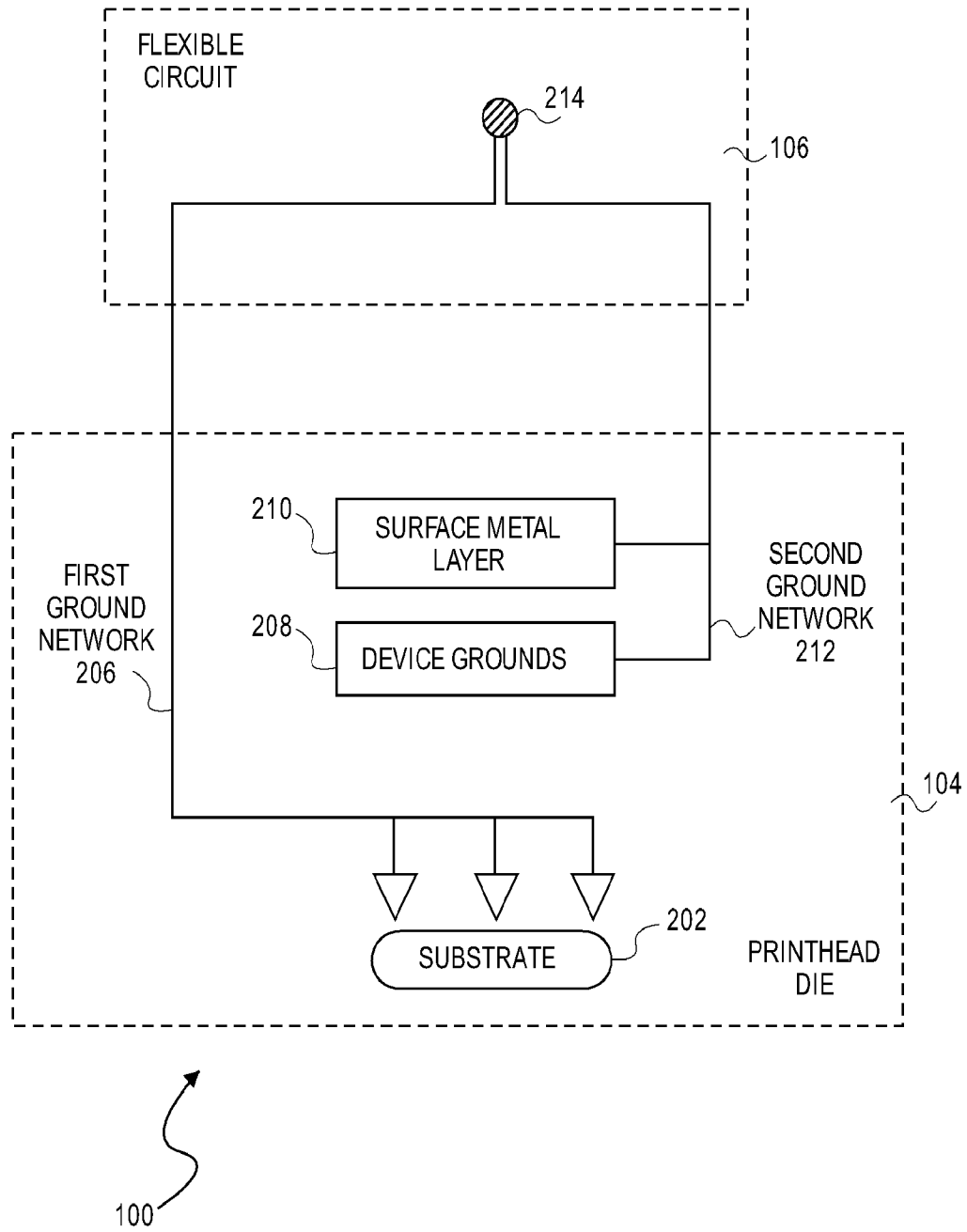
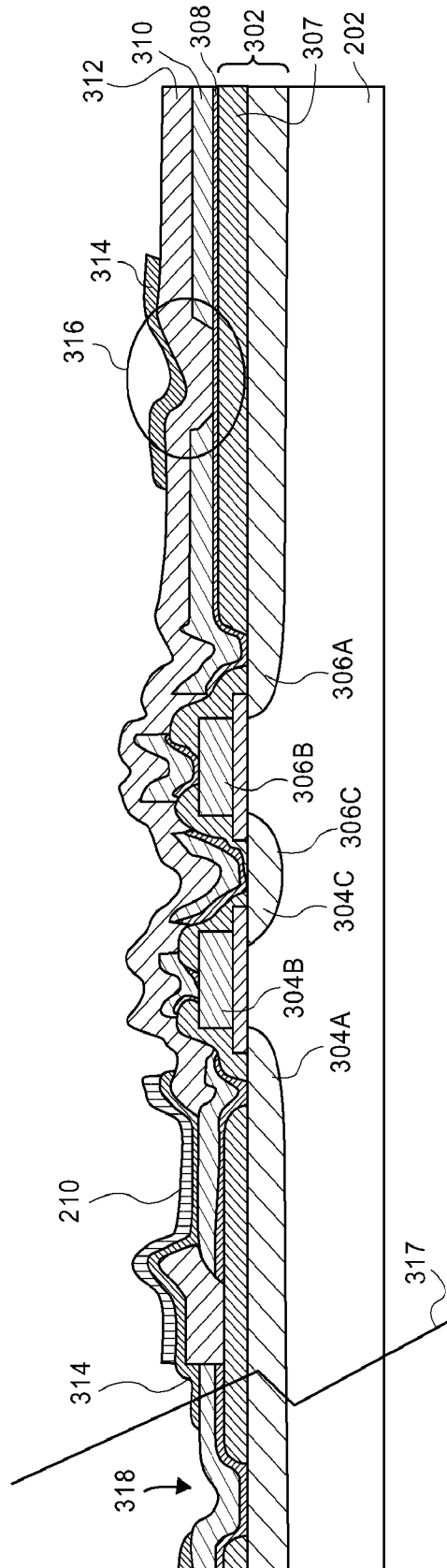
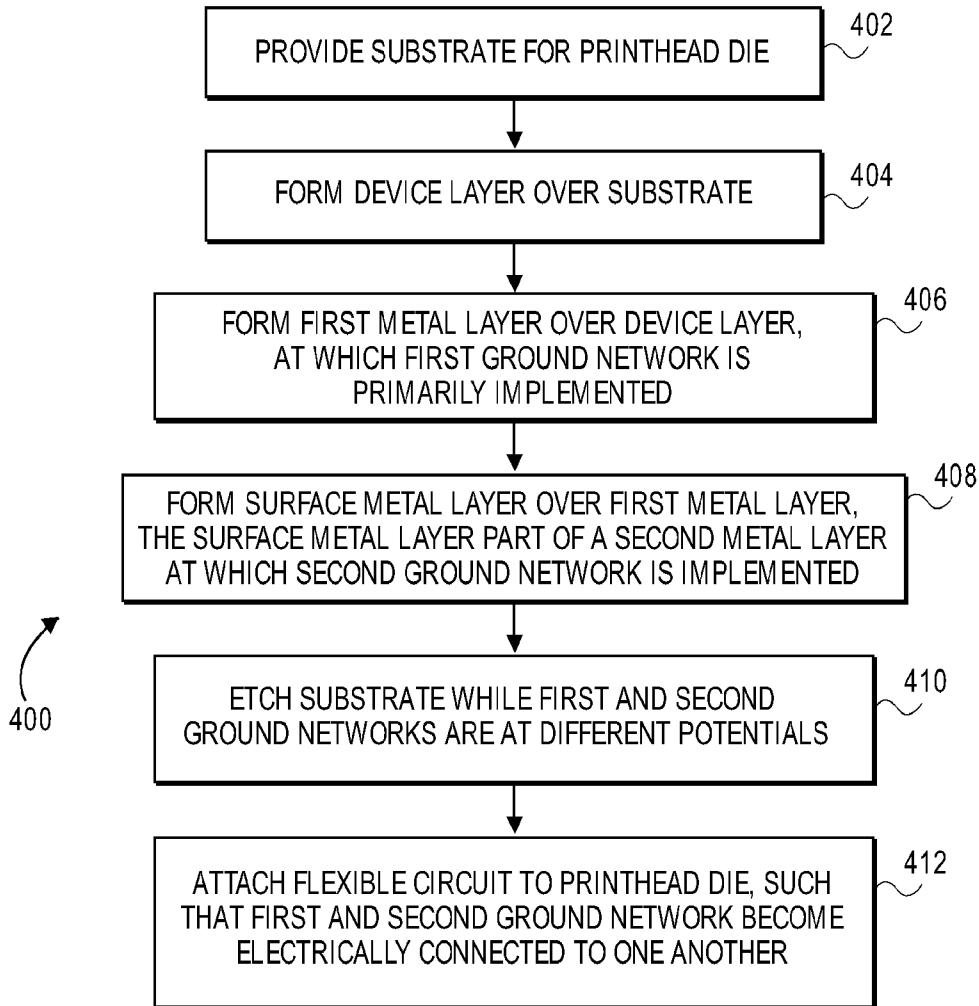


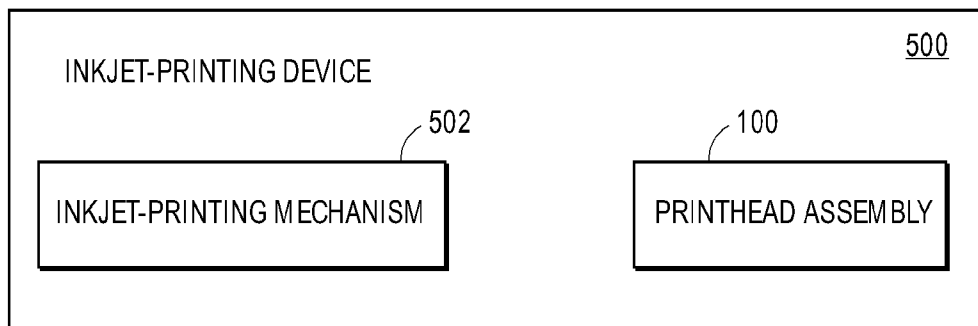
FIG. 3



**FIG. 4**



**FIG. 5**



## INTERNATIONAL SEARCH REPORT

International application No.  
**PCT/US2007/086210****A. CLASSIFICATION OF SUBJECT MATTER***B41J 2/16(2006.01)i, B41J 2/015(2006.01)i, B41J 2/01(2006.01)i*

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 8 : B41J2/16, B41J2/015, B41J2/01

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models since 1975  
Japanese utility models and applications for utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKIPASS (KIPO internal) &amp; keywords: "ground" and "etching"

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 7,267,430 B2 (PARISH G. K.) 11 Sep. 2007 See the whole documents.	1-20
A	JP 2001-199092 A (AOI ELECTRONICS CO., LTD.) 24 Jul. 2001 See the whole documents.	1-20
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 Further documents are listed in the continuation of Box C. See patent family annex.

\* Special categories of cited documents:

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Date of the actual completion of the international search

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**INTERNATIONAL SEARCH REPORT**

Information on patent family members

International application No.

**PCT/US2007/086210**

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