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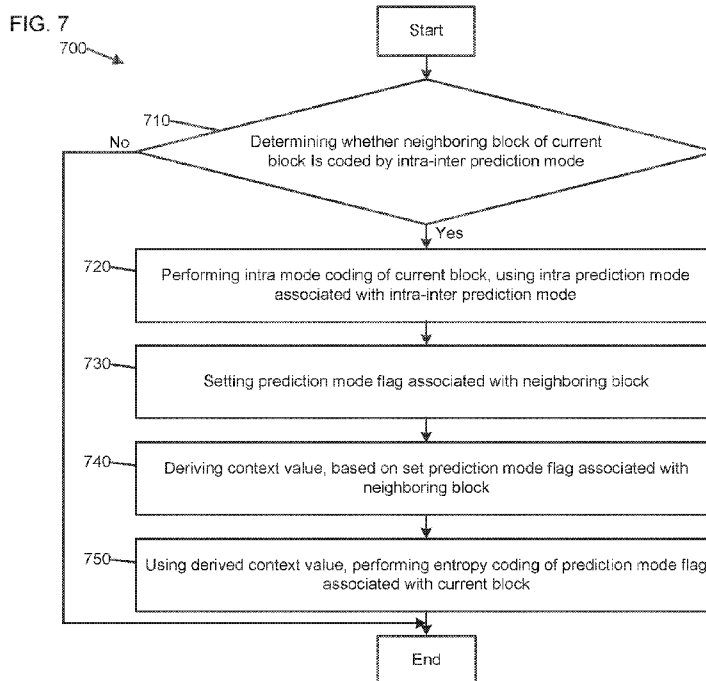
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(54) Title: METHOD AND APPARATUS FOR IMPROVED CONTEXT DESIGN FOR PREDICTION MODE AND CODED BLOCK FLAG (CBF)



(57) Abstract: A method of controlling intra-inter prediction for decoding or encoding of a video sequence, is performed by at least one processor. The method includes determining whether a neighboring block of a current block is coded by an intra-inter prediction mode, and based on the neighboring block being determined to be coded by the intra-inter prediction mode, performing intra mode coding of the current block, using an intra prediction mode associated with the intra-inter prediction mode, setting a prediction mode flag associated with the neighboring block, deriving a context value, based on the set prediction mode flag associated with the neighboring block, and using the derived context value, performing entropy coding of a prediction mode flag associated with the current block that indicates that the current block is intra coded.



SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN,  
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## A. CLASSIFICATION OF SUBJECT MATTER

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CPC - H04N 19/105, 19/11, 19/159, 19/52, 19/61

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
See Search History documentDocumentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
See Search History documentElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
See Search History document

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2007/0009044 A1 (TOURAPIS, A et al.) 11 January 2007; entire document	1-20
A	US 10091502 B2 (ELECTRONICS AND TELECOMMUNICATIONS RESEARCH INSTITUTE) 02 October 2018; entire document	1-20
A	EP 3293975 A1 (SAMSUNG ELECTRONICS CO., LTD.) 14 March 2018; entire document	1-20

 Further documents are listed in the continuation of Box C. See patent family annex.

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