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(12) **United States Design Patent**
Jones

(10) **Patent No.:** **US D548,201 S**

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(54) **ION SOLENOID CIRCUIT BOARD**

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(57) **CLAIM**

(**) Term: **14 Years**

The ornamental design for an ion solenoid circuit board, as shown and described.

(21) Appl. No.: **29/253,164**

(22) Filed: **Jan. 31, 2006**

DESCRIPTION

(51) **LOC (8) Cl.** **13-03**

FIG. 1 is a perspective view of an ion solenoid circuit board;

(52) **U.S. Cl.** **D13/182**

FIG. 2 is a top plan view of an ion solenoid circuit board;

(58) **Field of Classification Search** D13/182;
174/252, 253, 255; 361/760, 820

FIG. 3 is a left side elevation view of an ion solenoid circuit board;

See application file for complete search history.

FIG. 4 is a front elevation view of an ion solenoid circuit board;

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,300,165	B2 *	10/2001	Castro	438/118
6,492,598	B1 *	12/2002	Klein	174/256
6,979,595	B1 *	12/2005	James et al.	438/115
7,142,433	B2 *	11/2006	Lechner et al.	361/797
2002/0007961	A1 *	1/2002	Yamato et al.	174/250

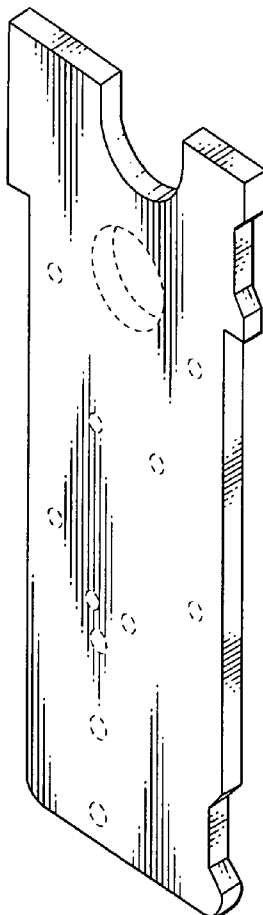
FIG. 5 is a right side elevation view of an ion solenoid circuit board; and,

FIG. 6 is a bottom plan view of an ion solenoid circuit board.

The broken line showing of the environment is for illustrative purpose only and forms no part of the claimed design.

* cited by examiner

1 Claim, 2 Drawing Sheets



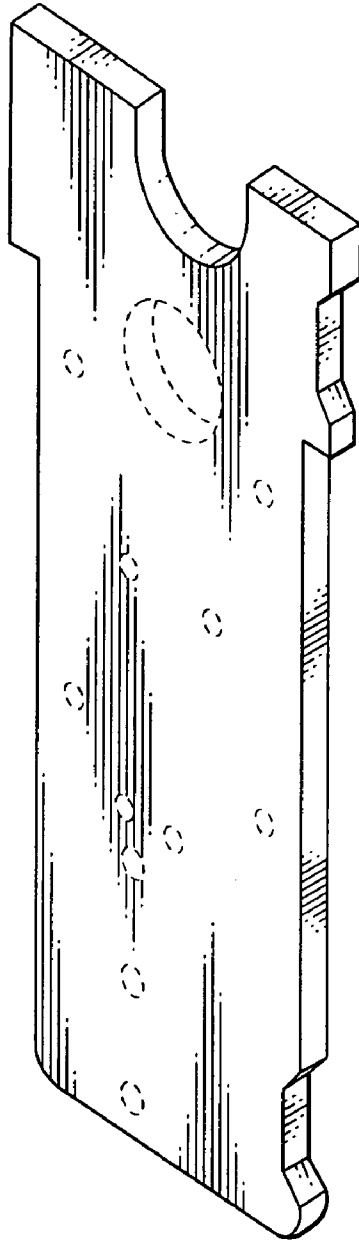


FIG. 1



FIG. 2



FIG. 3

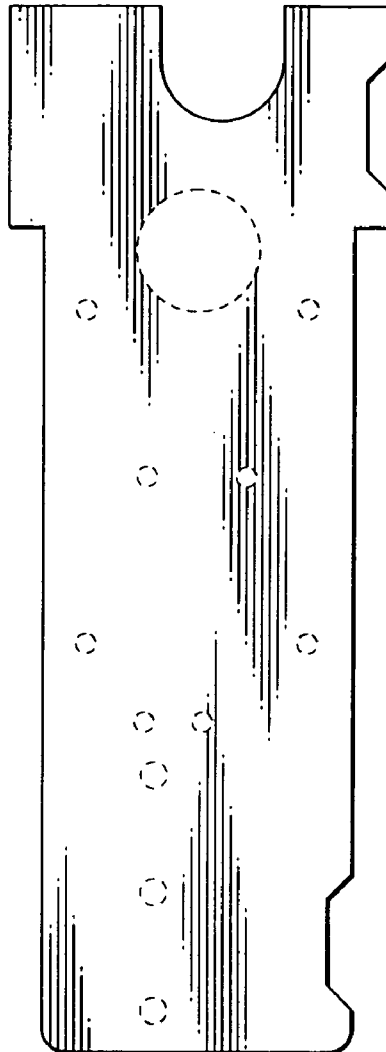


FIG. 4



FIG. 5

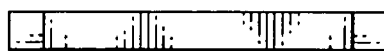


FIG. 6