

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization

International Bureau



(10) International Publication Number

WO 2017/053316 A1

(43) International Publication Date
30 March 2017 (30.03.2017)

WIPO | PCT

(51) International Patent Classification:
H01L 21/027 (2006.01) *G03F 1/70* (2012.01)
G03F 7/20 (2006.01)

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(21) International Application Number:
PCT/US2016/052694

(22) International Filing Date:
20 September 2016 (20.09.2016)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
62/232,005 24 September 2015 (24.09.2015) US
62/258,119 20 November 2015 (20.11.2015) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: METHODS OF FORMING ETCH MASKS FOR SUB-RESOLUTION SUBSTRATE PATTERNING

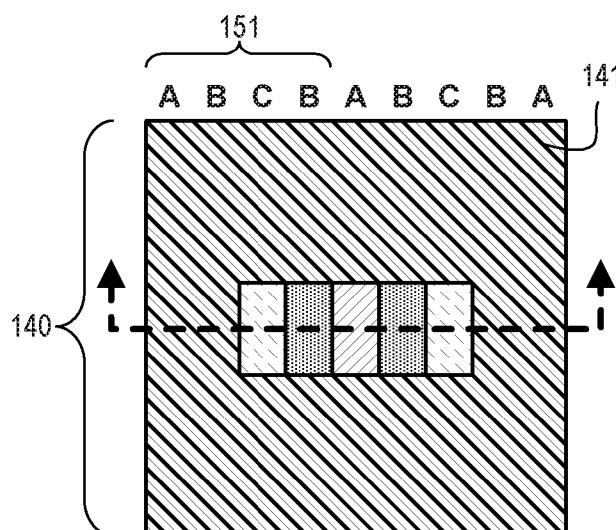


FIG. 4A

(57) Abstract: Techniques disclosed herein provide a method and fabrication structure for pitch reduction for creating high-resolution features and also for cutting on pitch of sub-resolution features. Techniques include using multiple materials having different etch characteristics to selectively etch features and create cuts or blocks where specified. A pattern of alternating materials is formed on an underlying layer. An etch mask is positioned on the pattern of alternating materials. One or more of the alternating materials can be preferentially removed relative to other materials to uncover a portion of the underlying layer. The etch mask and the remaining lines of alternating material together form a combined etch mask defining sub-resolution features.



Published:

— *with international search report (Art. 21(3))*

Methods of Forming Etch Masks for Sub-Resolution Substrate Patterning

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] The present application claims the benefit of U.S. Provisional Patent Application No. 62/258,119, filed on November 20, 2015, entitled "Methods of Forming Etch Masks for Sub-Resolution Substrate Patterning," which is incorporated herein by reference in its entirety. The present application also claims the benefit of U.S. Provisional Patent Application No. 62/232,005, filed on September 24, 2015, entitled "Methods of Forming Etch Masks for Sub-Resolution Substrate Patterning," which is incorporated herein by reference in its entirety.

BACKGROUND OF THE INVENTION

[0002] This disclosure relates to substrate processing, and, more particularly, to techniques for patterning substrates including patterning semiconductor wafers.

[0003] Methods of shrinking line-widths in lithographic processes have historically involved using greater-NA optics (numerical aperture), shorter exposure wavelengths, or interfacial media other than air (e.g., water immersion). As the resolution of conventional lithographic processes has approached theoretical limits, manufacturers have started to turn to double-patterning (DP) methods to overcome optical limitations.

[0004] In material processing methodologies (such as photolithography), creating patterned layers comprises the application of a thin layer of radiation-sensitive material, such as photoresist, to an upper surface of a substrate. This radiation-sensitive material is transformed into a relief pattern which can be used as an etch mask to transfer a pattern into an underlying layer on a substrate. Patterning of the radiation-sensitive material generally involves exposure to actinic radiation through a reticle (and associated optics) onto the radiation-sensitive material using, for example, a photo-lithography system. This exposure can then be followed by the removal of irradiated regions of the radiation-sensitive material (as in the case of positive photoresist), or non-irradiated regions (as in the case of negative resist) using a developing solvent. This mask layer can comprise multiple sub-layers.

[0005] Conventional lithographic techniques for exposing a pattern of radiation or light onto a substrate have various challenges that limit a size of features exposed,

and limit pitch or spacing between exposed features. One conventional technique to mitigate exposure limitations is that of using a double patterning approach to allow the patterning of smaller features at a smaller pitch than what is currently possible with conventional lithographic techniques.

SUMMARY

[0006] Semiconductor technologies are continually progressing to smaller feature sizes or nodes including feature sizes of 14 nanometers, 7 nm, 5 nm, and below. This continual reduction in sizes of features from which various elements are fabricated places ever-greater demands on techniques used to form the features. The concept of “pitch” can be used to describe the sizing of these features. Pitch is the distance between two identical points in two adjacent repetitive features. Half-pitch then is half the distance between identical features of adjacent features.

[0007] Pitch reduction techniques are termed (often somewhat erroneously yet routinely) “pitch multiplication” as exemplified by “pitch doubling” et cetera. Pitch reduction techniques can extend the capabilities of photolithography beyond feature size limitations (optical resolution limitations). That is, conventional multiplication of pitch (more accurately pitch reduction, or multiplication of pitch density) by a certain factor involves reducing a target pitch by a specified factor. Double patterning techniques used with 193 nm immersion lithography are conventionally considered as one of the most promising techniques to pattern 22 nm nodes and smaller. Noteworthy is that self-aligned spacer double patterning (SADP) has already been established as a pitch density doubling process and has been adapted in high volume manufacturing of NAND flash memory devices. Moreover, ultra-fine resolution can be obtained to repeat the SADP step twice as pitch quadrupling.

[0008] Although there exist several patterning techniques to increase pattern density or pitch density, conventional patterning techniques suffer from poor resolution or rough surfaces of etched features. Thus, conventional techniques cannot provide a level of uniformity and fidelity desired for very small dimensions (20 nm and smaller). Reliable lithographic techniques can produce features having a pitch of about 80 nm. Conventional and emerging design specifications, however, desire to fabricate features having critical dimensions less than about 20 nm or 10 nm. Moreover, with pitch density doubling and quadrupling techniques, sub-resolution lines can be created, but making cuts or connections between these lines

is challenging, especially since the pitch and dimensions needed for such cuts is far below capabilities of conventional photo-lithography systems.

[0009] Techniques disclosed herein provide a method for pitch reduction (increasing pitch/feature density) for creating high-resolution features and also for cutting on pitch of sub-resolution features. Techniques herein include positioning multiple lines of materials (multiple adjacent materials) having different etch characteristics on a substrate. Then an etch mask is formed on the multiple lines of materials to isolate a portion of those materials to selectively etch features and create cuts and blocks where desired. The multiple materials can be a pattern of alternating, sub-resolution lines, and each line can be preferentially etched relative to the other lines. The etch mask combined with one or more etched lines, provides a combined etch mask defining sub-resolution features. Thus, methods herein provide a sequence of materials that provide selective self-alignment, such as for blocking or cutting. Combined with an underlying transfer layer or memorization layer, many different etch selectivities can be accessed.

[0010] One embodiment includes a method of patterning a substrate. A multi-line layer is formed above or on an underlying layer. The multi-line layer includes a region having a pattern of alternating lines of two or more differing materials. Each line has a horizontal thickness, a vertical height, and extends across the underlying layer. Each line of the pattern of alternating lines is uncovered on a top surface of the multi-line layer and vertically extends to a bottom surface of the multi-line layer. At least two of the two or more differing materials differ chemically from each other by having different etch resistivities relative to each other. A patterned mask layer is formed on the multi-line layer. The patterned mask layer includes mask material that masks a portion of the multi-line layer. At least one of the two or more differing materials are selectively removed resulting in a portion of the underlying layer being uncovered.

[0011] Of course, the order of discussion of the different steps as described herein has been presented for clarity sake. In general, these steps can be performed in any suitable order. Additionally, although each of the different features, techniques, configurations, etc. herein may be discussed in different places of this disclosure, it is intended that each of the concepts can be executed independently of

each other or in combination with each other. Accordingly, the present invention can be embodied and viewed in many different ways.

[0012] Note that this summary section does not specify every embodiment and/or incrementally novel aspect of the present disclosure or claimed invention. Instead, this summary only provides a preliminary discussion of different embodiments and corresponding points of novelty over conventional techniques. For additional details and/or possible perspectives of the invention and embodiments, the reader is directed to the Detailed Description section and corresponding figures of the present disclosure as further discussed below.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] A more complete appreciation of various embodiments of the invention and many of the attendant advantages thereof will become readily apparent with reference to the following detailed description considered in conjunction with the accompanying drawings. The drawings are not necessarily to scale, with emphasis instead being placed upon illustrating the features, principles and concepts.

[0014] FIGS. 1A, 1B, 1C, and 1D are cross-sectional side views of an example substrate segment according to embodiments disclosed herein.

[0015] FIGS. 2A and 2B are top views of an example substrate segment according to embodiments disclosed herein.

[0016] FIGS. 3A and 3B are cross-sectional side views of an example substrate segment according to embodiments disclosed herein.

[0017] FIGS. 4A and 4B are top views of an example substrate segment according to embodiments disclosed herein.

[0018] FIGS. 5A and 5B are cross-sectional side views of an example substrate segment according to embodiments disclosed herein.

[0019] FIGS. 6A and 6B are top views of an example substrate segment according to embodiments disclosed herein.

[0020] FIGS. 7A and 7B are cross-sectional side views of an example substrate segment according to embodiments disclosed herein.

[0021] FIGS. 8A and 8B are top views of an example substrate segment according to embodiments disclosed herein.

[0022] FIGS. 9A and 9B are cross-sectional side views of an example substrate segment according to embodiments disclosed herein.

[0023] FIGS. 10A and 10B are top views of an example substrate segment according to embodiments disclosed herein.

[0024] FIGS. 11-18 are cross-sectional side views of an example substrate segment according to embodiments disclosed herein.

DETAILED DESCRIPTION

[0025] Techniques disclosed herein provide a method and fabrication structure for pitch reduction (increasing pitch/feature density) for creating high-resolution features and also for cutting on pitch of sub-resolution features. Techniques include using multiple materials having different etch characteristics to selectively etch features and create cuts or blocks where specified. A pattern of alternating materials is formed on an underlying layer. An etch mask is positioned on the pattern of alternating materials. One or more of the alternating materials can be preferentially removed relative to other materials to uncover a portion of the underlying layer. The etch mask and the remaining lines of alternating material together form a combined etch mask defining sub-resolution features. Various patterns of materials can be formed on the underlying layer, and patterns can include two, three, four, five, or more different materials. Patterns can include having half pitches below 40 nanometers and even below 12 nanometers and smaller. Critical dimensions of materials can be controlled by type of deposition (such as with atomic layer deposition) instead of relying only on optical resolution of lithography systems.

[0026] One embodiment includes a method of patterning a substrate. Such a method is useful for microfabrication of semiconductor devices and integrated circuits. Referring now to FIGS. 1A, 1B, 1C, 1D, 2A, and 2B, a multi-line layer is formed above or on underlying layer 135. The multi-line layer can be formed directly on the underlying layer, or on any intervening layer or interfacial films or planarization layers, such as on an anti-reflective coating (ARC) layer. The multi-line layer includes a region having a pattern of alternating lines of two or more differing materials. In some embodiments the alternating lines can cover essentially an entire surface of a substrate, but in other alternative embodiments only particular regions have the pattern of alternating lines. Each line has a horizontal thickness, a vertical height, and extends across the underlying layer. Note that the alternating lines can include straight lines, curved lines, race track path, et cetera. Another example of alternating lines is a set of concentric circles with each ring being a curved line.

Each line of the pattern of alternating lines is uncovered on a top surface of the multi-line layer and vertically extends to a bottom surface of the multi-line layer. In other words, each line of a particular material can be anisotropically etched to a bottom surface of the multi-line layer thereby uncovering underlying layers because lines of material alternate horizontally across a substrate surface in contrast to a vertical stack of materials. At least two of the two or more differing materials differ chemically from each other by having different etch resistivities relative to each other.

[0027] Having different etch resistivities from each other, as used herein, means that there is at least one etchant (or etchant combination) that etches a given one material at a greater rate than the other material(s). Note that there can exist particular etchants that etch two or more given materials at a same rate, but there is at least one etchant that etches an included material faster relative to the other material(s). Etching one material relative to another can include etching one material without substantially etching the other, or etching one material at a substantially greater rate as compared to the other material such as having an etch rate ratio of 3:1, 4:1, 10:1, etc. For two materials to have different etch resistivities this typically means that two materials are chemically different from each other such as by particular atomic elements included or arrangement of atomic elements. Two materials that are largely the same, except that one of the two materials includes a dopant, can nevertheless have different etch resistivities. Moreover, materials having the same atomic elements but with differing molecular or crystal structures can provide an etch resistivity differential also.

[0028] FIGS. 1A, 1B, 1C, and 1D illustrate example results of forming a particular multi-line layer. For example, FIGS. 1A and 1B show a side cross-sectional substrate segment having three lines of material formed thereon. The different materials are labeled A, B, and C. Note that bracket 151 shows a particular pattern segment of alternating lines. This pattern follows a sequence of A-B-C-B, which is then repeated. Thus, this pattern can continue with the sequence of A-B-C-B-A-B-C-B-A-B-C-B-A and so on. Note that in this particular multi-line layer material A can be isolated from being in contact with material C by having lines of material B on both sides of material A. In other embodiments, the half pitch of a given material can be varied so that material C can be absent in some regions or larger in other regions. FIGS. 2A and 2B show a top view of this substrate segment. Note that

from a top view each different material from the multi-line layer 150 is uncovered or accessible.

[0029] FIG. 1C shows multi-line layer 150 having a different pattern of alternating lines in that there are just two materials (A and B) that alternate with each other as shown by bracket 152. FIG. 1D shows multi-line layer 150 having a different pattern of alternating lines with four materials. Bracket 153 marks a segment of this example pattern that can be repeated. For example, an alternating pattern of repeating lines can have sequence of A-B-C-D-C-B-A-B-C-D-C-B-A, which can continue as is or have some areas with pitch variation. With any of these multi-line layers of patterns of alternating lines of two or more lines of different materials, an etch mask can be used to isolate particular regions of the multi-line layer for selective removal of one or more of these materials to modify a combined etch mask (either adding to the etch mask or subtracting from the etch mask as an aggregate etch mask) as will be subsequently described.

[0030] Referring now to FIGS. 3A, 3B, 4A, and 4B, a patterned mask layer 140 is formed on a substrate 105. The patterned mask layer includes mask material 141 that masks a portion of the multi-line layer 150. Note that any conventional mask material and pattern forming process can be used to form patterned mask layer 140. For example, patterned mask layer 140 can be lithographically patterned, or can be the result of a given pitch multiplication process. The mask material 141 can include organic materials and photoresists, inorganic materials as well as metal-containing materials, organometallic, et cetera. In one example, a mask layer material is deposited on multi-line layer 150, such as by spin-on deposition, chemical vapor deposition, etc. The mask material is then patterned and etched to remove a portion of the mask material, thereby resulting in a relief pattern of mask material on substrate 105.

[0031] A given mask material can be deposited as a relatively thin layer or relatively thicker layer. FIG. 3A is a side cross-sectional view through a center portion of FIG. 4A. Corresponding FIGS. 3B and 4B show a different pattern/configuration of patterned mask layer 140 in which mask material 141 is a mesa defining an opening, that is, the area surrounding the mesa is the opening. Both types of relief patterns are shown in the drawings to illustrate different resulting

patterns that come from different arrangement of materials, as will be apparent below.

[0032] In some embodiments, the patterned mask layer 140 can include multiple individual mask layers including separately patterned layers. For example, a few litho-etch operations can be executed during mask layer formation. Thus, the mask layer itself can include multiple mask patterns or films, or can be created as a result of decomposition pattern forming techniques.

[0033] After forming the patterned mask layer on the multi-line layer, at least one of the two or more differing materials can be selectively removed resulting in a portion of the underlying layer 135 being uncovered. Selective removal can be executed by any of various material removal processes such as wet etching, dry (plasma) etching, ashing, etc. FIG. 5A shows an example of such selective removal. In FIG. 5A, material B has been removed through openings in the patterned mask layer 140, such as by directional etching. Materials C and A remain as part of the patterned mask layer 140. In FIG. 6A, from a top view, portions of the underlying layer 135 are uncovered. Note that in FIG. 6A, the opening in patterned mask layer 140 is narrowed or restricted by lines of materials A and C, leaving two relatively smaller openings when the multi-line layer 150 is combined with the patterned mask layer 140 to form a combined etch mask.

[0034] FIGS. 5B and 6B show a different example. In FIG. 5B, materials A and B have been removed, such as by directional etching. In some embodiments, materials A and B can be removed one after the other, such as by using different etch chemistries. In other embodiments, materials A and B can be removed simultaneously while leaving lines of material A (that are not blocked by the patterned etch mask). Lines of materials herein can be removed simultaneously with etchants (process gas mixture) configured to etch two materials at a same rate. In some embodiments, the two materials being removed can have a same chemical composition to facilitate simultaneous removal. Note that in FIGS. 5B and 6B, with only material C remaining, material C adds to the mask material 141 and this combined pattern essentially forms an "H" shape in this particular substrate segment example. As can be appreciated, there are multiple different options for adding or removing materials to form combined etch masks with a given patterned mask layer positioned on lines of material.

[0035] For example, in other embodiments, the two or more differing materials include three or more differing materials. Selectively removing at least one of the two or more differing materials can then include selectively removing two of the three or more differing materials resulting in corresponding portions of the patterned mask layer being uncovered. In another embodiment, the two or more differing materials include four or more differing materials. Selectively removing at least one of the two or more differing materials then includes selectively removing two of the four or more differing materials resulting in corresponding portions of the patterned mask layer being uncovered.

[0036] In some embodiments, the pattern of alternating lines of two or more differing materials includes a repeating sequence of A-B-A-B in which material A and material B have different etch resistivities relative to each other. In other embodiments, the pattern of alternating lines of two or more differing materials includes a repeating sequence of A-B-C-B-A-B-C-B in which material A and material B have different etch resistivities relative to each other. For example, material C can have a different etch resistivity relative to material A and material B. In another embodiment, the pattern of alternating lines of two or more differing materials includes a repeating sequence of A-B-C-D-C-B-A-B-C-D-C-B, wherein at least two of materials A, B, C and D have different etch resistivities relative to each other.

[0037] Various different fabrication techniques can be used to form the multi-line layer. For example, a substrate can be provided having mandrels positioned on the underlying layer with the mandrels comprised of a first material. First sidewall spacers are then formed on exposed sidewalls of mandrels. The first sidewall spacers are comprised of a second material. Second sidewall spacers are formed on exposed sidewalls of the first sidewall spacers. The second sidewall spacers are comprised of a third material. Fill structures are then formed that fill open spaces defined between exposed sidewalls of second sidewall spacers that face each other. The fill structures are comprised of a fourth material. Top surfaces of the mandrels, the first sidewall spacers, the second sidewall spacers, and the fill structures are all uncovered (exposed). At least two materials of the first material, the second material, the third material and the fourth material are chemically different from each other.

[0038] In another embodiment, a substrate can be provided having mandrels positioned on the underlying layer with the mandrels comprised of a first material.

First sidewall spacers are then formed on exposed sidewalls of mandrels. The first sidewall spacers are comprised of a second material. Fill structures are then formed that fill open spaces defined between exposed sidewalls of first sidewall spacers that face each other. The fill structures are comprised of a fourth material. Top surfaces of the mandrels, the first sidewall spacers, and the fill structures are all uncovered (exposed). At least two materials of the first material, the second material, and the fourth material are chemically different from each other.

[0039] In another embodiment, a substrate can be provided having mandrels positioned on the underlying layer with the mandrels comprised of a first material. Fill structures are then formed that fill open spaces defined between exposed sidewalls of the mandrels. The fill structures are comprised of a fourth material. Top surfaces of the mandrels and the fill structures are all uncovered (exposed). At least two materials of the first material and the fourth material are chemically different from each other. An alternative method for forming two or three lines of alternating material is to execute a directed self-assembly operation of block copolymers to form alternating lines of differing material. Many other techniques can be used for forming all or a portion of the multi-line layer including extreme ultraviolet lithography, direct write print pattern, self-aligned quad patterning, self-aligned double patterning, and so forth.

[0040] Referring now to FIGS. 7A, 8A, 7B, and 8B, techniques herein can include transferring a combined pattern 160 into the underlying layer 135. The combined pattern 160 is defined by mask material and remaining materials of the multi-line layer. In some embodiments, an additional layer in between the multi-line layer and the underlying layer can have a different etch resistivity as compared to other materials in the multi-line layer and can thus provide another etch selectivity option for controlling pattern transfer. Alternatively, the underlying layer can provide this additional etch selectively material and then a target layer below the underlying layer can be a target for pattern transfer. In some embodiments, the underlying layer can be a memorization layer that receives the combined pattern during transfer of the combined pattern. This memorization layer can be comprised of material having a different etch resistivity relative to materials in the multi-line layer. With such a configuration, a given combined pattern can be transferred into the memorization

layer, then a currently existing line in the multi-line layer can be removed, and a combined pattern transfer can be executed again.

[0041] FIGS. 9A, 10A, 9B, and 10B show example patterned substrate segments after combined pattern transfer into underlying layer 135 and after having removed the patterned mask layer and multi-line layer. As with previous drawings, FIG. 9A is a cross-sectional side view that corresponds to top view 10A, while FIG. 9B is a cross-sectional side view that corresponds to top view 10B.

[0042] In other embodiments, transferring the combined pattern into the underlying layer can include cutting one or more buried structures within the underlying layer. The underlying layer can have buried fin structures or other features that can be cut or added to with the combined pattern transfer. As can be appreciated, a pitch of a given line of material in the two or more lines can be less than an optical resolution of a given photolithography system. This can be realized because mandrels can be formed by double or multiple patterning reduction techniques, and then additional materials can be deposited by atomic layer deposition or other highly controllable deposition techniques. Lines of material formed this way can have a half-pitch spacing that is less than 16 nanometers.

[0043] FIGS. 11-18 provide more detail on one example technique for forming a particular multi-line layer. In these figures, a multi-line layer of four different materials is being formed. In FIG. 11, a substrate is provided having mandrels 111 positioned on underlying layer 135. The mandrels 111 are comprised of a first material. The substrate can include a silicon wafer. One or more additional underlying layers and/or buried structures can be included depending on a fabrication step of a given substrate within a given fabrication flow. There are many different materials from which the mandrels can be constructed. Materials can include various nitrides, oxides, organics, metals, as well as other conventionally available materials. Mandrels 111 can be formed using conventional patterning techniques. For example, mandrels 111 can be a result of self-aligned double patterning or self-aligned quadruple patterning techniques and thus can have sub-resolution half pitches.

[0044] First sidewall spacers 112 are formed on exposed sidewalls of the mandrels 111 as shown in FIG. 13. First sidewall spacers 112 are comprised of a second material. Note that FIG. 13 shows spacers formed on vertical sidewalls of

the mandrels 111. Forming first sidewall spacers 112 can include conformally depositing the second material on the substrate. FIG. 12 shows a first conformal film 122 having been deposited on the substrate 105. Such spacer formation is conventionally known. For example, highly conformal deposition techniques, such as atomic layer deposition (ALD), can be selected for depositing spacer material, which approximately uniformly covers mandrels 111 and underlying layer 135. A spacer open etch can then be executed to complete formation of sidewall spacers. Such a spacer open etch is typically a directional etch that removes the second material from a top surface of the mandrels 111 and from the underlying layer 135 in between second material deposited on sidewalls of the mandrels 111 (except where material on sidewalls of mandrels covers the underlying layer 135).

[0045] Second sidewall spacers 113 are formed on exposed sidewalls of the first sidewall spacers 112 as shown in FIG. 15. The second sidewall spacers 113 are comprised of a third material. Note that FIG. 15 shows spacers formed on vertical sidewalls of the first sidewall spacers 112. Forming second sidewall spacers 113 can include conformally depositing the third material on the substrate. FIG. 14 shows a second conformal film 123 having been deposited on the substrate 105. Such spacer formation is conventionally known. For example, highly conformal deposition techniques, such as atomic layer deposition (ALD), can be selected for depositing spacer material, which approximately uniformly covers existing structures on the substrate, which can include mandrels 111, first sidewall spacers 112, and patterned mask layer 140. A spacer open etch can then be executed to complete formation of sidewall spacers. Such a spacer open etch is typically a directional etch that removes the third material from a top surface of the mandrels 111, the first sidewall spacers 112, and from patterned mask layer 140 in between third material deposited on sidewalls of the first sidewall spacers 112 (except where material on vertical sidewalls of structures covers the patterned mask layer 140). At least a portion of the first sidewall spacers 112 define open space between each other prior to forming second sidewall spacers. In some locations, mandrel half-pitch can be shortened such that forming first sidewall spacers completely fills space between selected mandrel pairs and thus prevents forming second sidewall spacers in such a location. In other words, varying pitch of the mandrels can cause some merged spacers, either from the first sidewall spacers or the second sidewall spacers. Such

a fabrication technique can be beneficial (for example) in forming power rails for integrated circuits.

[0046] Referring now to FIG. 17, fill structures 114 are then formed on the substrate 105 that fills open spaces defined between exposed sidewalls of second sidewall spacers 113 that face each other (prior to forming the fill structures 114). The fill structures 114 are comprised of a fourth material. Fill structures 114 are formed such that top surfaces of the mandrels 111, the first sidewall spacers 112, the second sidewall spacers 113, and the fill structures 114 are all uncovered. Material selection during formation is such that the first material, the second material, the third material and the fourth material are all chemically different from each other. Forming fill structures 114 can include depositing an overburden material 124 of the fourth material on the substrate. FIG. 16 shows overburden material 124 deposited on substrate 105, which can entirely cover existing structures. Various deposition techniques for depositing the overburden material 124 can be used including spin-on deposition. After deposition, overburden material 124 can be etched back, or otherwise pulled down, until the fourth material is recessed below top surfaces of the second sidewall spacers 113. The fourth material can also be recessed below top surfaces of first sidewall spacers 112 and mandrels 111. After this multi-line layer is formed, the patterned mask layer 140 can be formed thereon, such as a relief pattern of photoresist or hardmask material.

[0047] Other embodiments can exclude the fill structures and instead have trenches (un-filled lines) function as one or more lines of the multi-line layer. For example, a multi-line layer is formed above the underlying layer. A patterned mask is formed on the multi-line layer. The multi-line layer includes a region having a pattern of alternating lines of two or more differing materials. In this region, each line has a horizontal thickness, a vertical height, and extends across the patterned mask layer, and each line of the pattern of alternating lines is uncovered on a top surface of the multi-line layer and vertically extends to a bottom surface of the multi-line layer. At least two of the two or more differing materials differ chemically from each other by having different etch resistivities relative to each other. The multi-line layer also defines trenches as part of the pattern of alternating lines of two or more differing materials. Thus, defined trenches extend parallel with lines of material and uncover a portion of the patterned mask layer.

[0048] One example of such a multi-line layer is shown in FIG. 15 as a multi-line layer ready for pattern transfer or after positioning an etch mask thereon. Thus, in this particular example embodiment, depositing overburden material and pulling down the overburden material is omitted. This can be beneficial in some applications because certain fill materials can be more difficult than others to fill trenches and pull-down. A given design can account for there being a pattern of trenches remaining in the multi-line layer and use these openings as the first pattern-transfer location. Accordingly, a combined pattern can be transferred into the underlying layer. The combined pattern is then defined by mask material and materials of the multi-line layer covering the patterned mask layer. Thus, with one line having no material, the initial pattern transfer can be executed without first having to selectively remove one of the lines. Another example can be seen in FIG. 1B. The pattern in FIG. 1B is A-B-C-B-A-B-C-B, which then repeats. In this multi-line layer, forming material C can be omitted and thus where material C is indicated there would be a trench instead. This configuration can be created by forming material A as a mandrel, deposited material B conformally, and then executing a spacer open etch on material B to yield sidewall spacers on material A and with material B removed from the underlying layer.

[0049] Accordingly, a multi-line layer 150 is formed on the underlying layer 135. Any line or combination of lines from multi-line layer 150 can be selectively removed, and then a combined pattern of remaining lines and patterned mask layer 140 can be transferred into underlying layer 135.

[0050] As can be appreciated, a matrix of selectable materials and material combinations can be created to create features at desired locations and lengths that are below resolution capabilities of conventional photolithography systems. Note that etched features themselves can be transferred into memorization layers and/or target layers, and can also be used to reverse patterns. Accordingly, two, three, four, five or more different materials can be accessed for selective etching. Self-alignment can be selected at various places on a substrate using a patterned mask and the differential etch selectivities of the different materials. In other words, with different materials of known dimensions, a designer can select where to execute an etch and have that etch be self-aligned at sub-resolution dimensions. For example, if a given contact pattern from a photoresist material is relatively large and spans

multiple materials, a contact will only be etched at one of the materials within that particular contact pattern opening.

[0051] Thus, techniques herein can be used to provide a pedestalized color scheme, that is, materials with differential etch selectivities. Moreover, the pattern of alternating lines of material can be fabricated to have different pitches depending on design interests. Conventionally, it is very difficult to cut on pitch. Conventional photolithography systems can make cuts of about 42 nanometers. With techniques herein, however, a contact can be placed at will anywhere on a given substrate. This patterning technique also enables pitch splitting across colors. In some regions there can be a full half pitch between materials, while in other regions there are relatively large amounts of self-alignment, such as between mandrels. Moreover, by selecting two or more of available materials in which two of the materials are adjacent to each other, off-pitch or mixed-size etches can be executed. Thus, various pitch multiples can be made as a cut or a block with techniques herein.

[0052] With such a structure or pattern formed, many options are available for pattern transfer. For example, one option is to add or subtract a given mask pattern from a transfer. By way of a non-limiting example, adding a block can result in an "H" shape because a block was added to two lines. Subtracting a block can provide a self-aligned cut of a given block mask. Any clear field or dark field masks can be added to, or subtracted from, any of multiple overlying materials (after selective etching) to form a combined etch mask. In other words, any 2-dimensional multi-material layer can be formed on a substrate, and then any 2-dimensional mask pattern can be formed on the multi-material layer. Even though the mask layer might be initially formed (registered) by lithographic patterning, and even though the multi-material layer might also be initially registered lithographically, a cross point of the two layers provides sub-resolution patterning because the combination of the two layers and ability to selectively etch one or more of multiple uncovered materials, augments lithographic registration to provide many precise etch transfer operations and options including self-aligned gate and self-aligned block etches.

[0053] In the preceding description, specific details have been set forth, such as a particular geometry of a processing system and descriptions of various components and processes used therein. It should be understood, however, that techniques herein may be practiced in other embodiments that depart from these

specific details, and that such details are for purposes of explanation and not limitation. Embodiments disclosed herein have been described with reference to the accompanying drawings. Similarly, for purposes of explanation, specific numbers, materials, and configurations have been set forth in order to provide a thorough understanding. Nevertheless, embodiments may be practiced without such specific details. Components having substantially the same functional constructions are denoted by like reference characters, and thus any redundant descriptions may be omitted.

[0054] Various techniques have been described as multiple discrete operations to assist in understanding the various embodiments. The order of description should not be construed as to imply that these operations are necessarily order dependent. Indeed, these operations need not be performed in the order of presentation. Operations described may be performed in a different order than the described embodiment. Various additional operations may be performed and/or described operations may be omitted in additional embodiments.

[0055] "Substrate" or "target substrate" as used herein generically refers to an object being processed in accordance with the invention. The substrate may include any material portion or structure of a device, particularly a semiconductor or other electronics device, and may, for example, be a base substrate structure, such as a semiconductor wafer, reticle, or a layer on or overlying a base substrate structure such as a thin film. Thus, substrate is not limited to any particular base structure, underlying layer or overlying layer, patterned or un-patterned, but rather, is contemplated to include any such layer or base structure, and any combination of layers and/or base structures. The description may reference particular types of substrates, but this is for illustrative purposes only.

[0056] Those skilled in the art will also understand that there can be many variations made to the operations of the techniques explained above while still achieving the same objectives of the invention. Such variations are intended to be covered by the scope of this disclosure. As such, the foregoing descriptions of embodiments of the invention are not intended to be limiting. Rather, any limitations to embodiments of the invention are presented in the following claims.

CLAIMS

1. A method of patterning a substrate, the method comprising:

forming a multi-line layer on an underlying layer of a substrate, the multi-line layer including a region having a pattern of alternating lines of two or more differing materials, wherein each line has a horizontal thickness, a vertical height, and extends across the underlying layer, wherein each line of the pattern of alternating lines is uncovered on a top surface of the multi-line layer and vertically extends to a bottom surface of the multi-line layer, wherein at least two of the two or more differing materials differ chemically from each other by having different etch resistivities relative to each other;

forming a patterned mask layer on the multi-line layer, the patterned mask layer including mask material that masks a portion of the multi-line layer; and

selectively removing at least one of the two or more differing materials resulting in a portion of the underlying layer being uncovered.

2. The method of claim 1, wherein the two or more differing materials include three or more differing materials, wherein selectively removing at least one of the two or more differing materials includes selectively removing two of the three or more differing materials resulting in corresponding portions of the underlying layer being uncovered.

3. The method of claim 1, wherein the two or more differing materials include four or more differing materials, wherein selectively removing at least one of the two or more differing materials includes selectively removing two of the four or more differing materials resulting in corresponding portions of the underlying layer being uncovered.

4. The method of claim 1, wherein the pattern of alternating lines of two or more differing materials includes a repeating sequence of A-B-A-B in which material A and material B have different etch resistivities relative to each other.

5. The method of claim 1, wherein the pattern of alternating lines of two or more differing materials includes a repeating sequence of A-B-C-B-A-B-C-B in which material A and material B have different etch resistivities relative to each other.

6. The method of claim 5, wherein material C has a different etch resistivity relative to material A and material B.

7. The method of claim 1, wherein the pattern of alternating lines of two or more differing materials includes a repeating sequence of A-B-C-D-C-B-A-B-C-D-C-B, wherein at least two of materials A, B, C and D have different etch resistivities relative to each other.
8. The method of claim 1, wherein forming the patterned mask includes forming a relief pattern of mask material.
9. The method of claim 1, wherein the mask material includes a metal.
10. The method of claim 1, wherein forming the multi-line layer includes:
 - providing the substrate having mandrels positioned on the underlying layer, the mandrels comprised of a first material;
 - forming first sidewall spacers on exposed sidewalls of the mandrels, the first sidewall spacers comprised of a second material;
 - forming second sidewall spacers on exposed sidewalls of the first sidewall spacers, the second sidewall spacers comprised of a third material; and
 - forming fill structures that fill open spaces defined between exposed sidewalls of the second sidewall spacers that face each other, the fill structures comprised of a fourth material, wherein top surfaces of the mandrels, the first sidewall spacers, the second sidewall spacers, and the fill structures are all uncovered, and wherein at least two materials of the first material, the second material, the third material and the fourth material are chemically different from each other.
11. The method of claim 1, wherein forming the multi-line layer includes:
 - providing the substrate having mandrels positioned on the underlying layer, the mandrels comprised of a first material;
 - forming first sidewall spacers on exposed sidewalls of the mandrels, the first sidewall spacers comprised of a second material; and
 - forming fill structures that fill open spaces defined between exposed sidewalls of the first sidewall spacers that face each other, the fill structures comprised of a fourth material, wherein top surfaces of the mandrels, the first sidewall spacers, and the fill structures are all uncovered, and wherein at least two materials of the first material, the second material, and the fourth material are chemically different from each other.
12. The method of claim 1, wherein forming the multi-line layer includes:

providing the substrate having mandrels positioned on the underlying layer, the mandrels comprised of a first material; and

forming fill structures that fill open spaces defined between exposed sidewalls of the mandrels, the fill structures comprised of a fourth material, wherein top surfaces of the mandrels and the fill structures are all uncovered, and wherein the first material and the fourth material are chemically different from each other.

13. The method of claim 1, further comprising transferring a combined pattern into the underlying layer, the combined pattern being defined by the mask material and remaining materials of the multi-line layer.

14. The method of claim 13, wherein the underlying layer is a memorization layer that receives the combined pattern during transfer of the combined pattern.

15. The method of claim 14, wherein the memorization layer is comprised of material having a different etch resistivity relative to materials in the multi-line layer.

16. The method of claim 13, wherein transferring the combined pattern into the underlying layer includes cutting one or more buried structures within the underlying layer.

17. The method of claim 1, wherein a pitch of a given line of material in the two or more lines is less than an optical resolution of a given photolithography system.

18. The method of claim 1, wherein forming the multi-line layer includes executing a directed self-assembly operation of block copolymers to form alternating lines of differing material.

19. The method of claim 1, wherein lines of respective material have a half-pitch spacing of less than 16 nanometers.

20. A method of patterning a substrate, the method comprising:

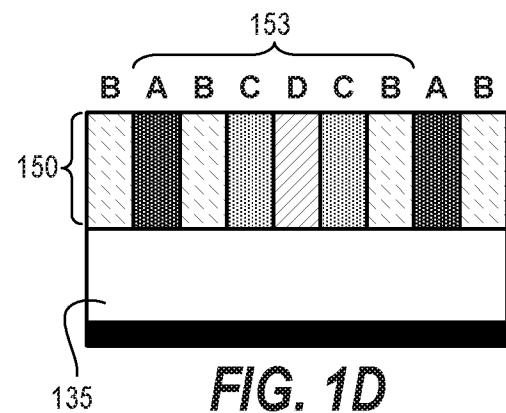
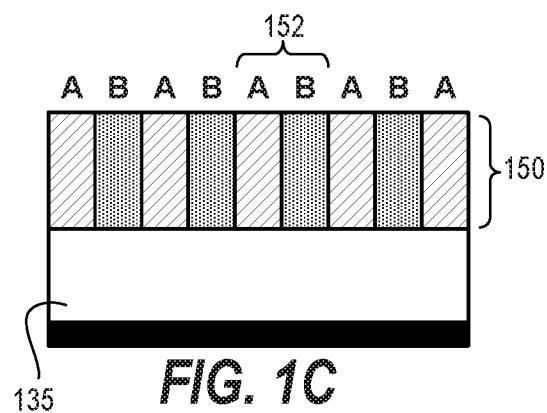
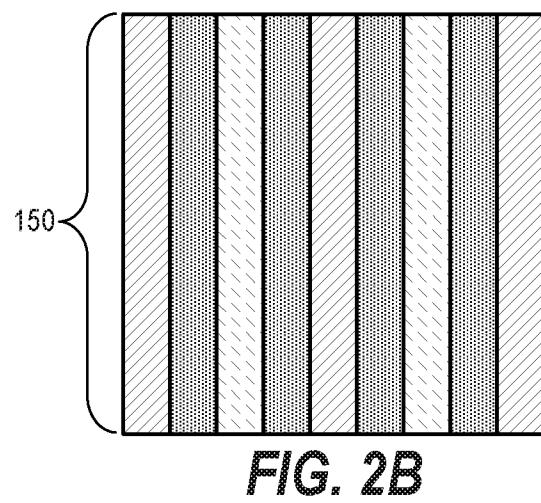
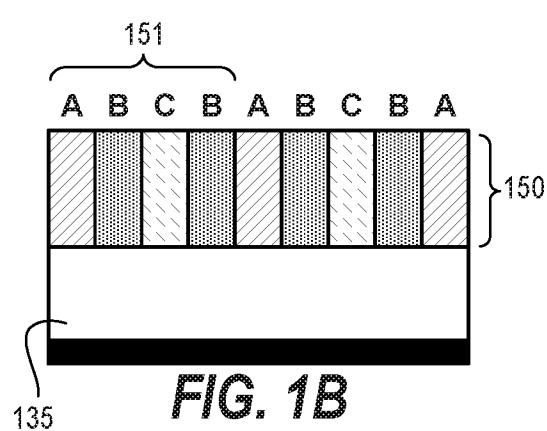
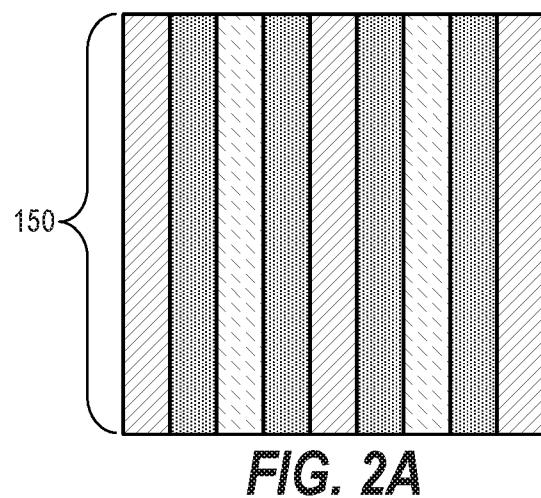
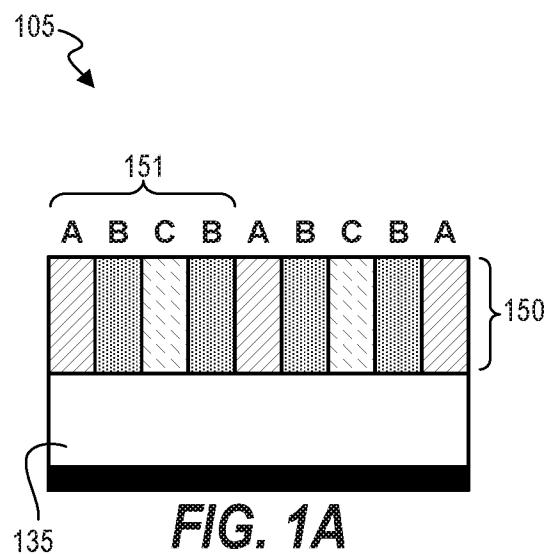
forming a multi-line layer on an underlying layer of a substrate, the multi-line layer including a region having a pattern of alternating lines of two or more differing materials, wherein each line has a horizontal thickness, a vertical height, and extends across the underlying layer, wherein each line of the pattern of alternating lines is uncovered on a top surface of the multi-line layer and vertically extends to a bottom surface of the multi-line layer, wherein at least two of the two or more differing materials differ chemically from each other by having different etch resistivities relative to each other, the multi-line layer also defines trenches as part of the pattern of alternating lines of two or more differing materials, wherein defined

trenches extend parallel with lines of material and uncover a portion of the patterned mask layer;

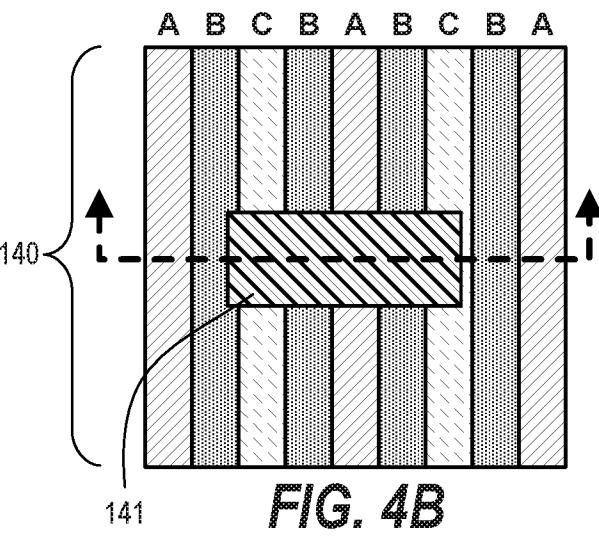
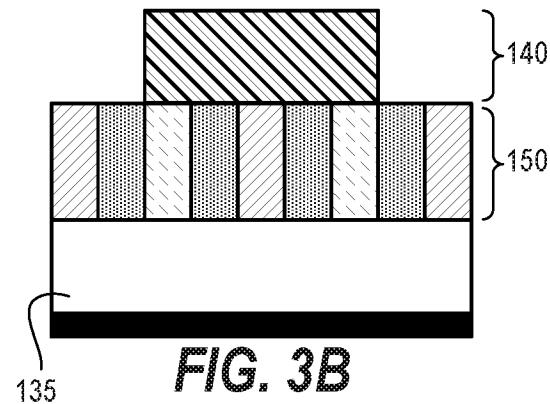
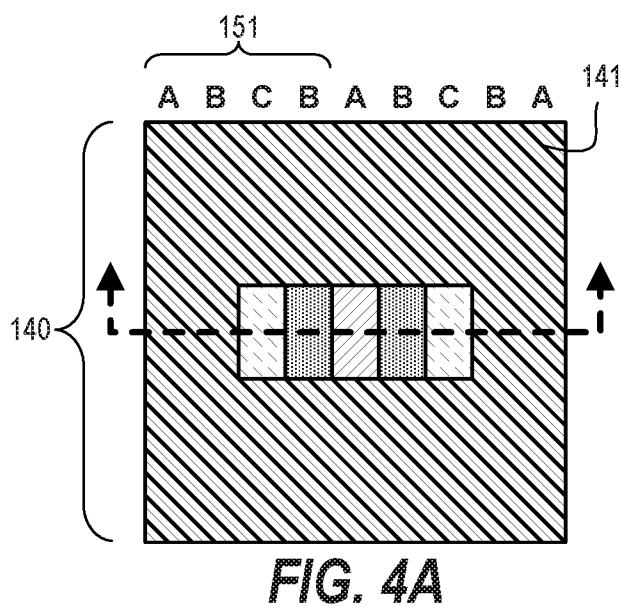
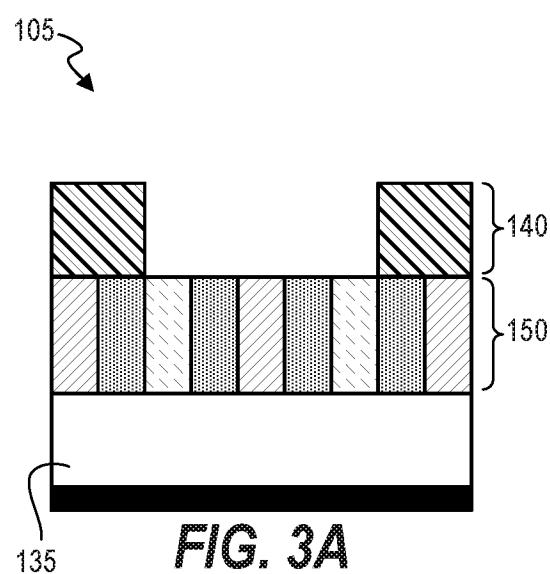
forming the patterned mask layer on the multi-line layer, the patterned mask layer including mask material that masks a portion of the multi-line layer; and

transferring a combined pattern into the underlying layer, the combined pattern being defined by mask material and materials of the multi-line layer covering the underlying layer.

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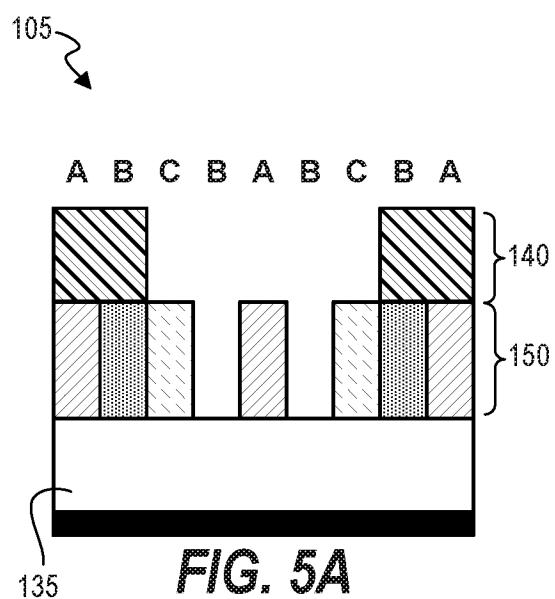


FIG. 5A

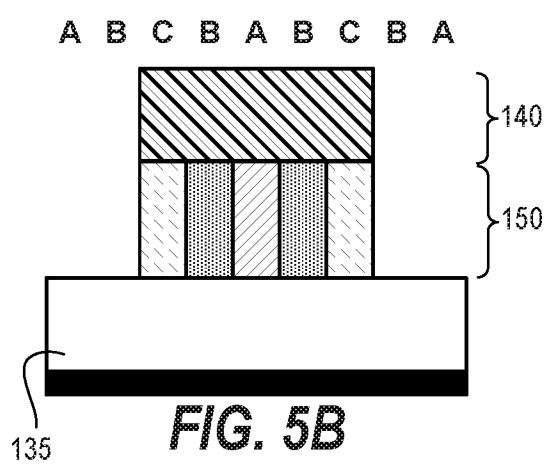


FIG. 5B

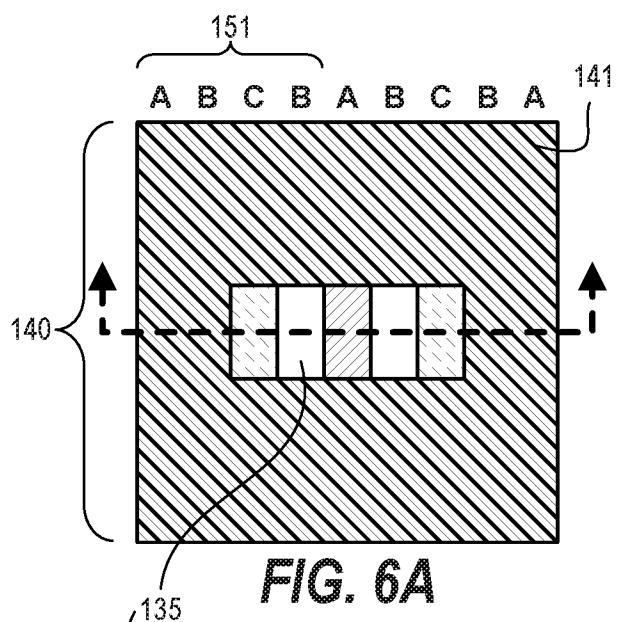


FIG. 6A

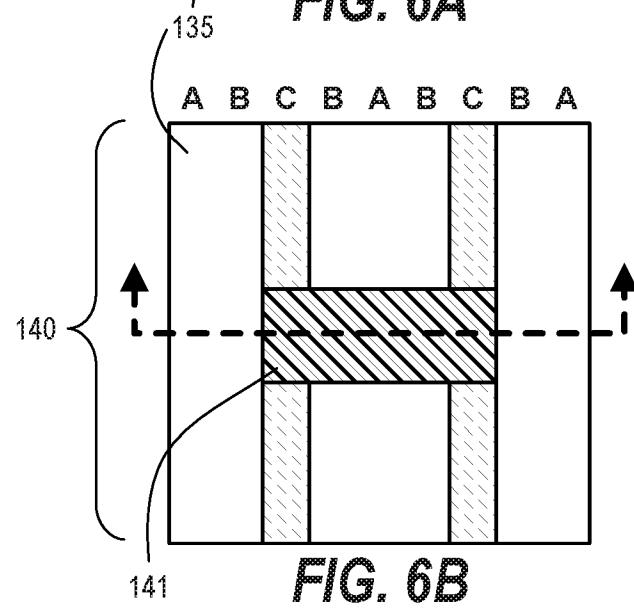


FIG. 6B

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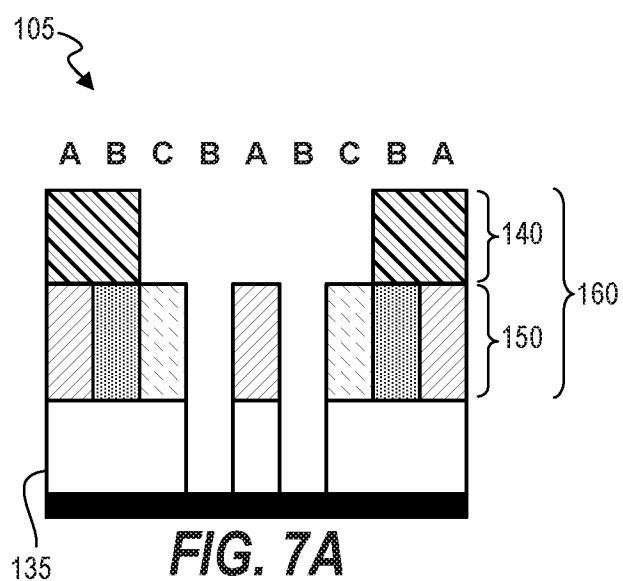


FIG. 7A

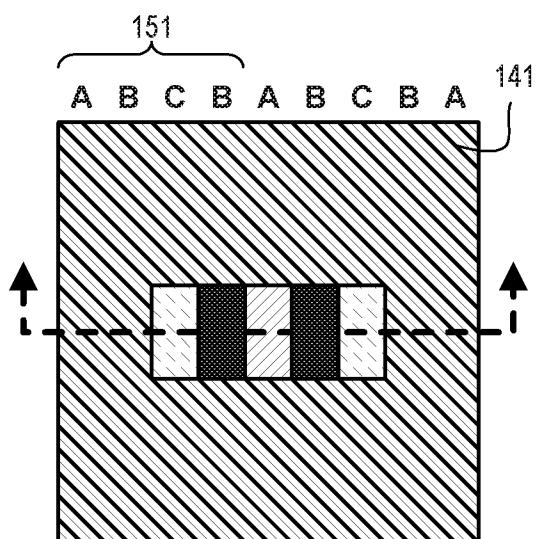


FIG. 8A

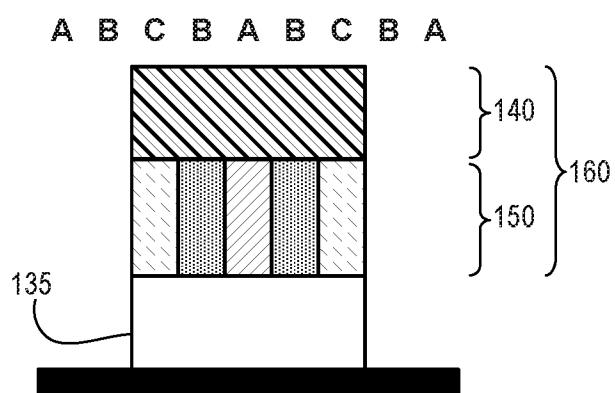


FIG. 7B

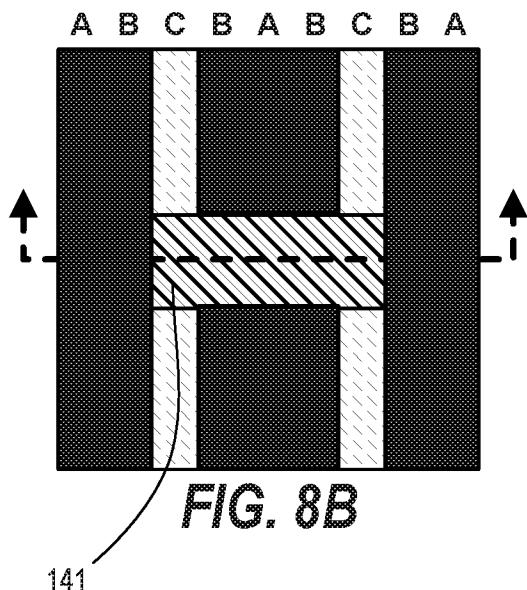


FIG. 8B

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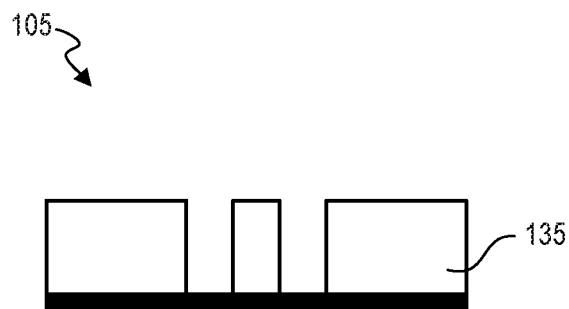


FIG. 9A

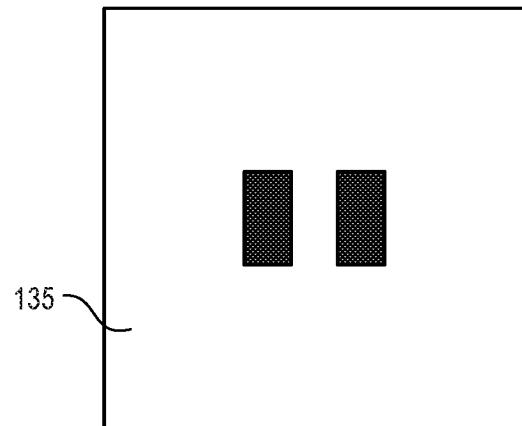


FIG. 10A

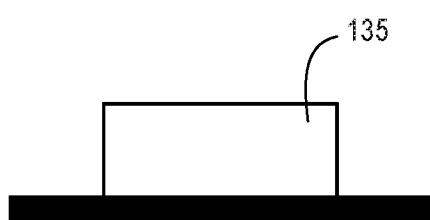


FIG. 9B

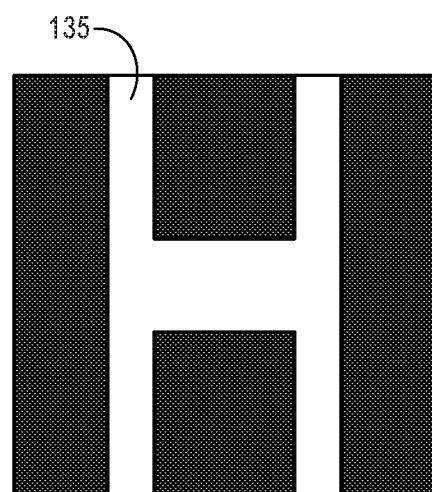


FIG. 10B

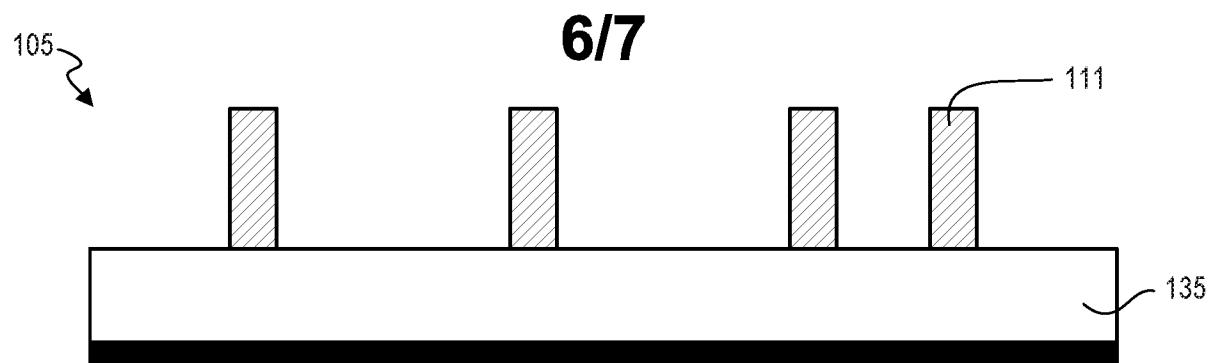


FIG. 11

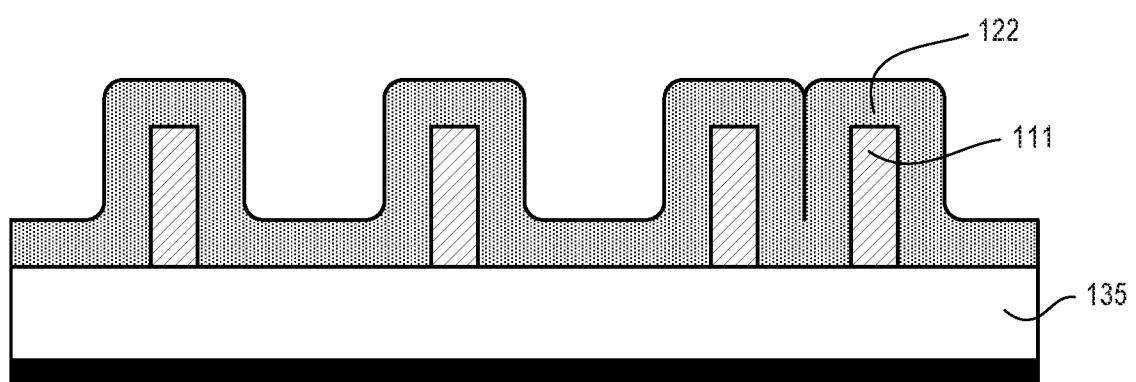


FIG. 12

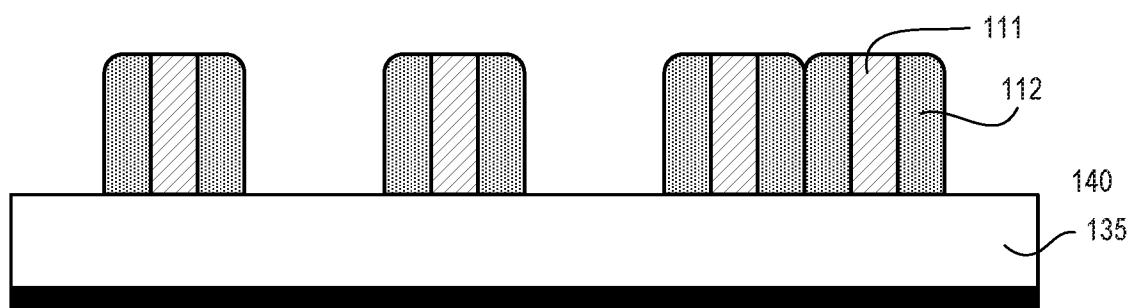


FIG. 13

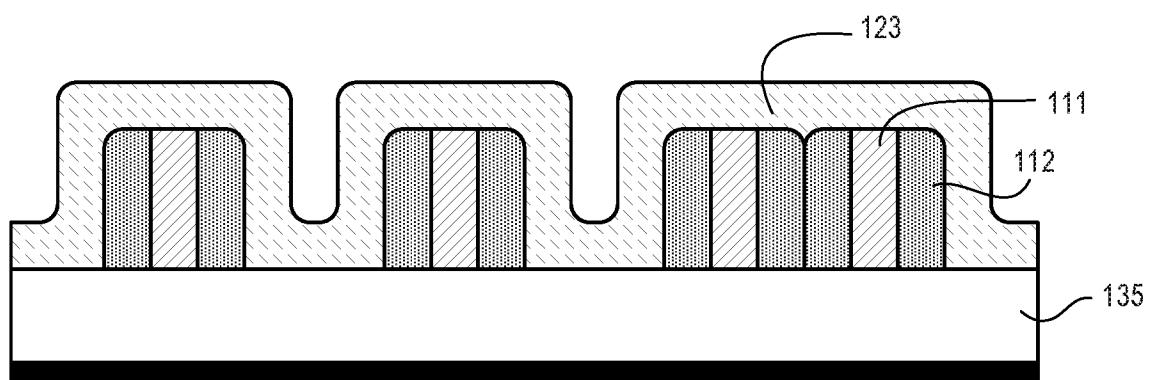
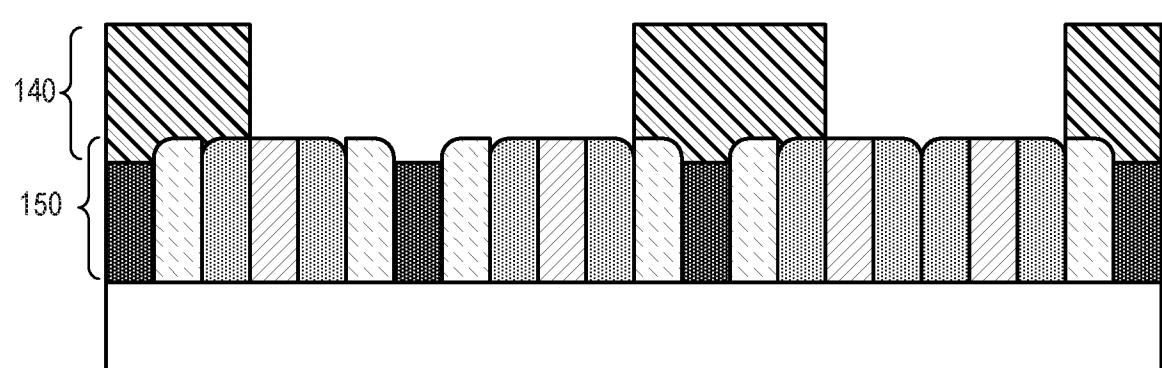
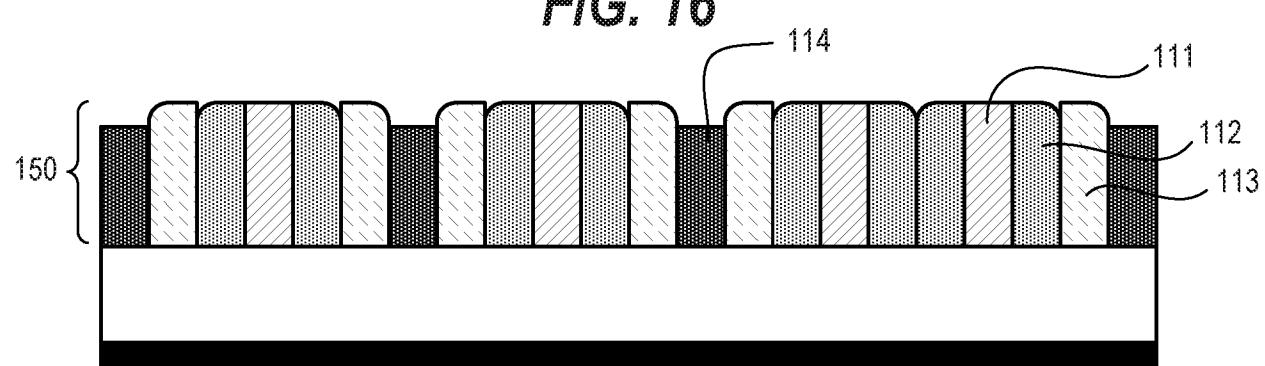
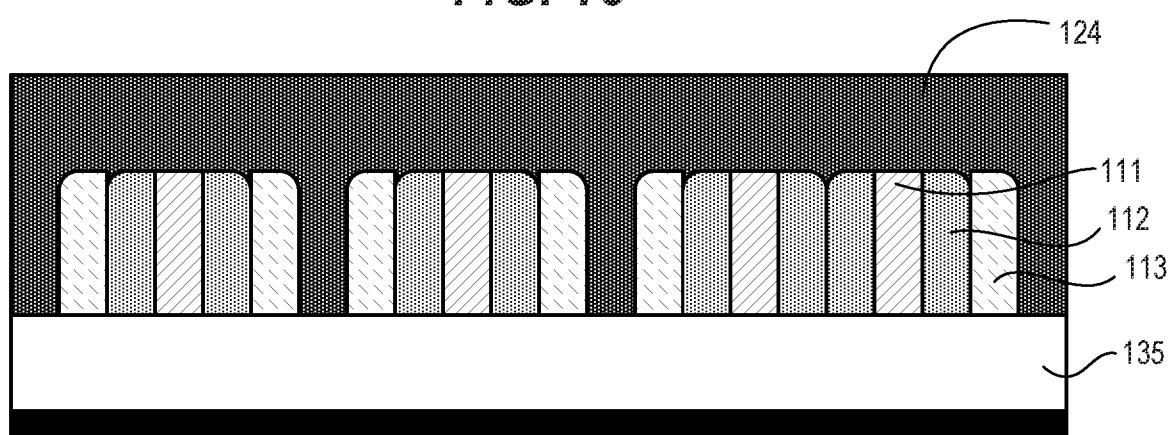
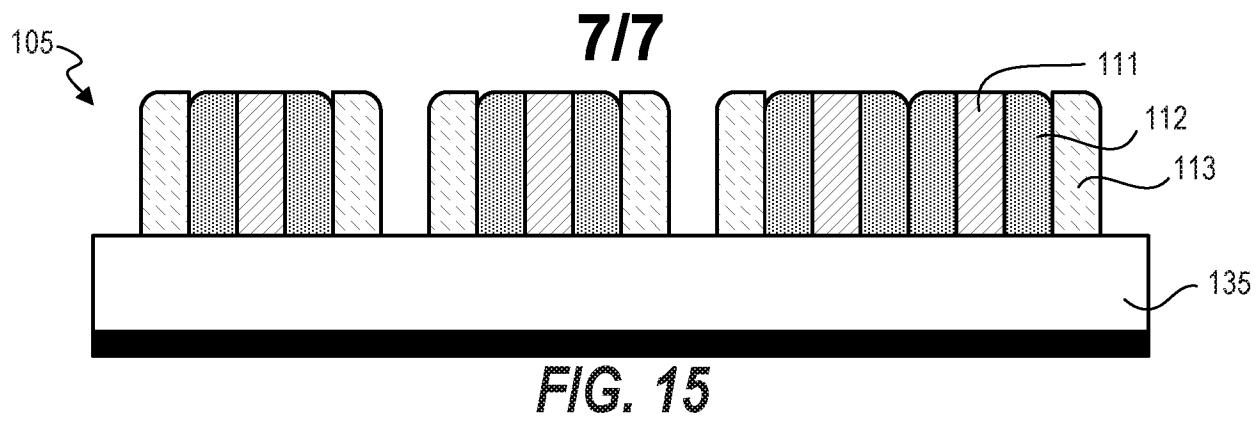


FIG. 14



INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2016/052694

A. CLASSIFICATION OF SUBJECT MATTER

H01L 21/027(2006.01)i, G03F 7/20(2006.01)i, G03F 1/70(2012.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/027; H01L 21/311; H01L 21/302; H01L 21/306; H01L 29/06; H01L 21/3205; B44C 1/22; H01L 21/76; G03F 7/20; G03F 1/70

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Korean utility models and applications for utility models
Japanese utility models and applications for utility modelsElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)
eKOMPASS(KIPO internal) & Keywords: patterning, mask, sidewall spacer, fill structure, etching chemistry

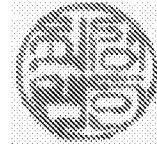
C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2015-0243518 A1 (TOKYO ELECTRON LIMITED) 27 August 2015 See abstract, paragraphs [0028]-[0039], claims 1-8 and figures 1-8.	1-20
A	US 2006-0027889 A1 (MATTHEW J. BREITWISCH et al.) 09 February 2006 See abstract, paragraphs [0043], [0044], claims 1, 3, 10 and figure 4.	1-20
A	US 6632741 B1 (LAWRENCE A. CLEVENGER et al.) 14 October 2003 See abstract, column 4, line 35 - column 5, line 21, claim 1 and figures 4-7.	1-20
A	US 5223083 A (DAVID A. CATHEY et al.) 29 June 1993 See abstract, column 5, line 29 - column 6, line 28, claim 1 and figures 3, 4.	1-20
A	KR 10-2009-0004172 A (SAMSUNG ELECTRONICS CO., LTD.) 12 January 2009 See abstract, paragraphs [0014]-[0038], claims 1-16 and figures 1a-1f.	1-20

 Further documents are listed in the continuation of Box C. See patent family annex.

* Special categories of cited documents:
 "A" document defining the general state of the art which is not considered to be of particular relevance
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 "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
 "O" document referring to an oral disclosure, use, exhibition or other means
 "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
 "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
 "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
 "&" document member of the same patent family

Date of the actual completion of the international search
04 January 2017 (04.01.2017)Date of mailing of the international search report
05 January 2017 (05.01.2017)Name and mailing address of the ISA/KR
International Application Division
Korean Intellectual Property Office
189 Cheongsa-ro, Seo-gu, Daejeon, 35208, Republic of Korea
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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/US2016/052694

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