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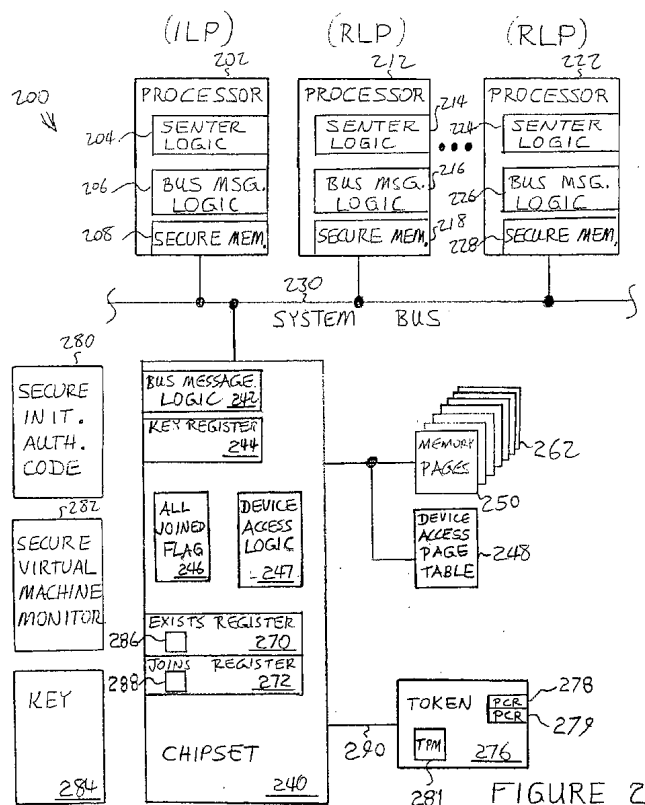
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[Continued on next page]

(54) Title: SYSTEM AND METHOD FOR EXECUTION OF A SECURED ENVIRONMENT INITIALIZATION INSTRUCTION



(57) Abstract: A method and apparatus for initiating secure operations in a microprocessor system is described. In one embodiment, one initiating logical processor initiates the process by halting the execution of the other logical processors, and then loading initialization and secure virtual machine monitor software into memory. The initiating processor then loads the initialization software into secure memory for authentication and execution. The initialization software then authenticates and registers the secure virtual machine monitor software prior to secure system operations.

FIGURE 2



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 G06F12/14 G06F9/455

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>WO 01 75565 A (THAKKAR SHREEKANT S ; LIN DERRICK C (US); RENERIS KEN (US); ELLISON) 11 October 2001 (2001-10-11)</p> <p>page 3 -page 11 figure 1C abstract</p> <p style="text-align: center;">--- -/--</p>	<p>1,3-5, 10, 14-16, 18,20,21</p>

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents :

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"Z" document member of the same patent family

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INTERNATIONAL SEARCH REPORT

International Application No

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>ROBIN J S ET AL: "Analysis of the Intel Pentium's ability to support a secure virtual machine monitor"</p> <p>PROCEEDINGS OF THE NINTH USENIX SECURITY SYMPOSIUM, PROCEEDINGS OF 9TH USENIX SECURITY SYMPOSIUM, DENVER, CO, USA, 14-17 AUG. 2000,</p> <p>pages 129-144, XP002248053</p> <p>2000, Berkeley, CA, USA, USENIX Assoc, USA</p> <p>ISBN: 1-880446-18-9</p> <p>the whole document</p> <p>-----</p>	1-49

INTERNATIONAL SEARCH REPORT

Information on patent family members

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