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(54) **Video data processing apparatus**

(57) When one video image (e.g., second video image) is blended or synthesized to video image (e.g., main video image), time base correction is made between both video data. A video data processing apparatus comprising a first video generator (27, 28, 29, 31) generating a first video data having an arbitrary frame rate, a second video generator (21, 22, 23, 24, 26) gen-

erating a second video data having an arbitrary frame rate different from the first video data, and a synchronization controller (24a) using video reproduction time managed by the first video generator as a reference time, and correcting video reproduction time of the second video generator shifted from the reference time by repeatedly using the second video data to obtain frame synchronization.

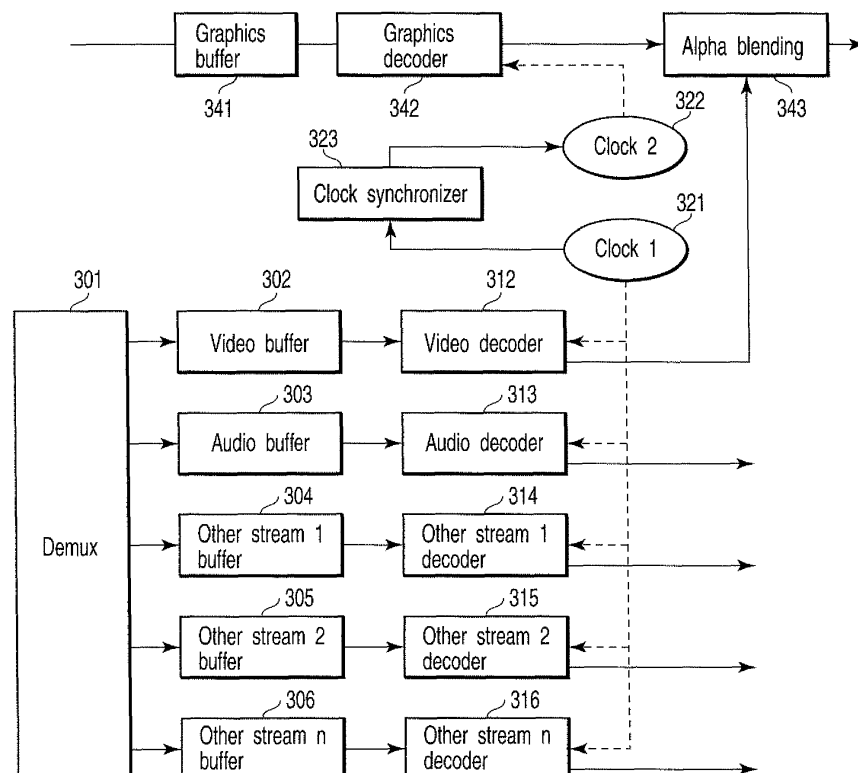


FIG. 10

Description

[0001] The present invention relates to a technique of synthesizing or blending several video data. In particular, the present invention relates to video data processing apparatus and method, which have improvement in time base correction between several video data.

[0002] There has been conventionally known a technique of synthesizing several video data. In many cases, first and second video data thus synchronized are a video data including a synchronizing signal having the same frequency inherently. Moreover, the following method is given as a technique of multiplexing a still image, that is, the second video data to the first video data as a moving image. According to the method, the second video data (e.g., OSD signal) is multiplexed after read from a memory (see JPN. PAT. APPLN. KOKAI Publication No. 2002-165018). In this case, the read timing of the second video data is preset to a predetermined timing of the first video data. It is presumed that the first and second video data are reproduced using a hardware circuit.

[0003] In the display area of a moving image, that is, main video image, the following display is desired in order to obtain various and plentiful expressions. More specifically, the main video image is displayed in a state that a second video image is matched thereto as a time zone event. For example, screen effect is desired such that the moving image, that is, main video image is matched to the second video image to change each transmittance of the main and second video images. Moreover, if the main video image is a motion picture (movie), it is desired to display translated texts in synchronous with the movie character's words.

[0004] In this case, the following matter should be considered. More specifically, the second video image has a video source reproduced using software, and not a video source processed using hardware like the conventional case. If the video source of the second video image is reproduced using the software, the reproduction time depends on a reference clock of a computer. Therefore, a frame rate of the second video image does not necessarily match with that of the main video image reproduced using another hardware. There is no problem so long as the second video image reproduced using the software is simply framed and displayed in a rectangular area of part of the moving image, that is, the main video image regardless of a change of the main video image.

[0005] However, there has not been disclosed a technique of closely relating display contents of main and second video images, and synchronizing the frame rates of the both.

[0006] An object of the embodiments is to provide video data processing apparatus and method, which can make time base correction of two video data when one video image (e.g., main video image) is blended or synthesized with the other video image (e.g., second video

image).

[0007] In order to achieve the foregoing object, according to one aspect of the present invention, there is provided a video data processing apparatus comprising: first video generator means for generating a first video data having an arbitrary frame rate; a second video generator means for generating a second video data having an arbitrary frame rate different from the first video data; and means for using video reproduction time managed by the first video generator as a reference time, and correcting video reproduction time of the second video generator shifted from the reference time by repeatedly using the second video data to obtain frame synchronization.

[0008] This summary of the invention does not necessarily describe all necessary features so that the invention may also be a sub-combination of these described features.

[0009] The invention can be more fully understood from the following detailed description when taken in conjunction with the accompanying drawings, in which:

FIG. 1 is a view to explain image plane blending handled in the present invention;

FIG. 2 is a block diagram showing the configuration of an apparatus (hardware) according to the present invention;

FIG. 3 is a view to explain the operation of the apparatus according to the present invention, that is, blending of a graphics frame with a video frame;

FIG. 4 is a view showing one example of a pixel data format;

FIG. 5 is a view to explain the principle of blending at a pixel rate;

FIG. 6 is a view to explain the operation of the apparatus according to the present invention, that is, blending of a graphics frame with a video frame, and to explain the operation when skip is given;

FIG. 7 is a block diagram showing the configuration of software for realizing the present invention;

FIG. 8 is a view showing one example of task priority in executable programs;

FIG. 9 is a flowchart to explain the flow of graphics reproduction procedures;

FIG. 10 is a view showing a time base correction system model according to the present invention; and

FIG. 11 is a view to explain an information recording/reproducing apparatus to which the present invention is applied.

[0010] Embodiments of the present invention will be described below with reference to the accompanying drawings.

[0011] FIG. 1 is a view to explain the basic principle of image plane blending handled in the present invention. A graphics plane 11 is an image computed and generated by a central processing unit (CPU). A video plane

12 is a sheet image of a moving image decoded by an MPEG2 decoder. The foregoing graphics and video planes 11 and 12 are generated separately from each other. For example, the graphics and video planes 11 and 12 are periodically blended in synchronous with an output frame rate of the MPEG2 decoder. A synchronizer 14 carries out the preceding blending; in other words, pixel level alpha blending is carried out.

[0012] FIG. 2 is a block diagram showing the configuration of hardware according to the present invention. A CPU 21 controls various devices connected to a system according to executable programs loaded onto a system memory 22, and generates a graphics output image.

[0013] A bus bridge 23 is connected with CPU 21, system memory 22, graphics controller 24 and PCI bus 25, and controls and relays a data exchange between various devices and buses.

[0014] The system memory 22 stores a copy of executable programs stored in a hard disk drive (HDD) 30 connected to an IDE controller 29. Moreover, the system memory 22 is used as a buffer area for temporarily storing variables required for computation and data transfer.

[0015] The graphics controller 24 controls a transfer of image computed by the CPU 21 from the system memory 2 to a graphics memory 26. Moreover, the graphics controller 24 executes complicated image processing computations according to the instructions from the CPU 21. In addition, the graphics controller 24 outputs image information to a video mixer 27 in synchronous with an output timing of an MPEG decoder 28. Thus, the graphics controller 24 includes a synchronization control section 24a. The graphics controller 24 further includes frame generation omitting section 24b, priority setting section 24c, frame rate control section 24d, which will be described later.

[0016] The video mixer 27 blends (mixes) NTSC signal information from the MPEG decoder 28 and image information from the graphics controller 24. In this case, the video mixer 27 carries out the foregoing mixing using a α (alpha) value included in the image information from the graphics controller 24. Then, the video mixer 27 reproduces an image signal synchronous with the NTSC signal, and thereafter, outputs it. The video mixer 27 includes an alpha blending section 27a. The video mixer 27 further includes a pixel processing section 27b for carrying out blending (synthesizing) at a pixel level described later.

[0017] The MPEG decoder 28 captures MPEG2 data extracted from data read from an optical drive 31 based on the control of the CPU 21, and converts it into image data. Thereafter, the MPEG decoder 28 outputs the converted image data to the video mixer 27.

[0018] A PCM decoder 32 periodically captures PCM data converted by the computation of the CPU 21 from audio data extracted from data read from the optical drive 31. Thereafter, the PCM decoder 32 converts the captured PCM data into an analog sound signal to out-

put the signal.

[0019] The IDE controller 29 is connected with the optical drive 31 and the HDD 30, and carries out control for reading data included in each drive to the memory based on the control of the CPU 21.

[0020] The optical drive 31 is a DVD drive, for example. The optical drive 31 reads data from a DVD disk stored with data for generating video and graphics planes 12 and 11.

[0021] The HDD 30 stores executable programs for operating this apparatus. An IR receiver 33 receives a signal from a remote controller for controlling this apparatus. A network interface controller (NIC) 34 controls communication data for making a connection with various networks.

[0022] FIG. 3 is a view to explain the operation of blending a graphics frame with a video frame. The present invention is adaptable even if a frame rate is different between video frames V1, V2, V3, ... V13 and graphics frames GA, GB, GC, ... GI. FIG. 3 shows the case where the video frame advances four frames while the graphics frame advances three frames. An output frame is synchronized with a frame rate of the video frame. If output timing shifts between the video frame and the graphics frame, a graphics frame generated before output frame is blended with the video frame. More specifically, from the relation between the video frame V1 and the graphics frame GA, an output frame (V1 + GA) blending the both frames is obtained. In the next output frame, an output frame (V2 + GA) blending the video frame V2 with the graphics frame GA is obtained. In the next output frame, an output frame (V2 + GB) blending the video frame V3 with the graphics frame GB is obtained. In the next output frame, the video frame V4 and the graphics frame GC are synchronized in the time base; therefore, an output frame (V4 + GC) blending the both frames is obtained. The frame blending (synthesis) described above is repeatedly made. In this case, the graphics controller 24 makes a comparison of the frame rate between video and graphics frames, and determines which of these frames advances. The number of times repeatedly using the same graphics frame may be changed in accordance with the frame rate value of the video and graphics frames.

[0023] As described above, the same graphics frame is blended (synthesized) to the continuous video frame of the first video data several times, thereby offsetting the difference between frame rates. Moreover, if the generation of the graphics frame is delayed, the number of times synthesizing the same graphics frame to the continuous video frame may be increased.

[0024] As described above, the apparatus of the present invention has a first video generator for generating first video data (video frame) having an arbitrary frame rate. The first video generator includes optical drive 31, IDE controller 29, MPEG decoder 28 and video mixer 27. Moreover, the apparatus of the present invention has a second video generator for generating second

video data (graphics frame) having an arbitrary frame rate different from the first video data. The second video generator includes CPU 21, system memory 22, bus bridge 23, graphics memory 26 and graphics controller 24. Video generation time managed by the first video generator is determined as reference time. If a shift from the reference time occurs in video generation time of the second video generator, the shift is corrected by repeatedly using the second video data, and thereby, frame synchronization is obtained. In this case, the foregoing "shift" is a frame unit. However, the "shift" includes the case where the second video data is not generated in time to obtain frame blending (synthesis) although described later.

[0025] FIG. 4 is a view showing one example of a pixel data format.

[0026] The pixel data format shows the structure of data for each pixel of the graphics frame. In FIG. 4, R, G, B and α denote red, green, blue and blending alpha value, respectively. These R, G, B and α each have one byte; therefore, the data comprises four bytes in total.

[0027] FIG. 5 is a view showing a pixel blending computation at a pixel level. In FIG. 5, G00,0 to G33,100 each express a pixel value of the graphics frame, and the numerical value before comma shows an RGB value at a pixel position while the numerical value after comma shows an α value. V00 to V33 each express an RGB value of each pixel of the video frame.

[0028] In this case, the pixel value for each output frame is obtained in a manner of computing and blending graphics and video pixel values using a graphics alpha ratio.

[0029] In blending the graphics pixel G00,0 shown at the upper left of FIG. 5 with the video pixel V00, a computation of $(G00 \times 0) + (V00 \times (1 - 0))$ is made. In this case, the graphics ratio is 0, and the video ratio is 1. Moreover, in blending the graphics pixel G10,33 with the video pixel V10, a computation of $(G10 \times 0.33) + (V10 \times (1 - 0.33))$ is made.

[0030] The output from the video frame has luminance Y, color difference Cb and Cr signals. Therefore, in FIG. 2, the video mixer 27 converts the pixel value outputted from the graphics frame into Y, Cb and Cr, and thereafter, blends them.

[0031] FIG. 6 is a view to explain frame blending in the case where graphics frame computation has not been made in time. FIG. 6 shows the case where the generation of the graphics frame GD is delayed. In other words, according to the present invention, the graphics frame is reproduced based on software. Of the reproduced frames, there exist a frame, which must be reproduced based complicated blending (synthesis) and operation result. Such a frame requires much time to fully generate it. In addition, the graphics frame is not generated in time resulting from the reasons described below. More specifically, the system performance including the CPU is not sufficient, or the computation of specified graphics frame is very complicated.

[0032] From the foregoing reasons, time becomes unmatching between the graphics frame GD and the video frame V6 inherently corresponding thereto.

[0033] In such a case, the video frame is preferentially generated according to the present invention. FIG. 6 shows the case where the computation of a frame "GD" of the graphics frame is not made in time. In this case, the video frame "V6" is inherently blended with the graphics frame "GD", thereby generating an output frame. However, the video frame "V6" is blended with the graphics frame "GC" already generated, thereby generating an output frame.

[0034] Therefore, the apparatus of the present invention has a mechanism such that any of the first or second video generators preferentially performs video frame generation. If video generation by another video generator is not made in reproduction time of video generation made preferentially, the apparatus has a function of omitting a generation of the video frame, which is not generated in time. By doing so, it is possible to prevent a delay of reproduction time of another video data according to the advance of the reproduction time of video generation made preferentially. The video generation made preferentially is performed using hardware. In this case, the hardware comprises a semiconductor chip for predetermined signal processing and electronic components, which are combined at a functional unit such as MPEG processing and PCM processing.

[0035] FIG. 7 is a functional block diagram showing the configuration of software for realizing the present invention. A video reproduction control section 111 is a module for controlling the operation of the software of the present invention. The control section 111 controls data read from a file system 112, user operation receipt from a remote control receiver 113 and the operations of DVD reproduction controller 114 and navigation controller 115. The DVD reproduction controller 114 controls video reproduction, and the navigation controller 115 controls graphics frame generation.

[0036] The navigation controller 115 generates a navigation image with respect to the graphics frame using navigation control data read from the file system 112. Moreover, the navigation controller 115 controls image output timing based on time information of the DVD reproduction controller 114 via a display driver 121. In addition, the DVD reproduction controller 114 controls an audio decoder 122 for outputting sound effect in accordance with user events.

[0037] The display driver 121 captures graphics frame data generated by the navigation controller 115, and controls a graphics controller 123 to output graphics frame data. Audio decoders 122 and 124 decode audio data supplied from the navigation controller 115 and the DVD reproduction controller 114. Then, these audio decoders 122 and 124 convert audio data into PCM data, and thereafter, output it to an audio mixer 125.

[0038] The audio mixer 125 blends (mixes) PCM data inputted from audio decoders so that the PCM data is

converted one PCM output data, and thereafter, output it to a PCM decoder driver 126. The PCM decoder driver 126 supplies PCM data inputted from the audio mixer 125 to a PCM decoder 127 to control sound reproduction.

[0039] The DVD reproduction controller 114 captures DVD data read via the file system 112 to separate the DVD data into video data, sound data and other data, and thereafter, controls their reproduction. The MPEG decoder driver 130 supplies MPEG data inputted from the DVD reproduction controller 114 to the MPEG decoder 131. The driver 130 carries out control for outputting the MPEG data at proper timing. The remote control receiver 113 interprets remote control switch events given by the IR driver 141. Then, the receiver 113 gives the event information to the video reproduction controller 111 as a message. The IR driver 141 controls interruption of the IR controller 142, and makes a detection which switch of the remote controller is depressed. Then, the IR receiver 141 gives the information to the remote control driver. The file system 112 controls a file system of the HDD 30 and the optical driver 31.

[0040] The file system 112 controls an ATAPI driver 151. The ATAPI driver 151 controls the IDE controller 142 to read data from the foregoing HDD 30 and optical driver 31.

[0041] FIG. 8 is a view to explain task priority of executable programs. According to the present invention, task priority is predetermined as shown in FIG. 8 in order to preferentially perform video reproduction on a multi-task OS. More specifically, according to Kernel service 211, basic processings of the OS are executed, and task switch and access to the file system is controlled. According to video processing 212, video data is analyzed and extracted, and video and audio controllers are controlled. According to audio processing 213, blending of audio data and the output are controlled. According to graphics processing 214, navigation image data is generated.

[0042] The task priority shown in FIG. 8 is predetermined, and then, task switch is set so that if processing having higher priority is left, the processing is always preferentially executed. In this case, the task switch preferentially allocates a task to the Kernel service and video processing until they become idle. Then, the task switch allocates the tasks to audio and graphics processing only when the Kernel service and video processing become a state of waiting any event.

[0043] FIG. 9 is a flowchart to explain the flow of graphics reproduction. Step S01 is a start point of the flowchart, and shows graphics reproduction start. In step S02, a variable "n" storing a frame number of reproduced graphics is initialized to "0". In step S03, the value of an auto-increment graphics reproduction timer counter is initialized/corrected to a current reproduction time of an overlay target video.

[0044] In step S04, time information "PTn" reproducing an n-th frame of the graphics is acquired. In step

S05, the current timer counter value "Ct" is acquired. In step S06, the following determination is made; namely, the reproduction time "PTn" of the n-th graphics frame is compared with the acquired timer counter value "Ct".

[0045] In step S07, if the reproduction time "PTn" of the n-th graphics frame is smaller than the acquired timer counter value "Ct"; in other words, if the video reproduction time already exceeds reproduction time, the procedure described below is taken. The n-th frame data is decoded and displayed in step S13, and thereafter, the value of reference frame number "n" is incremented by one.

[0046] In step S08, if the reproduction time "PTn" of the n-th graphics frame is larger than the acquired timer counter value "Ct"; in other words, if the current video reproduction time already exceeds the reproduction time of the n-th frame, the procedure described below is taken. Data required for generating the n-th graphics frame is acquired.

[0047] In step S09, the graphics frame data acquired in step S08 is decoded to image frame for screen display. In step S10, the current timer counter value "Ct" is acquired. In step S11, the following determination is made; namely, the reproduction time "PTn" of the n-th graphics frame is compared with the acquired timer counter value "Ct".

[0048] In step S12, if the timer counter value "Ct" is larger than the reproduction time "PTn" of the n-th graphics frame; in other words, if the current time does not reach the reproduction time of the n-th graphics frame, waiting time is given for slight period.

[0049] In step S13, if the reproduction time "PTn" of the n-th graphics frame is equal to or smaller than the timer counter value "Ct"; in other words, if the current time reaches or exceeds the reproduction time of the n-th graphics frame, the n-th frame is display on a screen. In step S14, it is determined whether or not all graphics frame processings are completed. Step S15, if all graphics frame processings are completed, the graphics reproduction procedure ends.

[0050] FIG. 10 is a block diagram showing a time correction system model of the present invention. A de-multiplexer (Demux) 301 extracts each data stream from one data stream multiplexing several data streams, and thereafter, supplies it to individual buffers.

[0051] A video buffer 302 temporarily stores main video data of separated data stream. An audio buffer 303 temporarily stores sound data reproduced synchronous with the main video of the separated data stream. Other stream 1 to n buffers 304, 305 and 306 temporarily stores other data streams. Another sound data and text data displayed synchronous with words of the main video are given as other data streams.

[0052] A video decoder 312 reads main video data stream from the video buffer 302, and decodes it to generate an image frame. When the time of a CLOCK1 321 reaches output time defined every image frame, decoded video data is outputted. An audio decoder 313 reads

sound data stream from the audio buffer 303, and decodes it to generate PCM data. The PCM data is outputted after being converted into an analog sound output when the time of the CLOCK1 321 reaches output time defined every sound frame.

[0053] Other stream decoders 314 to 316 read data stream from buffers 304 to 306 corresponding to them, and decode it according to an output format defined by a arbitrary method. When the time of the CLOCK1 321 reaches output format defined by the arbitrary method, these stream decoders output decoded data.

[0054] A graphics buffer 341 temporarily stores data streams such as second video image and computer graphics. A graphic decoder 342 outputs a video frame and rewrites it to a new screen when the time of a CLOCK2 322 reaches output timing storing data streams such as second video image and computer graphics in the data stream.

[0055] The CLOCK1 321 is a reference timer used for outputting sound and other data appended to the main video image. On the other hand, the CLOCK2 322 is a reference timer different from the CLOCK1 321, which is used for outputting data streams such as second video image and computer graphics.

[0056] A clock synchronizer 323 has a function of synchronizing the advance of time between the CLOCK1 321 and the CLOCK2 322. An alpha blending 343 blends the outputs of the video decoder 312 and the graphic decoder 342 based on an α value included in the graphic decoder output, and thereafter outputs it. Other streams include second video stream confirmable to DVD.

[0057] The second video data (graphic data) stored in the graphic buffer 341 of the present invention is mainly data reproduced using software by a computer.

[0058] FIG. 11 is a view to explain an information recording/reproducing apparatus to which the present invention is applied. An information recording/reproducing apparatus 400 shown in FIG. 11 records and reproduces information with respect to an optical disk 401. The apparatus 400 has a built-in hard disk drive; therefore, it records and reproduces information with respect to a hard disk 402.

[0059] Graphic information is captured via a receiver or network interface 500, and temporarily recorded to the optical disk 401 or hard disk 402. In this case, the graphic information is encoded or coded. Now, during information reproduction of the optical disk 401, a flag of reproducing graphic information and synthesizing it to main video image is detected. In this case, the flag is detected from management information (reproduction control information) of the optical disk 401. The graphic information is read from the optical disk 401 or hard disk 402, and then, the CPU is operated based on specified software to execute decoding (corresponding to 342 of FIG. 10). In this case, graphic data is reproduced using a system clock of the CPU; for this reason, it becomes an asynchronous state with the main video frame.

[0060] In such a case, the system of the present invention is operated to execute alpha blending. For example, according to the foregoing system (apparatus), it is possible to acquire Japanese subtitle second video with respect to a foreign sound disk (DVD) on the market. The Japanese subtitle second video information is temporarily recorded and stored in the hard disk. Then, a specified DVD is reproduced. If a command of instructing to reproduce the second video information recorded in the hard disk is described in the DVD reproduction control information, the second video information of Japanese subtitle is reproduced. Then, alpha blending is carried out with respect to the main video image.

[0061] Even if the frame rate is different between the first and second video data, blending target, that is, video data are variously broadened so that the foregoing means makes correction of matching the time base between the both data. Various second video images are displayable in a state of being blended with the main video image as a time zone event. Therefore, it is possible to obtain variety and plentiful expression in the display area of the main video image. The foregoing various second video images are previously downloaded, and in addition, video images captured from broadcasting data are usable.

[0062] The present invention is not limited to the foregoing embodiment. Components are modified and reduced to practice in a range without departing from the scope of the subject matter of the invention. Various inventions are provided by a proper combination of several components disclosed in the embodiments. For example, some may be deleted from all components disclosed in the embodiments. Components relevant to different embodiments may be properly combined.

Claims

1. A video data processing apparatus **characterized by** comprising:

a first video generator (27, 28, 29, 31) generating a first video data having an arbitrary frame rate;
a second video generator (21, 22, 23, 24, 26) generating a second video data having an arbitrary frame rate different from the first video data; and
a synchronization controller (24a) using video reproduction time managed by the first video generator as a reference time, and correcting video reproduction time of the second video generator shifted from the reference time by repeatedly using the second video data to obtain frame synchronization.

2. The apparatus according to claim 1, **characterized by** further comprising:

an alpha blending section (27a) using the second video data generated by the second video generator, which includes the first video data and an alpha value for alpha blending, and blending the first and second video data on the alpha value.

3. The apparatus according to claim 1, **characterized by** further comprising:

a pixel processing section (27b) blending the first and second video data at a pixel level.

4. The apparatus according to claim 1, **characterized in that** the first video generator outputs the first video data generated using hardware processing, and the second video generator outputs the second video data generated using software processing.

5. The apparatus according to claim 4, **characterized in that** the second video generator decodes coded file contents using the software processing to generate the second video data, and outputs it.

6. The apparatus according to claim 1, **characterized by** further comprising:

a priority setting section (24c) preferentially performing video generation by the first video generator; and
a frame generating omitting section (24b) omitting a generation of a video frame when video generation time of the second video generator is delayed from video generation time of the second video generator,
control is carried out so that video generation time of another video data is not delayed from video generation time of preferentially performed video generation according to the advance of generation time.

7. The apparatus according to claim 1, **characterized in that** the first video generator is an apparatus for processing video data from an optical disk.

8. The apparatus according to claim 1, **characterized in that** the first video generator is an apparatus for processing video data from a receiver.

9. A video data processing method of blending a first video data generated using hardware processing and having an arbitrary frame rate and a second video data generated using software processing and having an arbitrary frame rate different from the first video data, **characterized by** comprising:

comparing a frame rate of the first video data

with a frame rate of the second video data; and blending the same frame of the second video data to continuous each frame of the first video data several time, and thereby, offsetting a difference between the frame rates.

10. The method according to claim 9, **characterized in that** when frame generation of the second video data is delayed, the number of times of blending the same frame of the second video data to continuous each frame of the first video data is increased to offset a difference between the frame rates.

11. The method according to claim 9, **characterized in that** blending of the first and second video data is carried out at a pixel level.

12. A video data processing apparatus **characterized by** comprising:

first video generator means for generating a first video data having an arbitrary frame rate using hardware processing;

second video generator means for generating a second video data having an arbitrary frame rate different from the first video data;

means for using video reproduction time managed by the first video generator means as a reference time, and correcting video reproduction time of the second video generator means shifted from the reference time by repeatedly using the second video data to obtain frame synchronization; and

means for omitting a generation of a video frame when video generation of the second video generator means is delayed from video generation of the second video generator means.

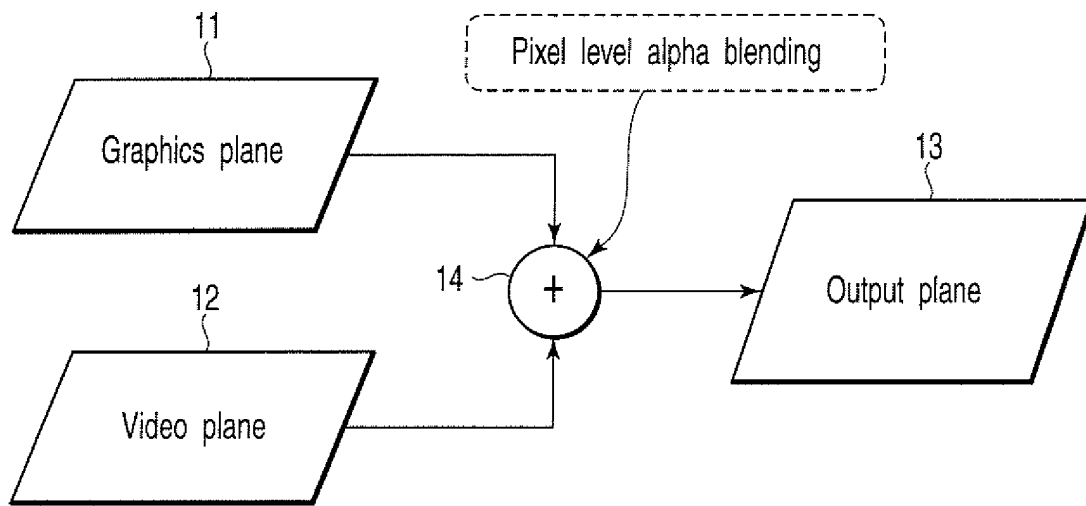


FIG. 1

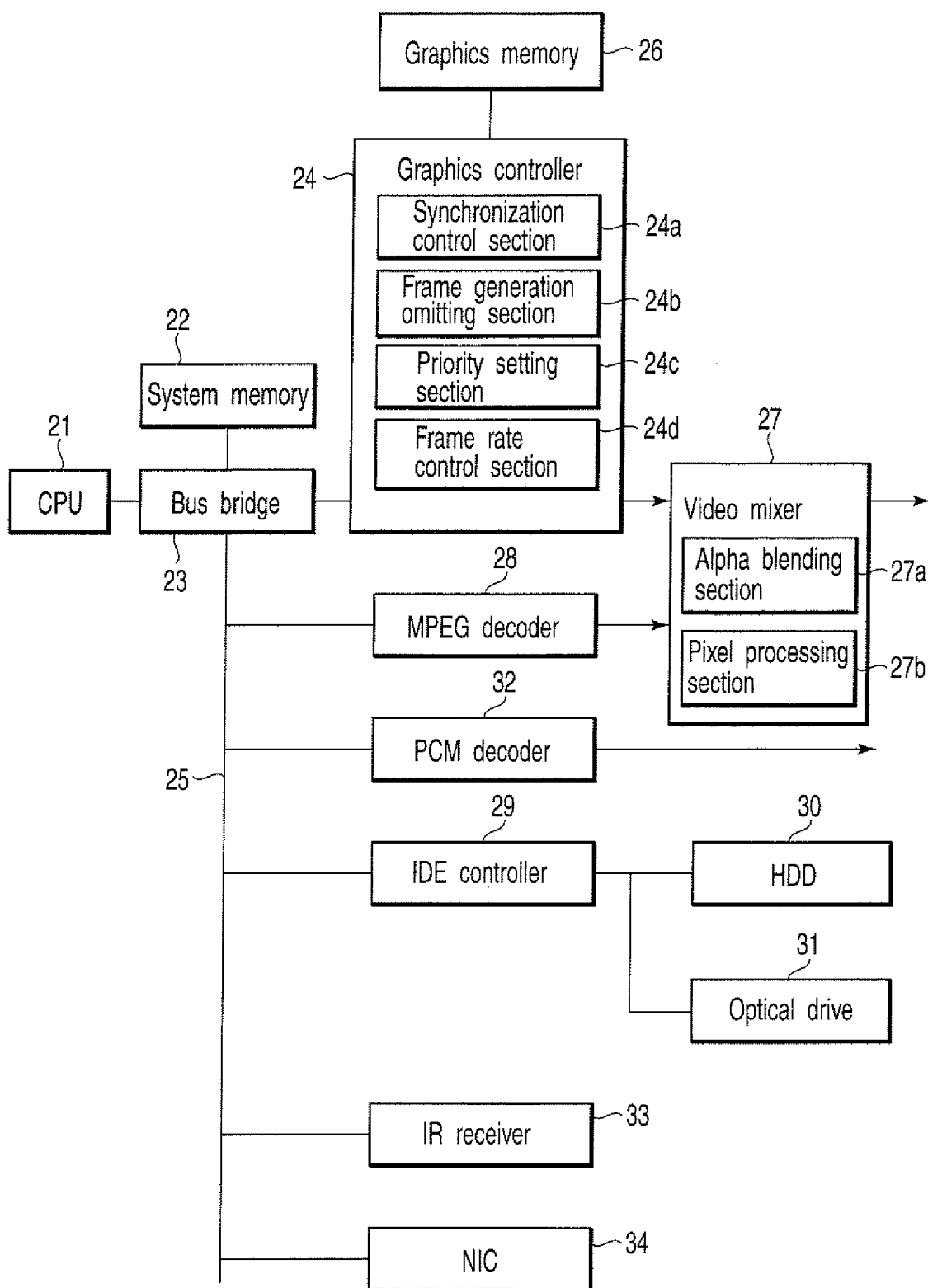


FIG. 2

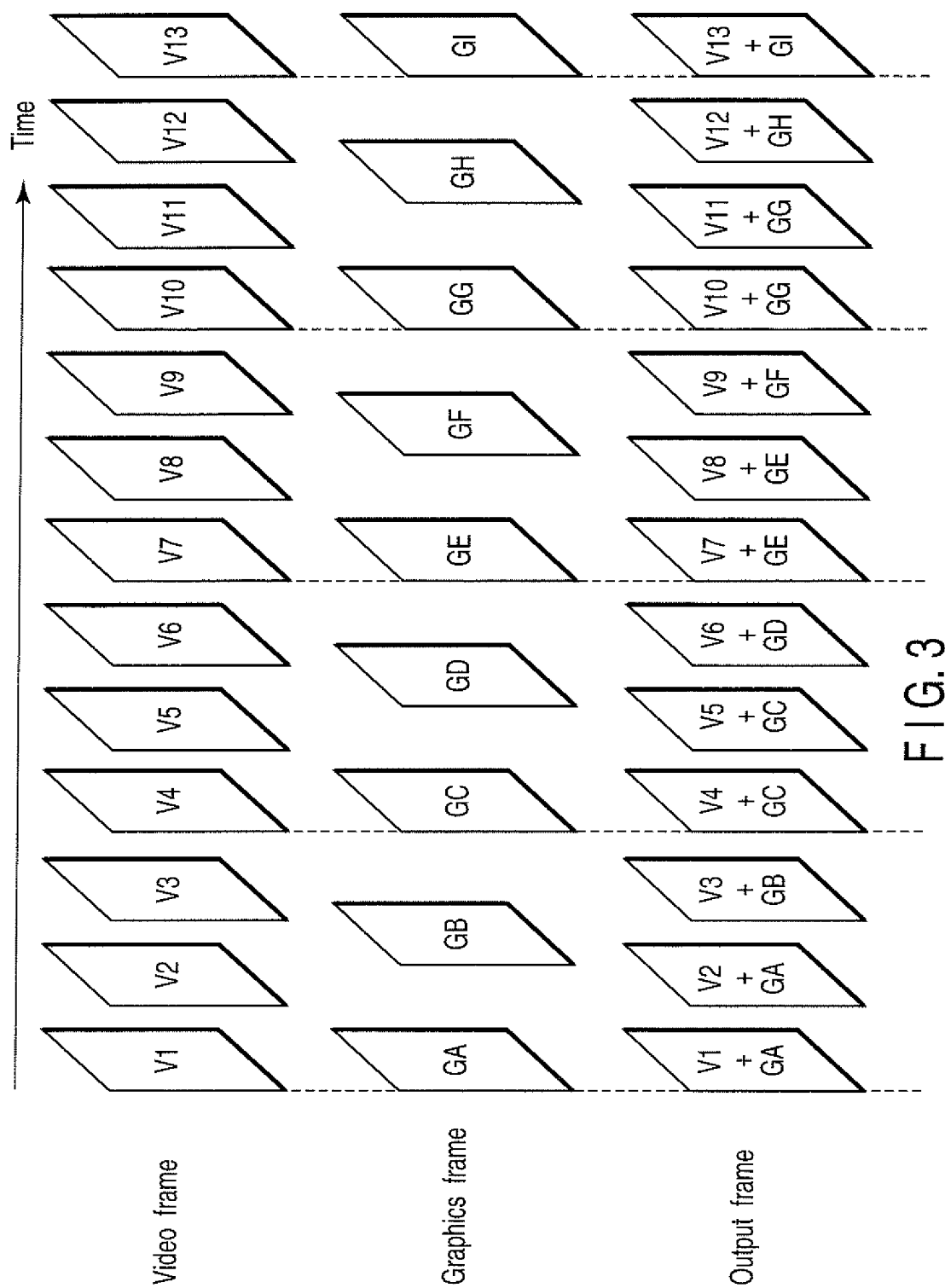


FIG. 3

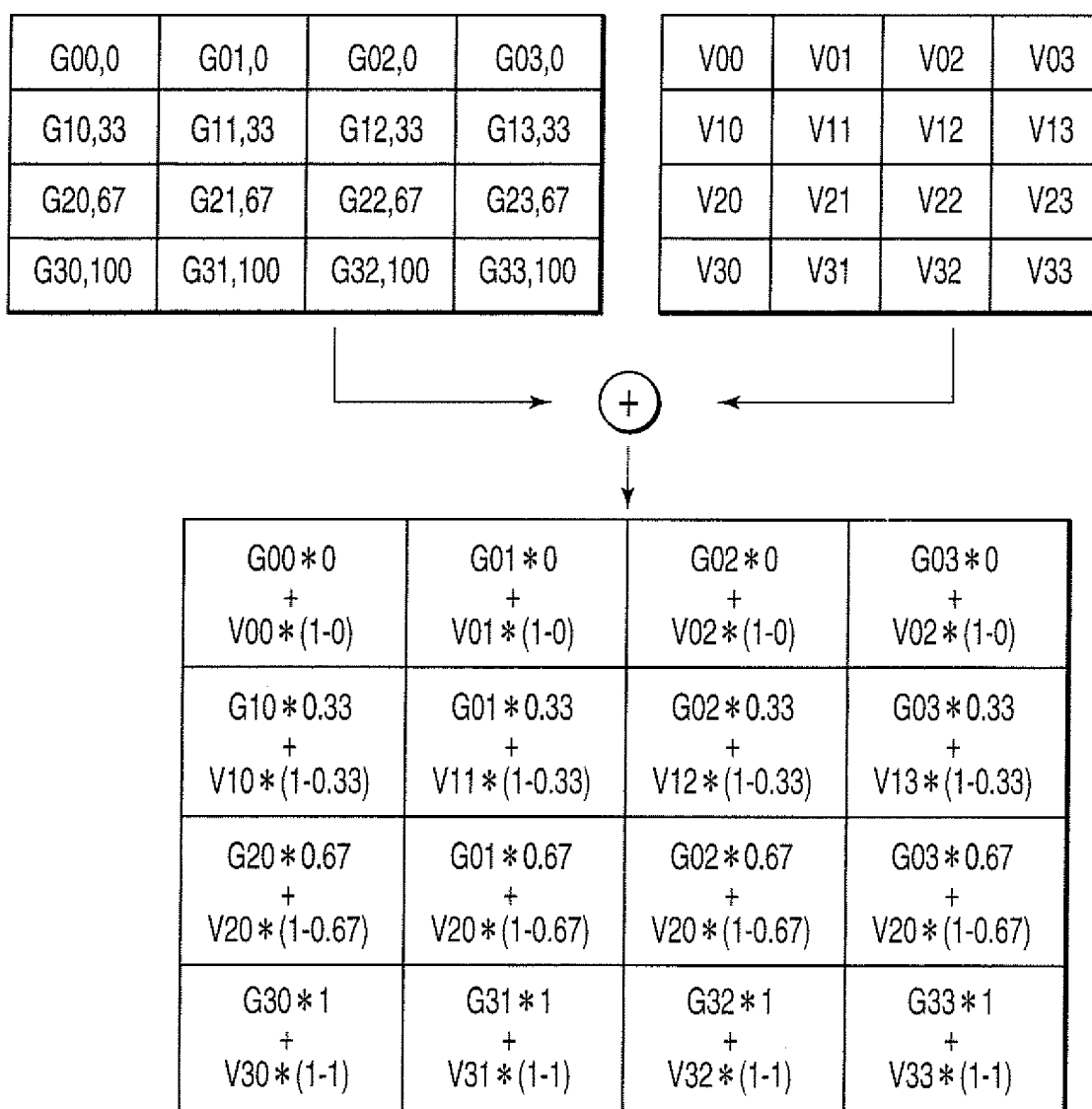
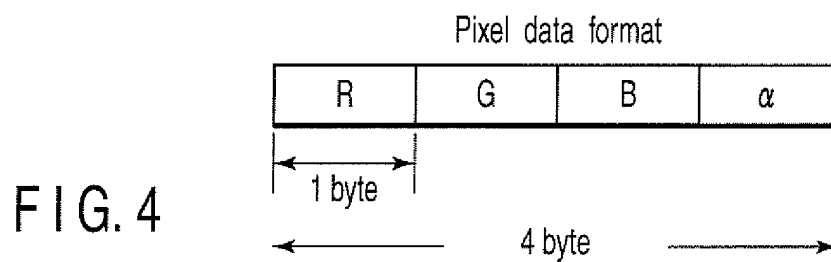
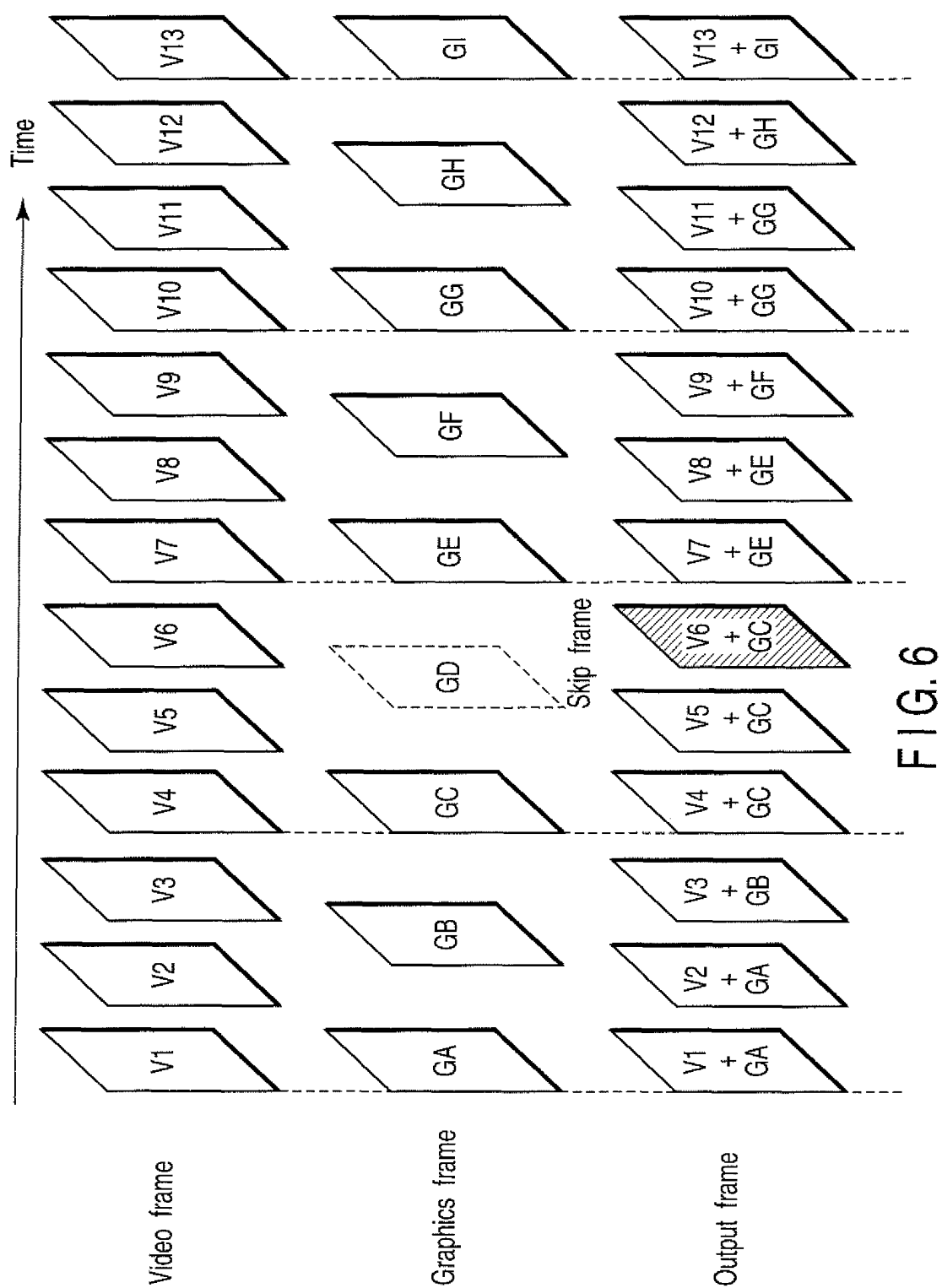


FIG. 5



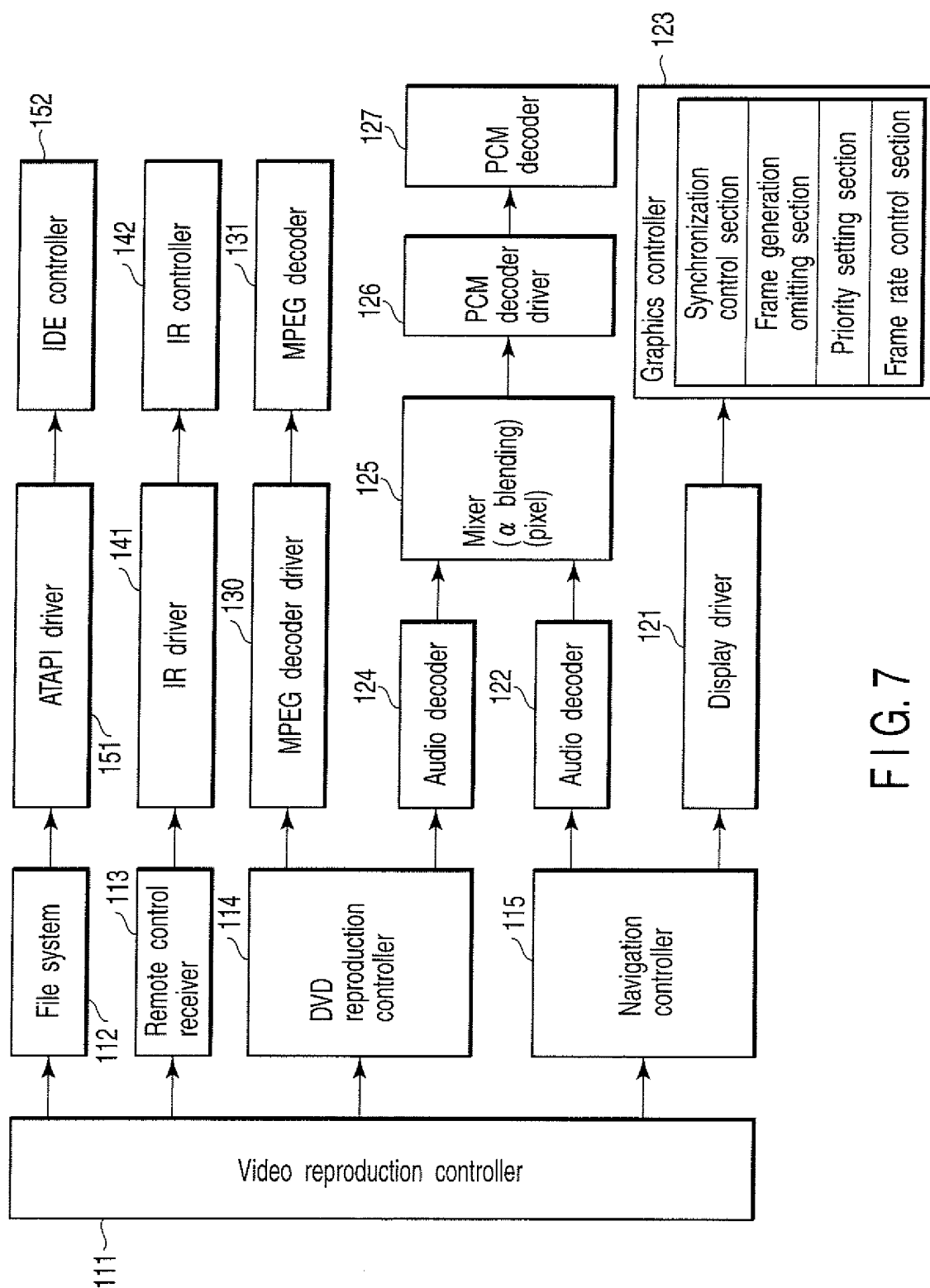


FIG. 7

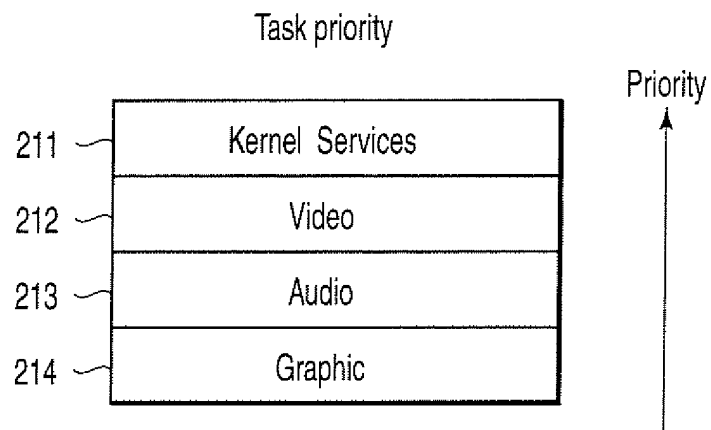


FIG. 8

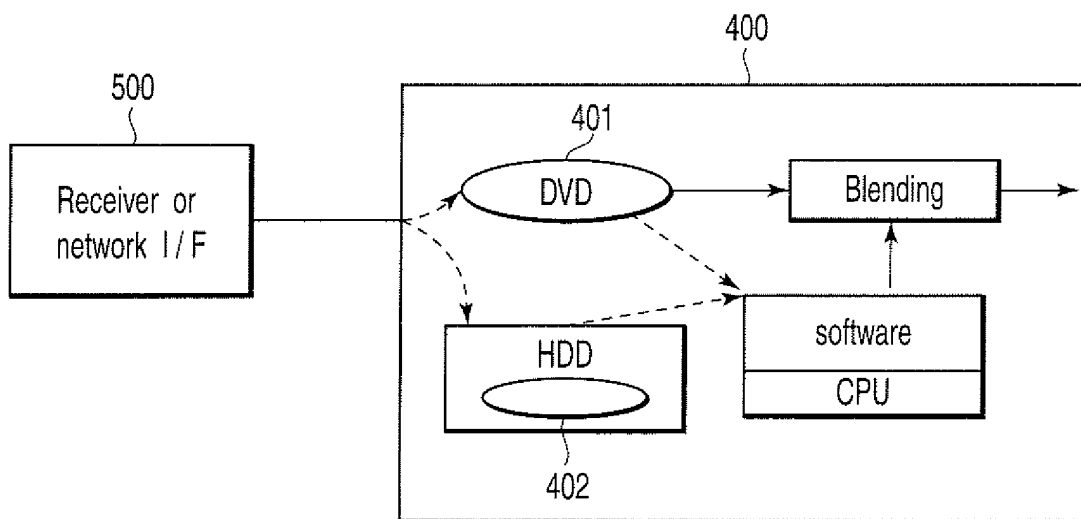


FIG. 11

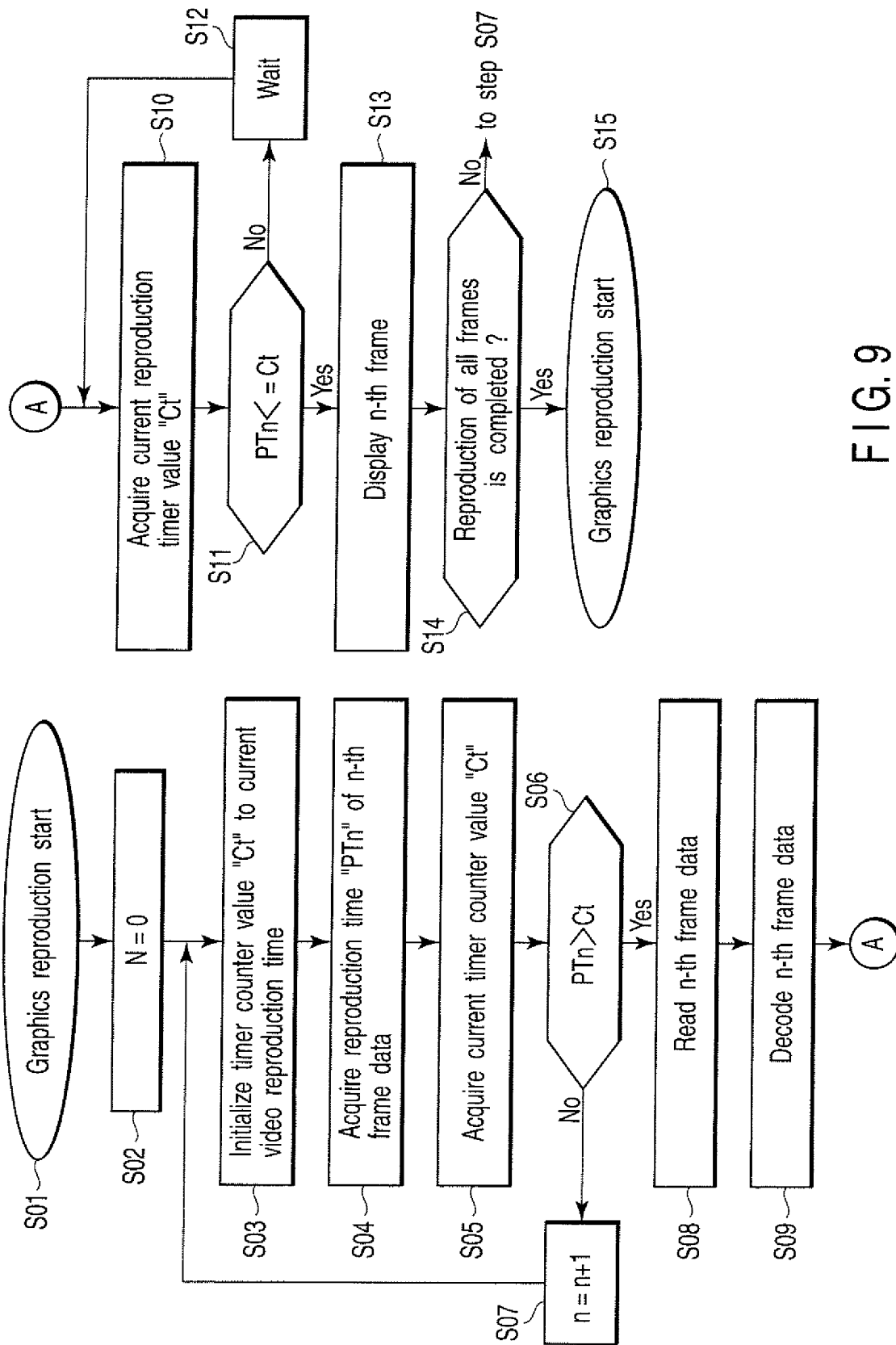


FIG. 9

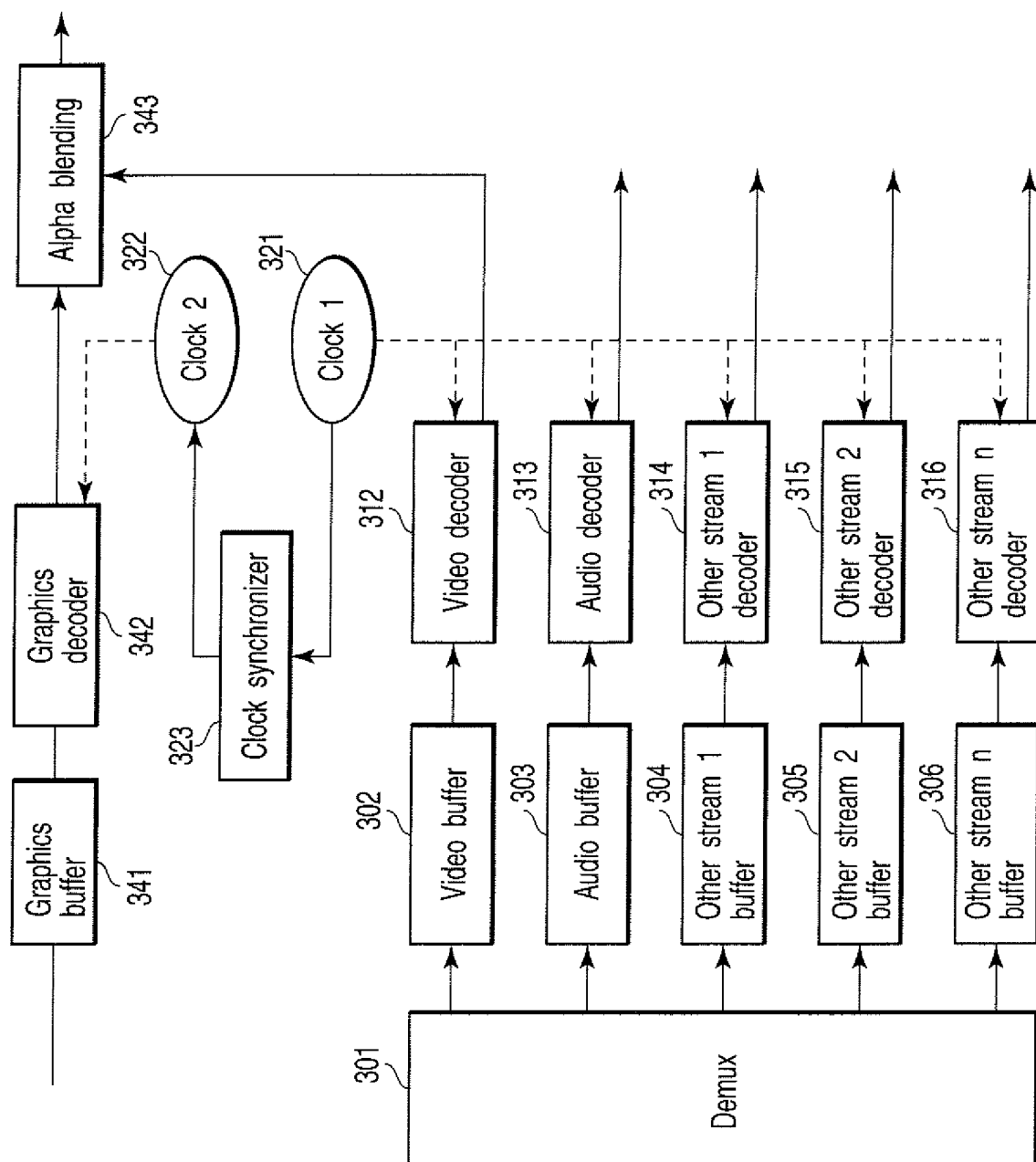


FIG. 10