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(12) **United States Design Patent**
Adamson et al.

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(45) **Date of Patent:** **** *Feb. 16, 2016**

(54) **ELECTRONIC COMPUTER WITH AN AT LEAST PARTIALLY TRANSPARENT INPUT DEVICE**

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(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **14 Years**

(21) Appl. No.: **29/478,161**

(22) Filed: **Dec. 31, 2013**

Related U.S. Application Data

(62) Division of application No. 29/423,472, filed on May 31, 2012, now Pat. No. Des. 698,778.

(51) **LOC (10) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/318**

(58) **Field of Classification Search**

USPC D14/315-327; D18/1, 2, 7, 11;
235/145 A, 145 R; 341/22, 23; 345/104,
345/156, 168, 169, 173; 361/679.08,
361/679.09, 679.11, 679.26, 679.27
CPC G06F 1/16; G06F 1/1616; H01R 35/02;
H03M 11/00; G09B 13/04; H03K 17/94
See application file for complete search history.

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Primary Examiner — Freda S Nunn

(74) *Attorney, Agent, or Firm* — Patent Capital Group

(57)

CLAIM

The ornamental design for an electronic computer with an at least partially transparent input device, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view showing of a first embodiment of our new design for an electronic computer with an at least partially transparent input device;

FIG. 2 is a front view thereof;

FIG. 3 is a back view thereof;

FIG. 4 is a side view thereof;

FIG. 5 is another side view thereof;

FIG. 6 is a top view thereof;

FIG. 7 is a bottom view thereof;

FIG. 8 is a top perspective view showing a second embodiment of a design for an electronic computer with an at least partially transparent input device;

FIG. 9 is a front view thereof;

FIG. 10 is a back view thereof;

FIG. 11 is a side view thereof;

FIG. 12 is another side view thereof;

FIG. 13 is a top view thereof;

FIG. 14 is a bottom view thereof;

FIG. 15 is a top perspective view showing a third embodiment of a design for an electronic computer with an at least partially transparent input device;

FIG. 16 is a front view thereof;

FIG. 17 is a back view thereof;

FIG. 18 is a side view thereof;

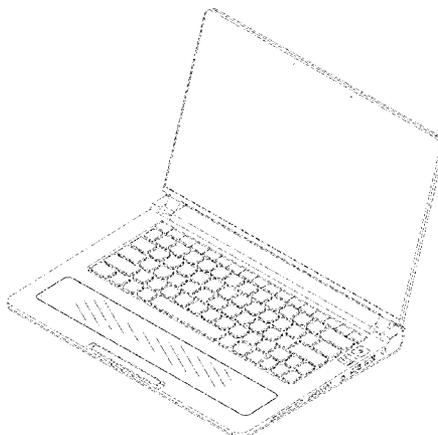
FIG. 19 is another side view thereof;

FIG. 20 is a top view thereof; and,

FIG. 21 is a bottom view thereof.

The broken lines in the preceding FIGURES illustrate portions of the electronic computer and form no part of the claimed design.

1 Claim, 21 Drawing Sheets



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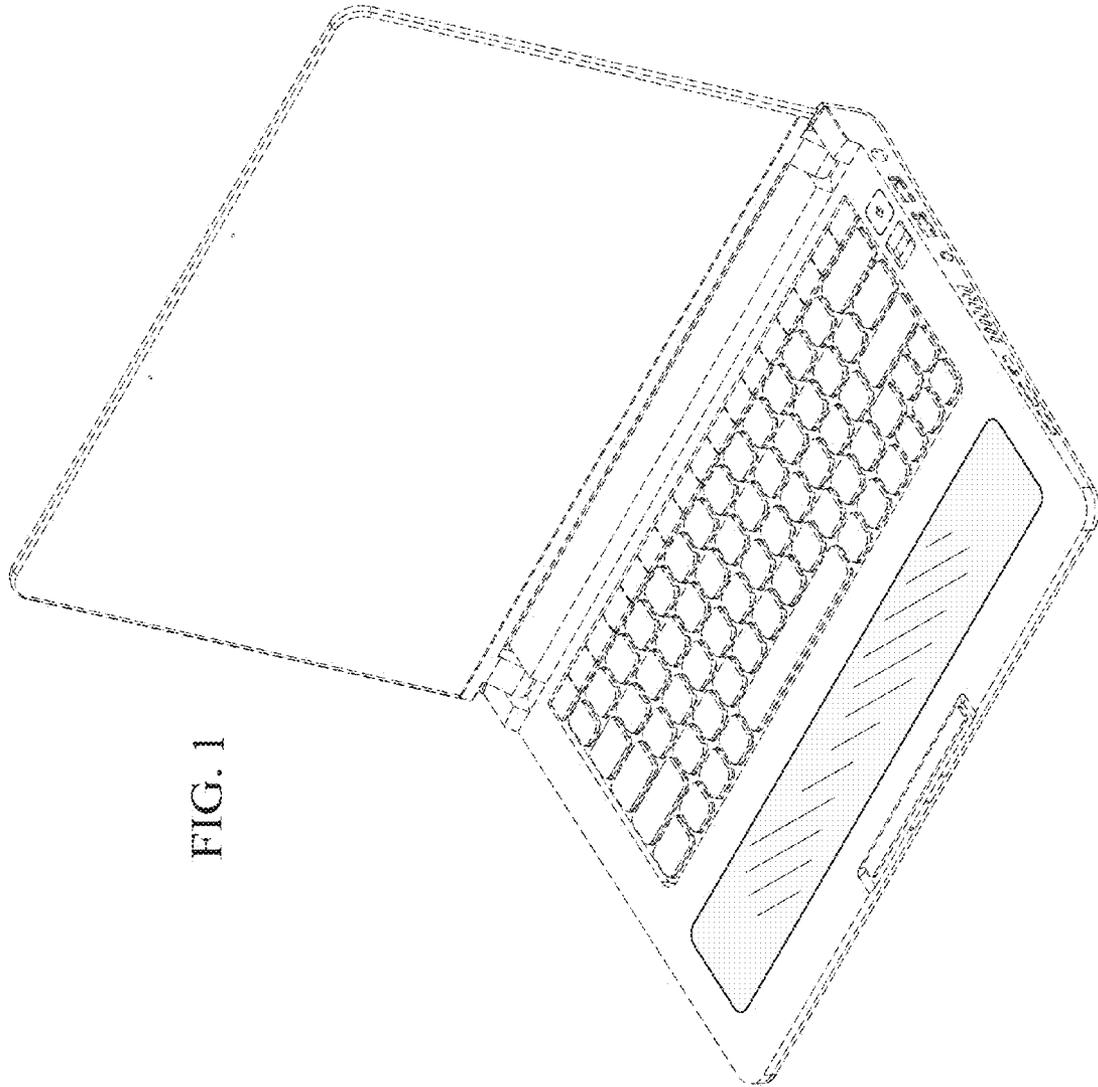


FIG. 1

FIG. 2

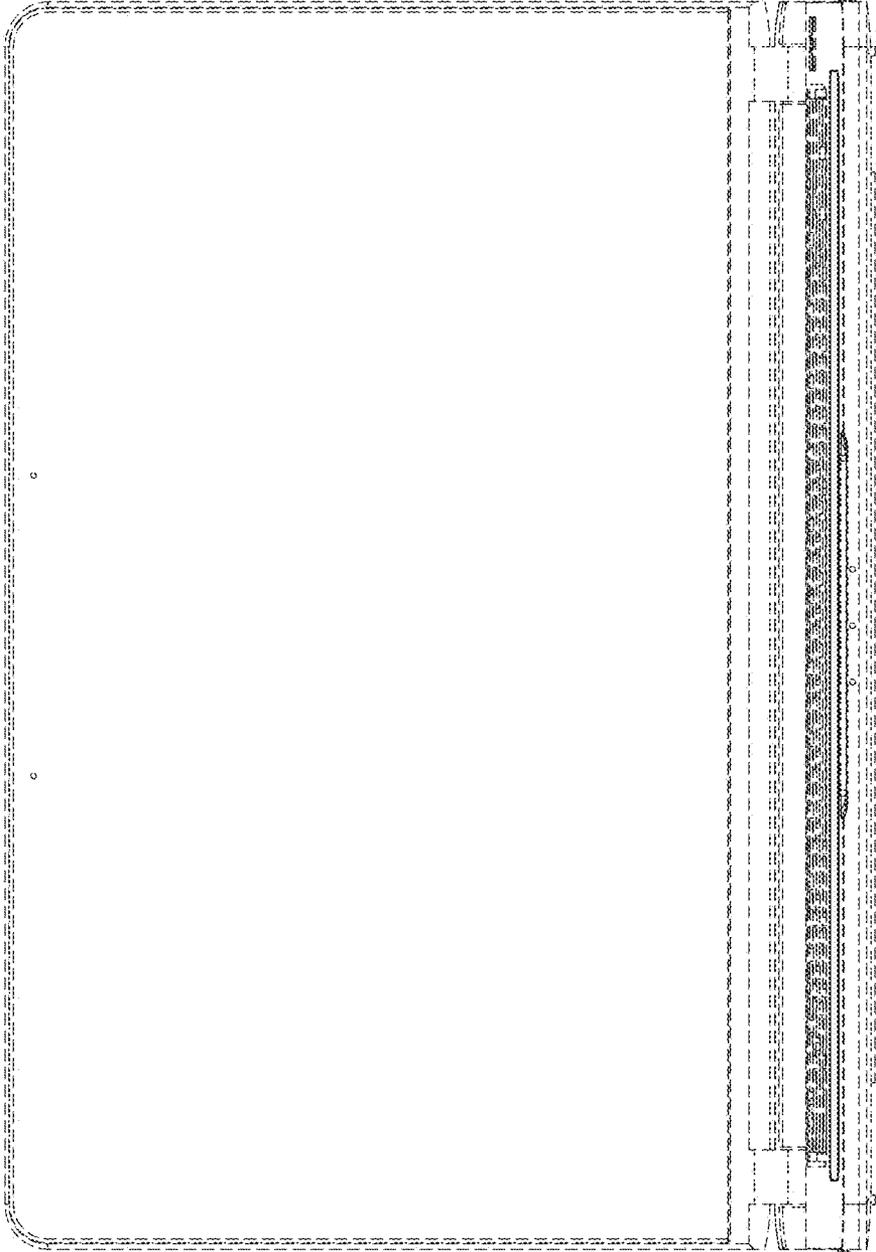
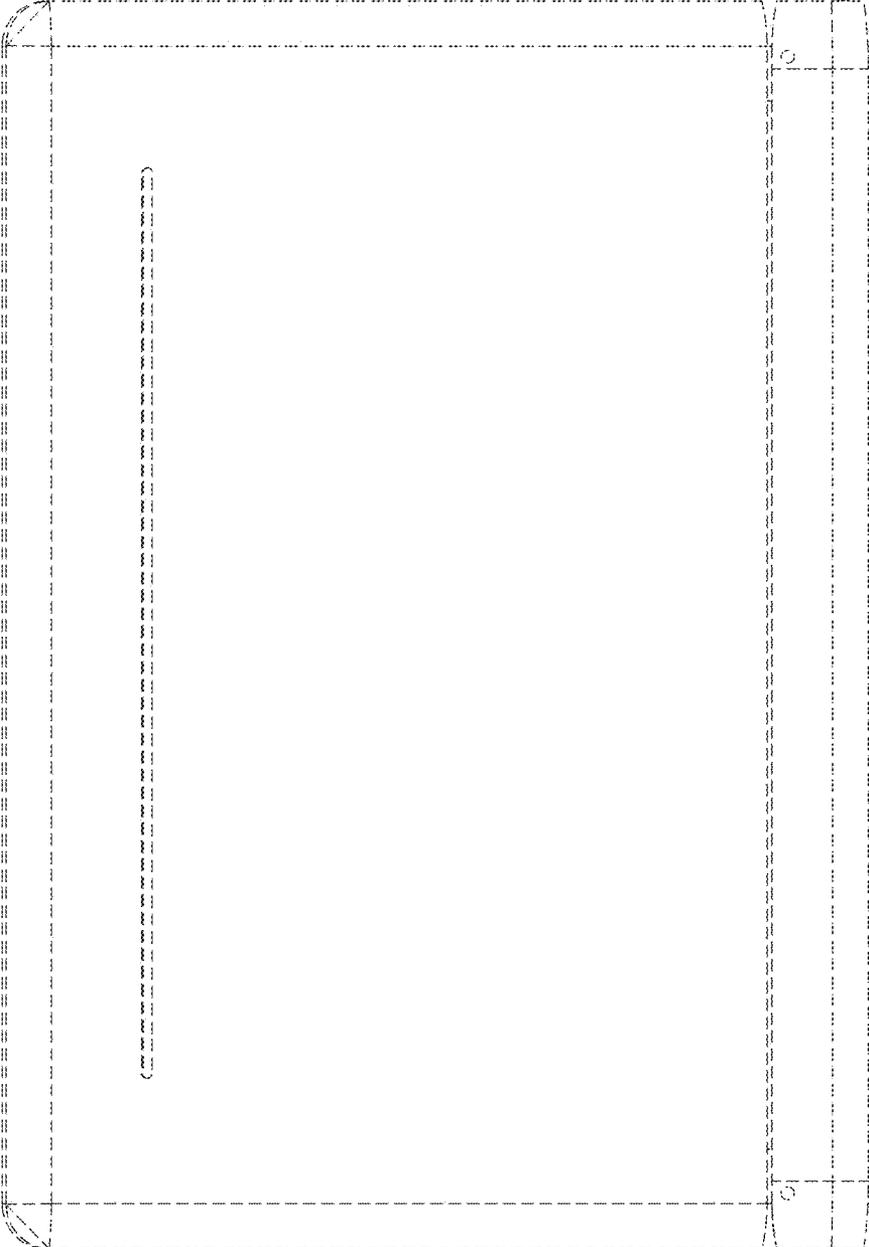


FIG. 3



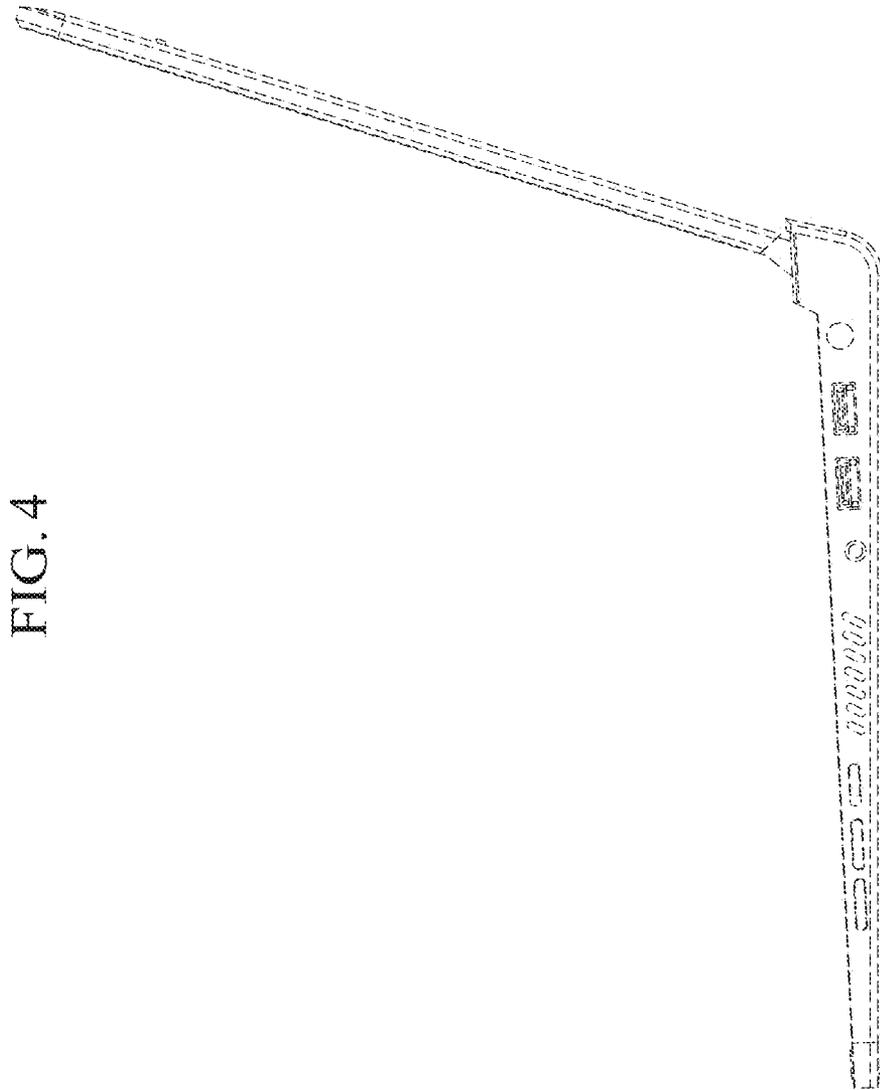


FIG. 4

FIG. 5

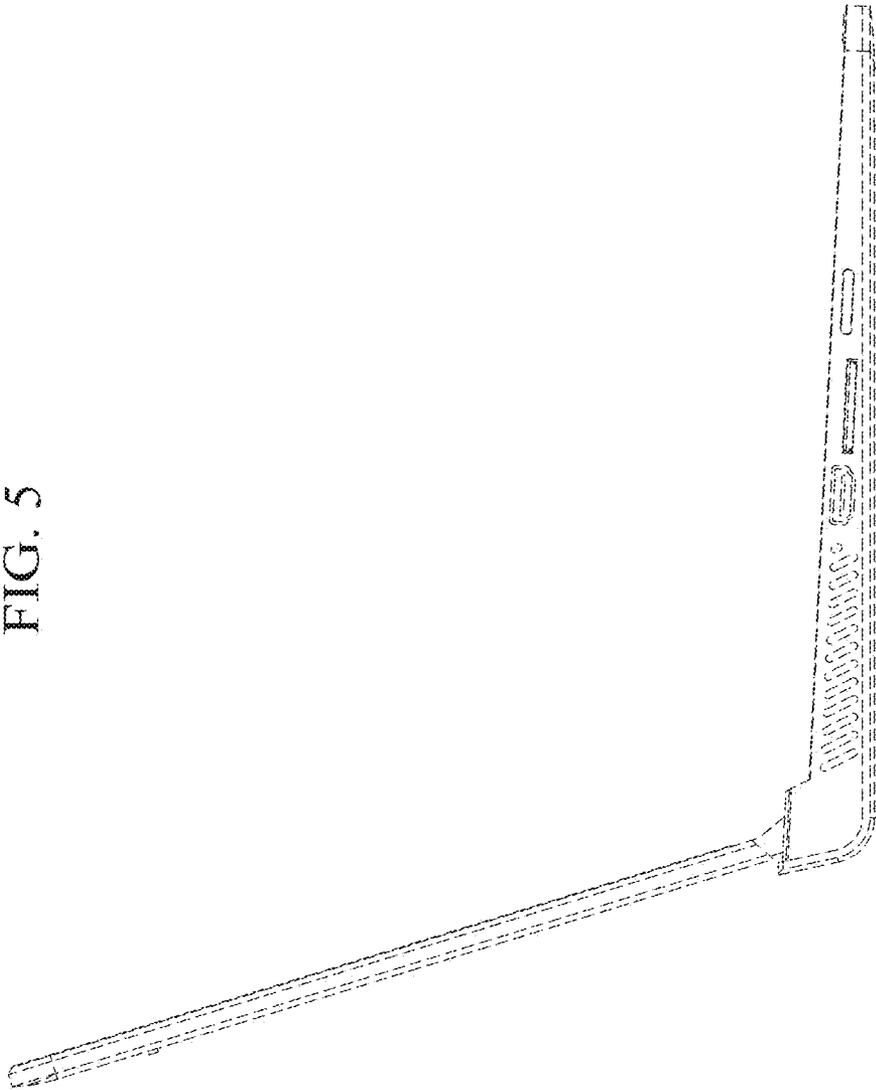


FIG. 6

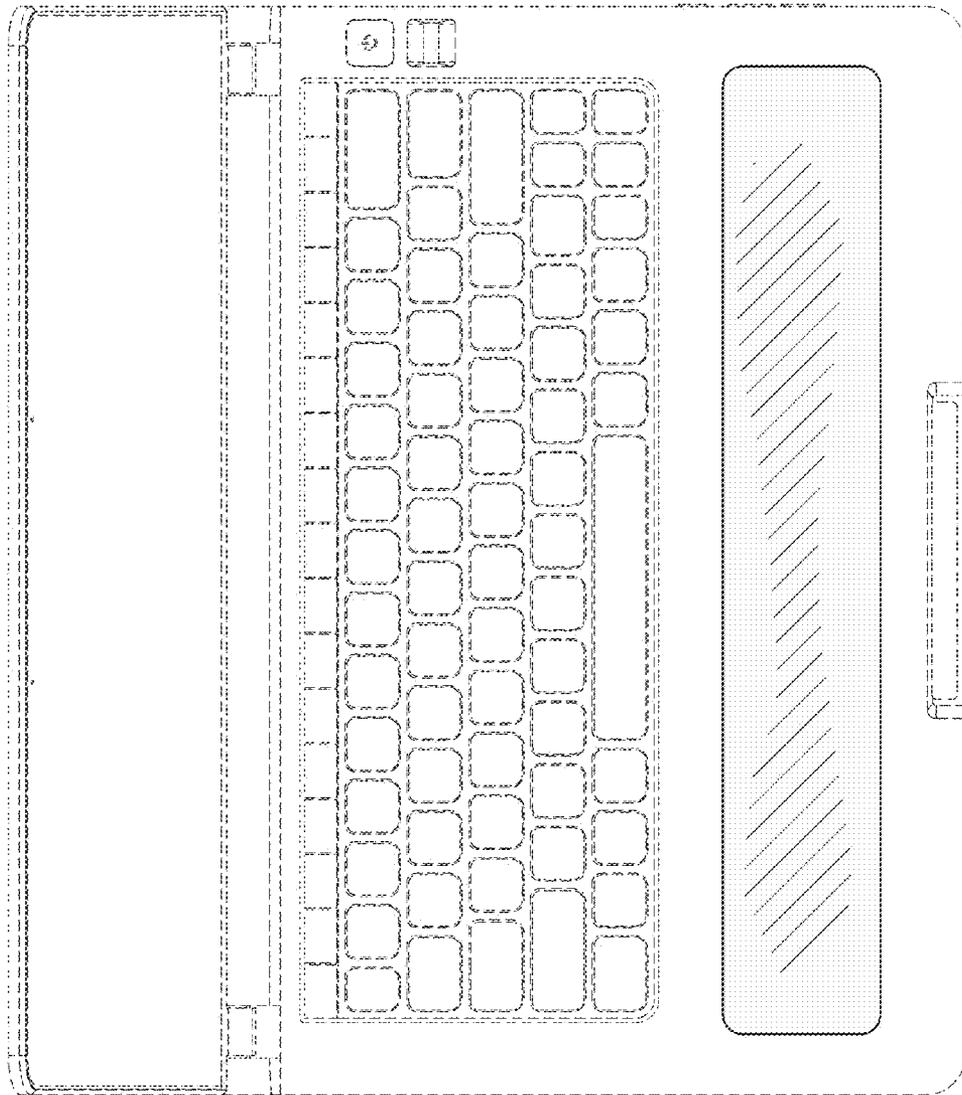
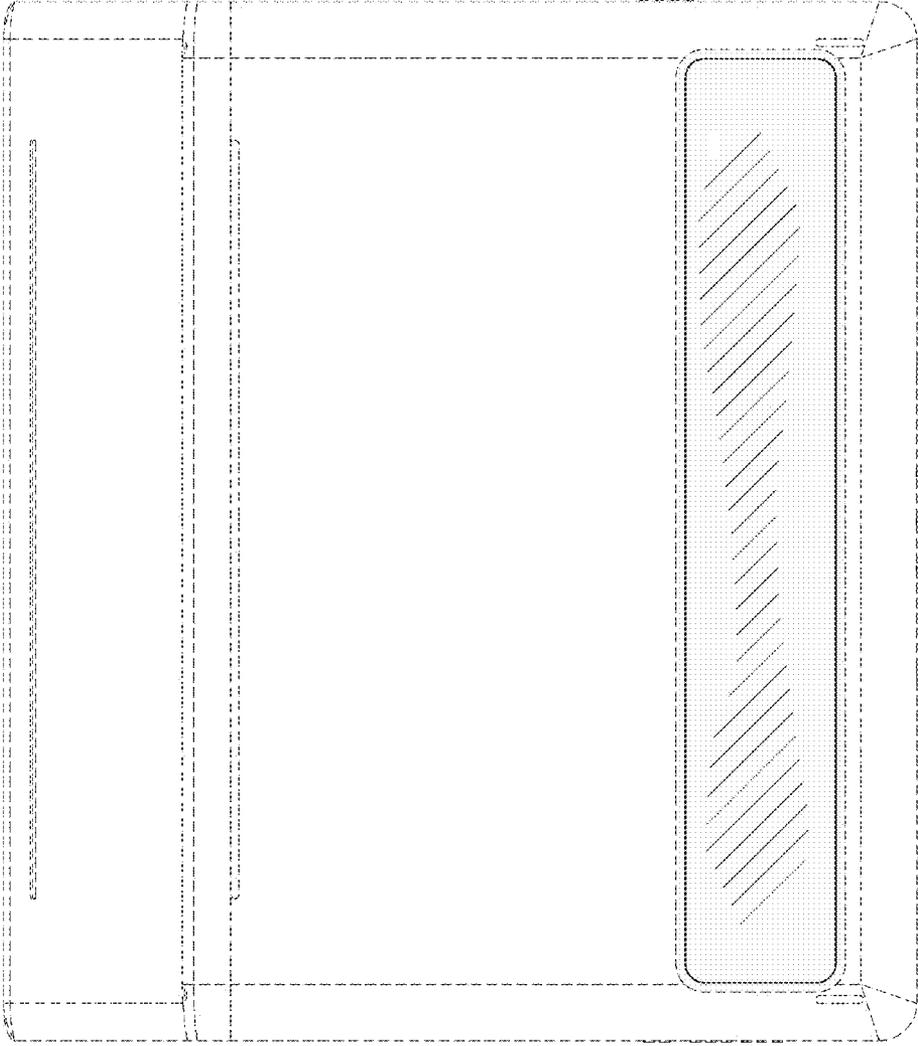


FIG. 7



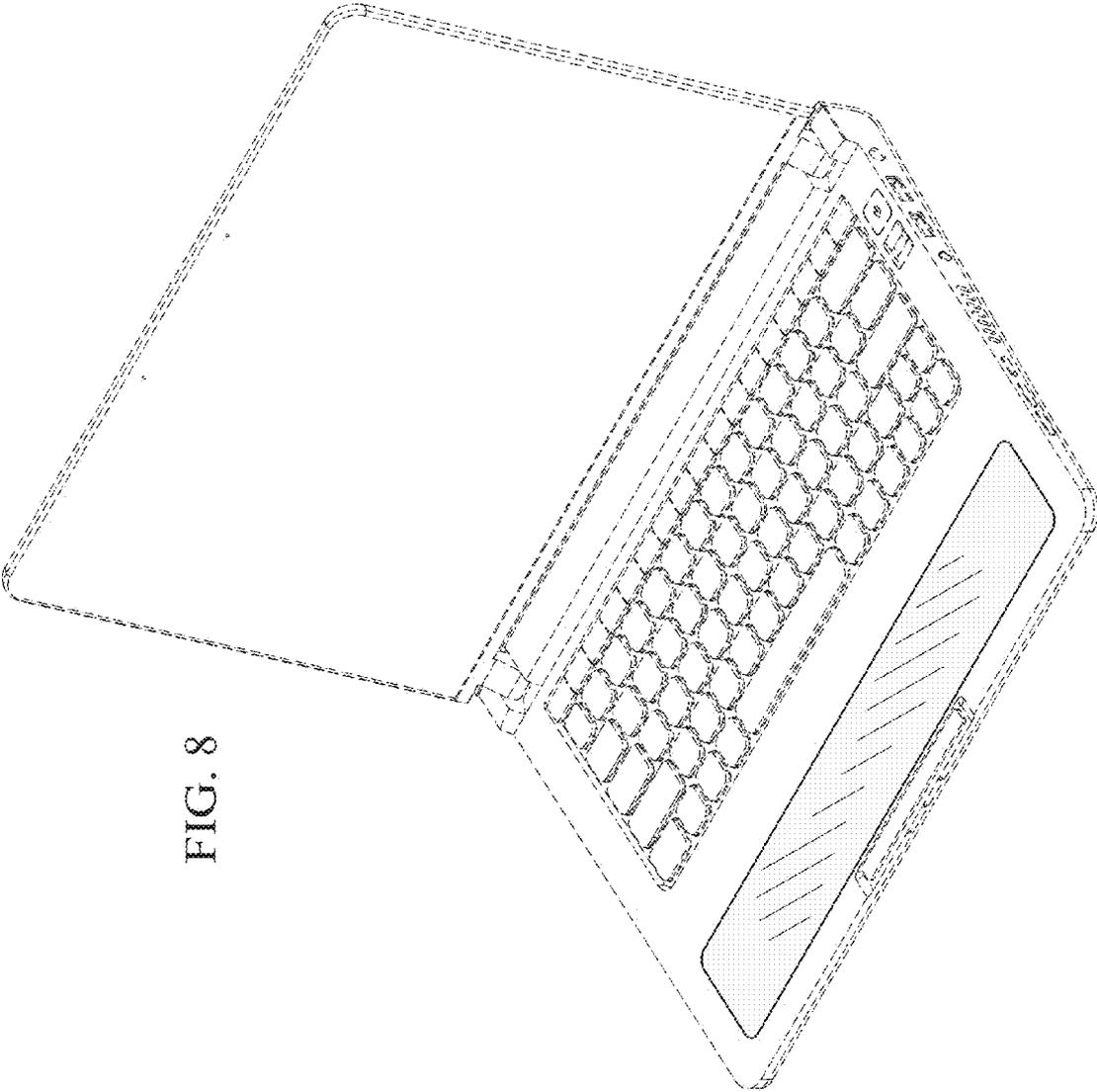


FIG. 8

FIG. 9

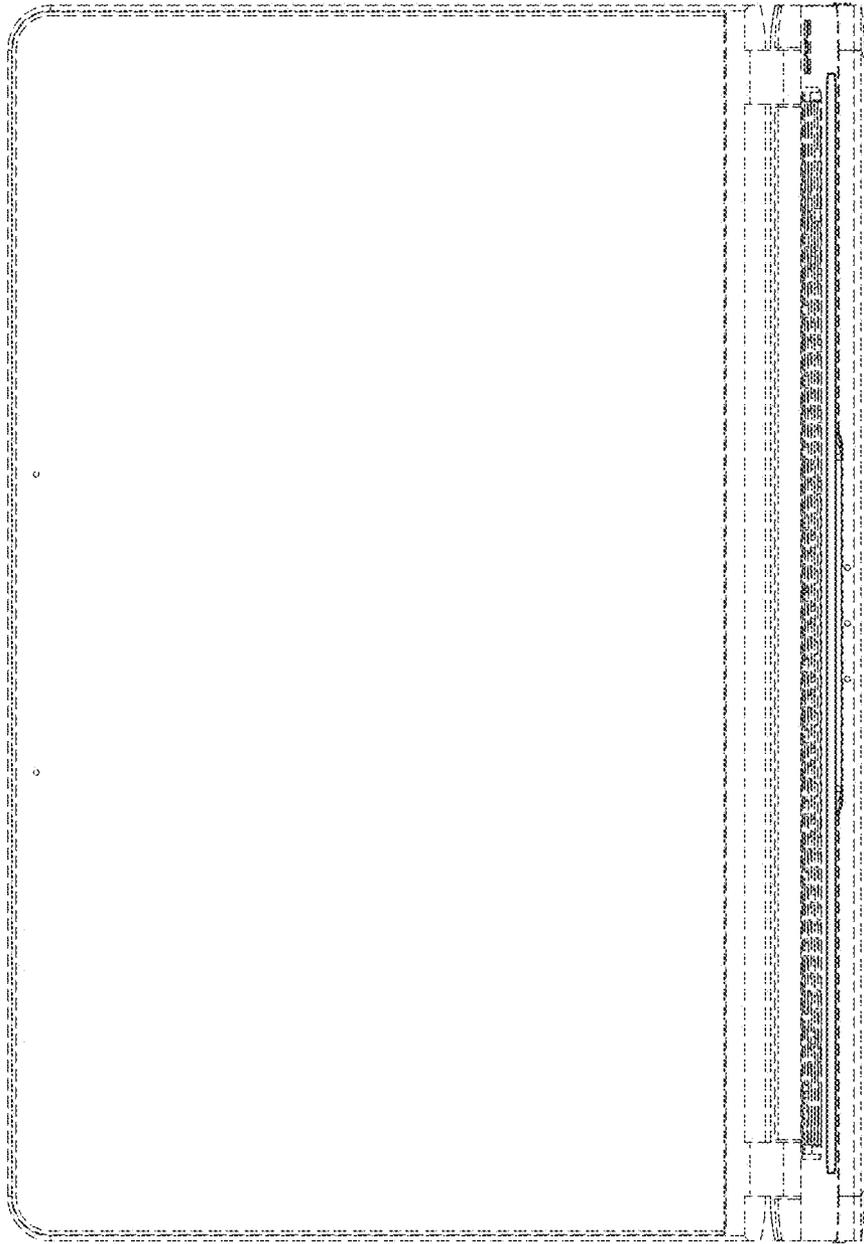


FIG. 10

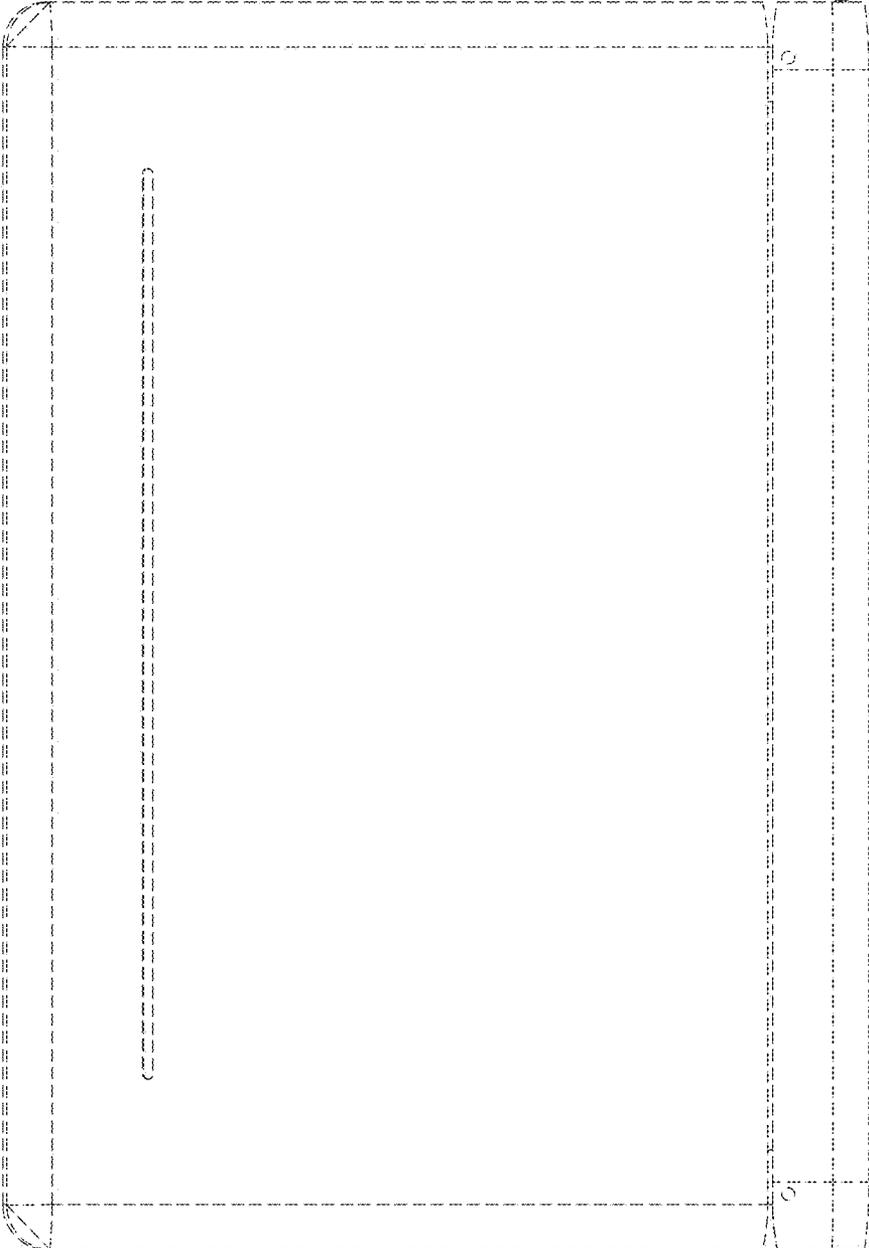


FIG. 11

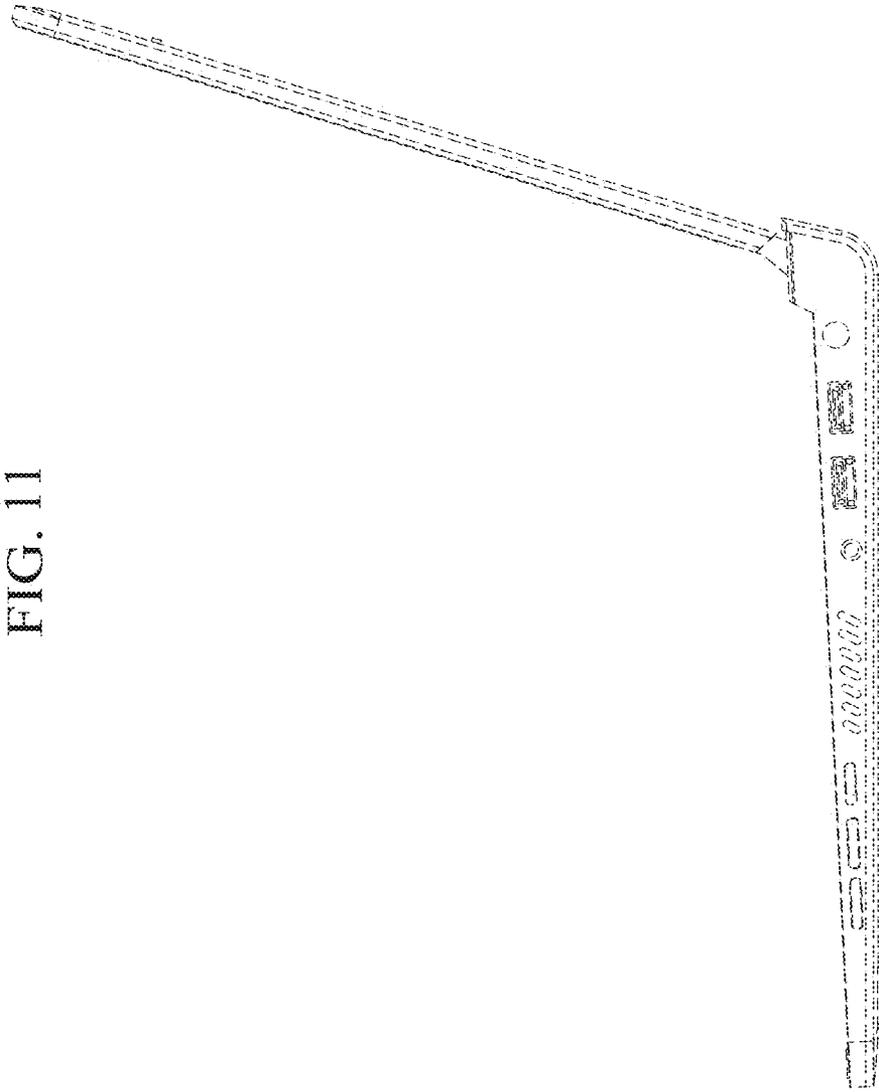


FIG. 12

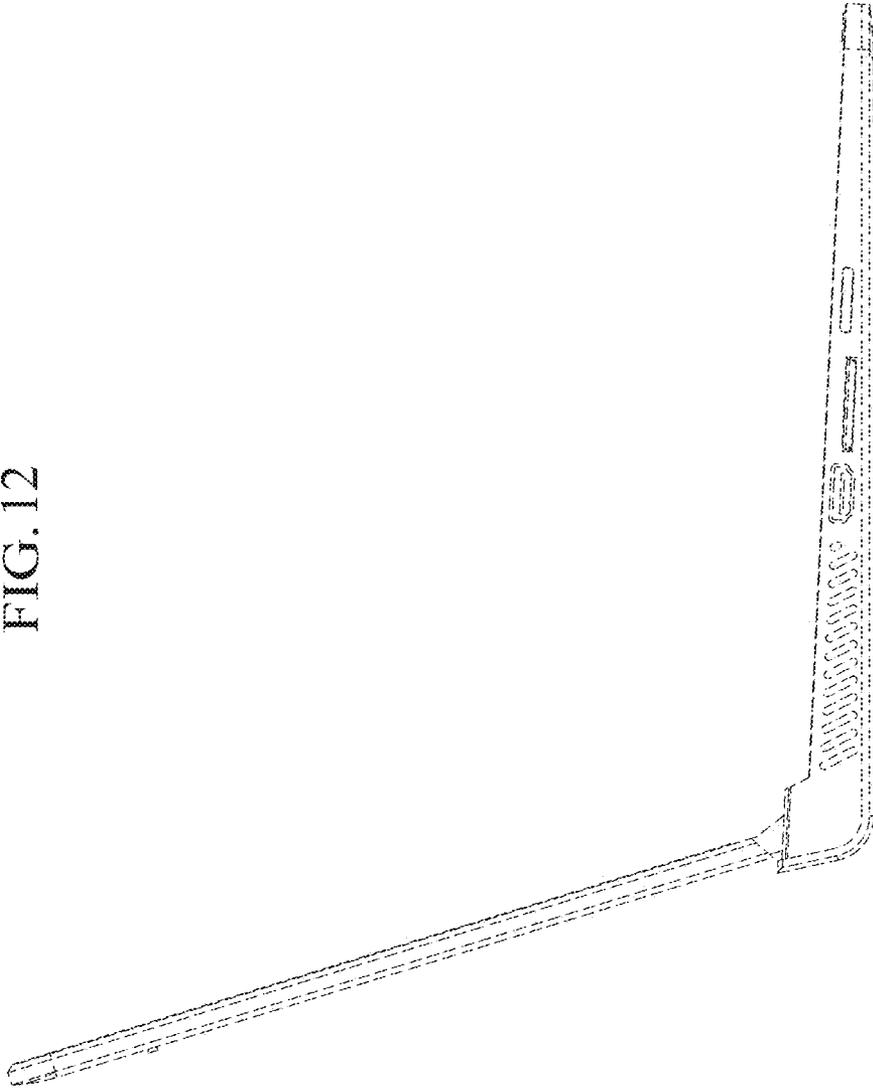


FIG. 13

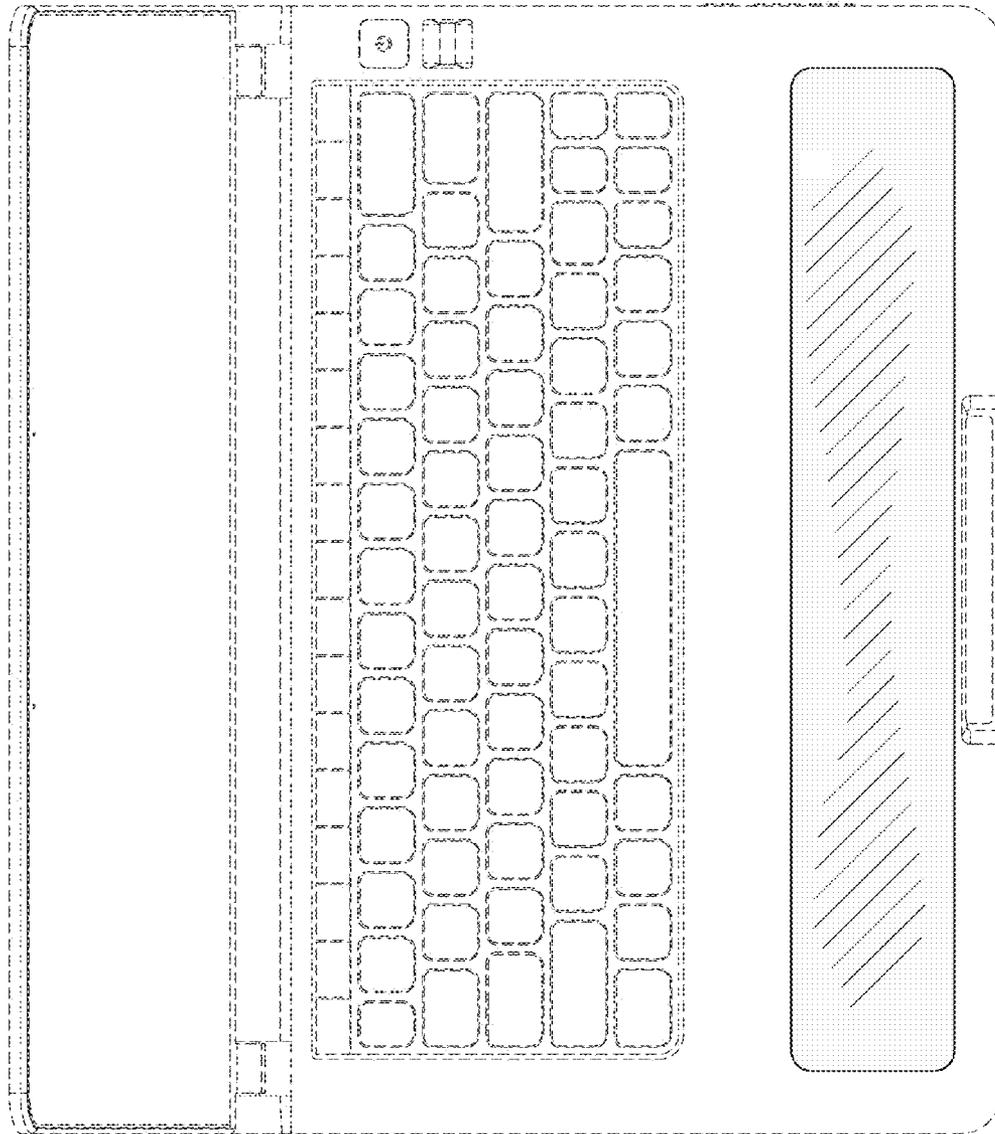
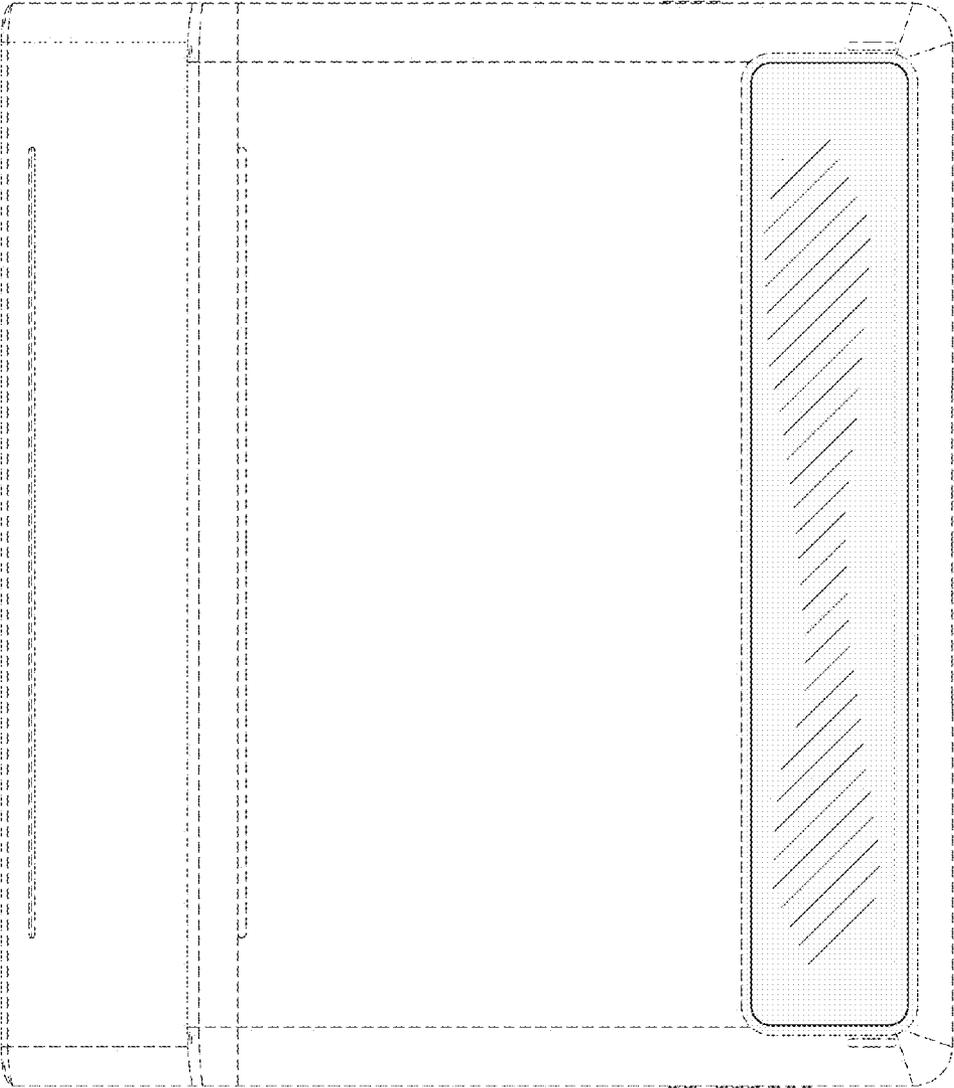


FIG. 14



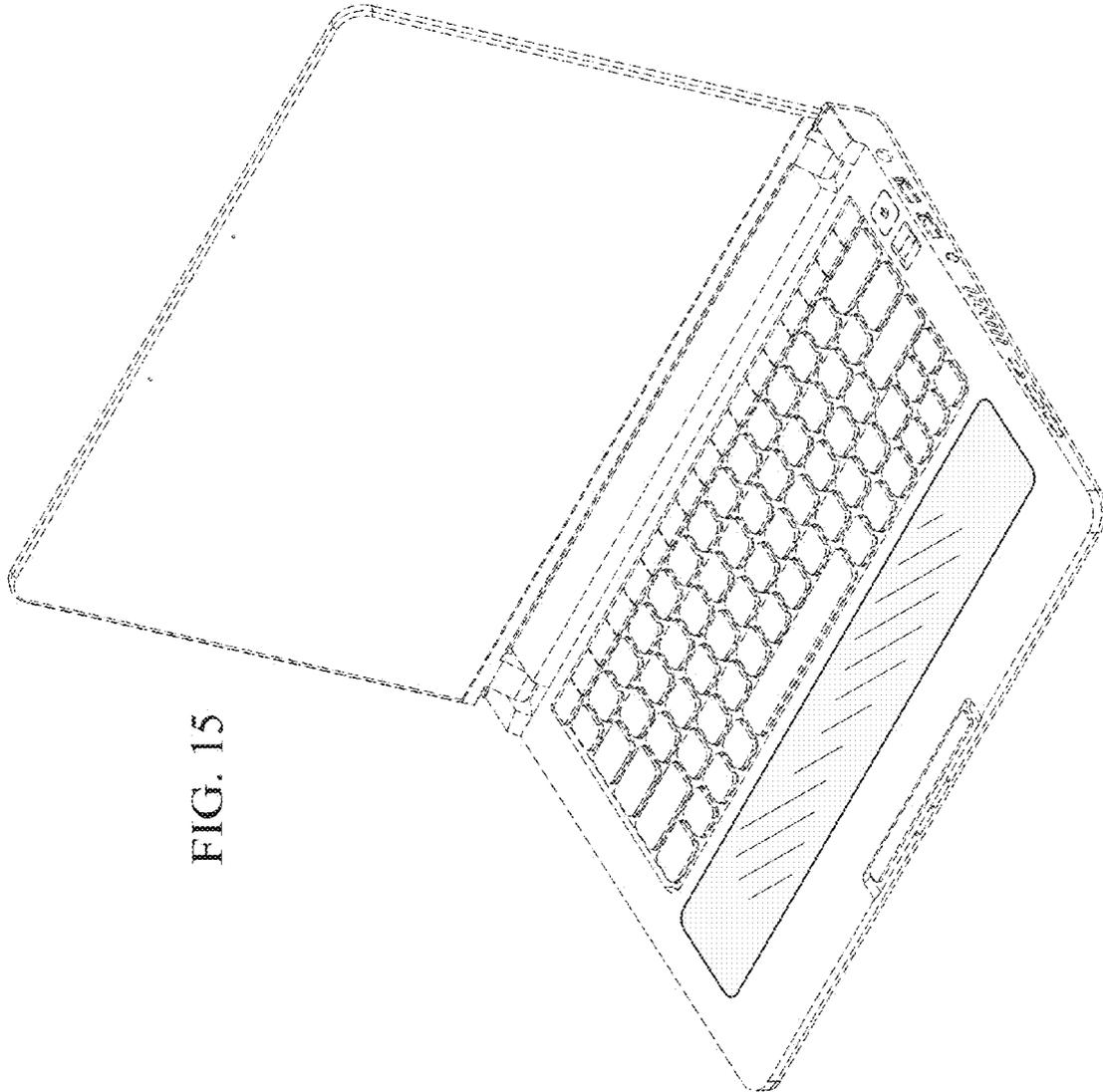


FIG. 15

FIG. 16

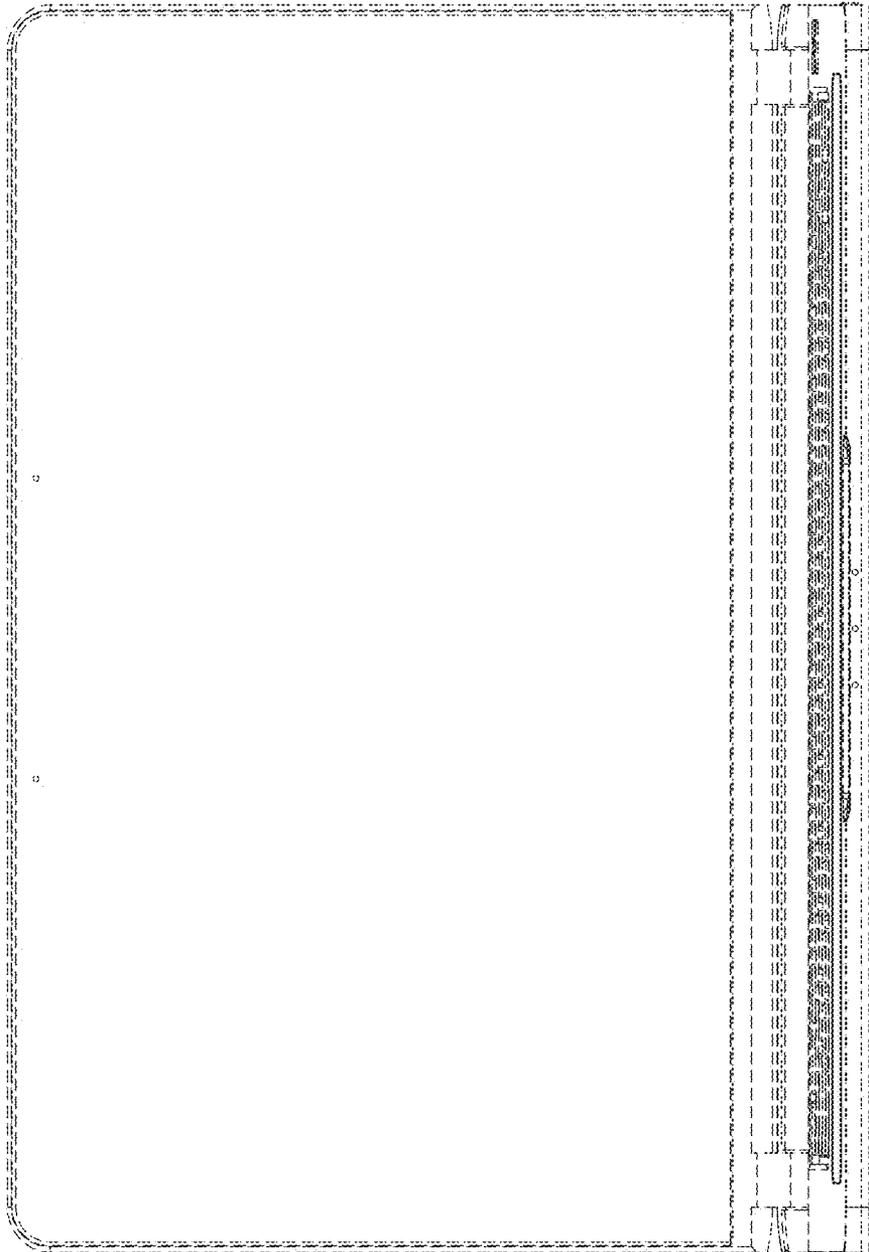


FIG. 17

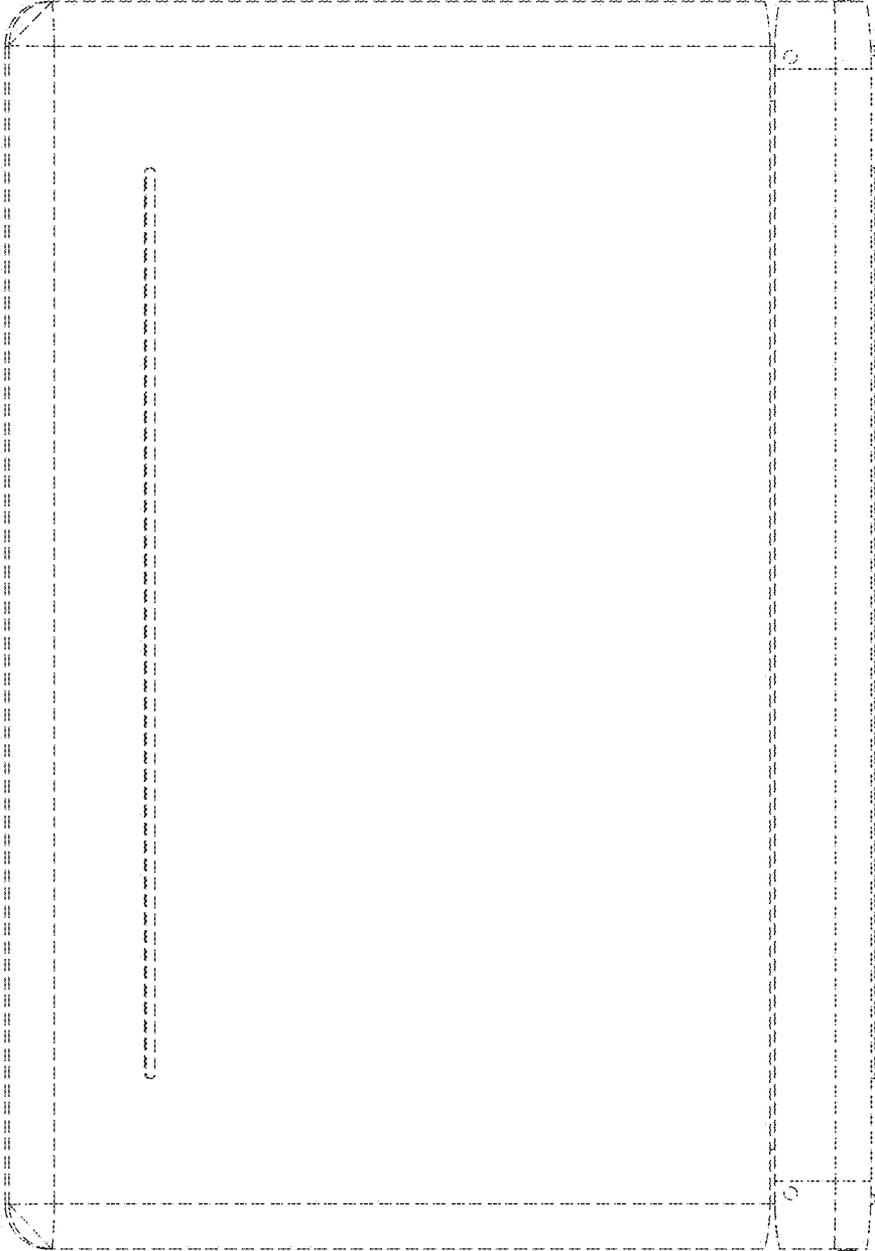


FIG. 18

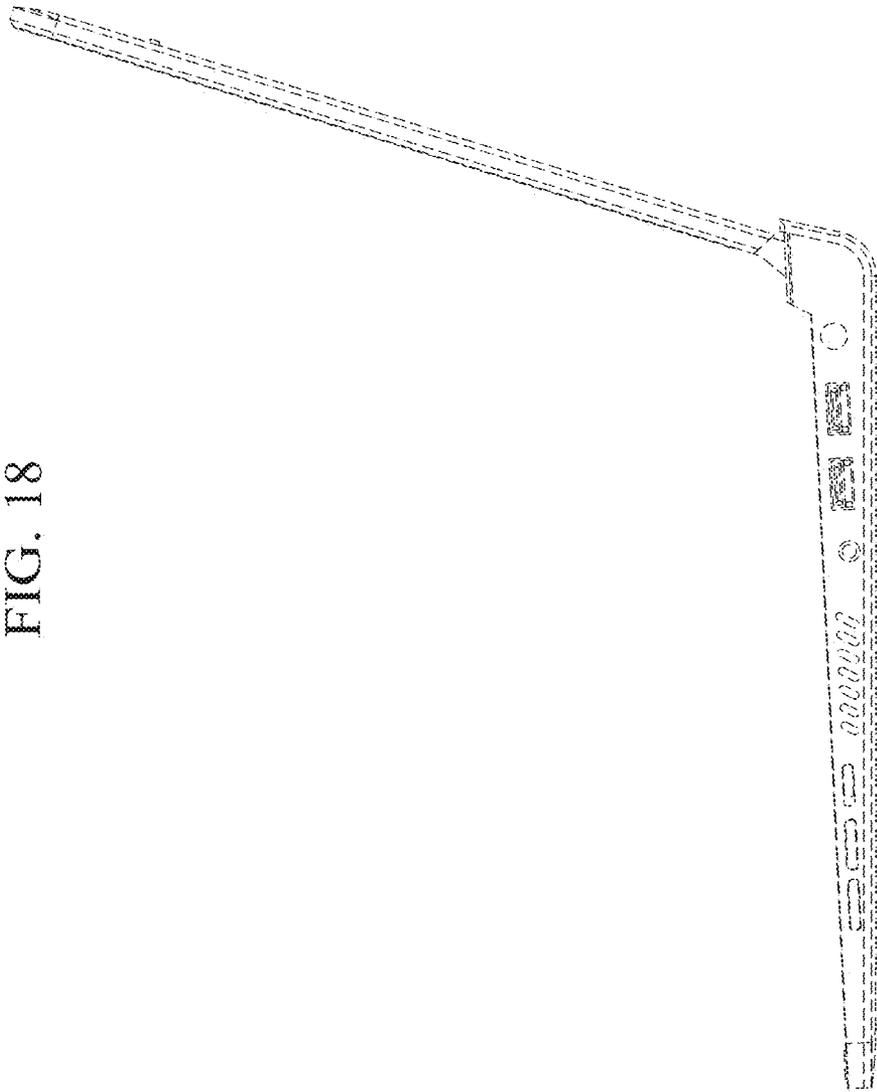


FIG. 19

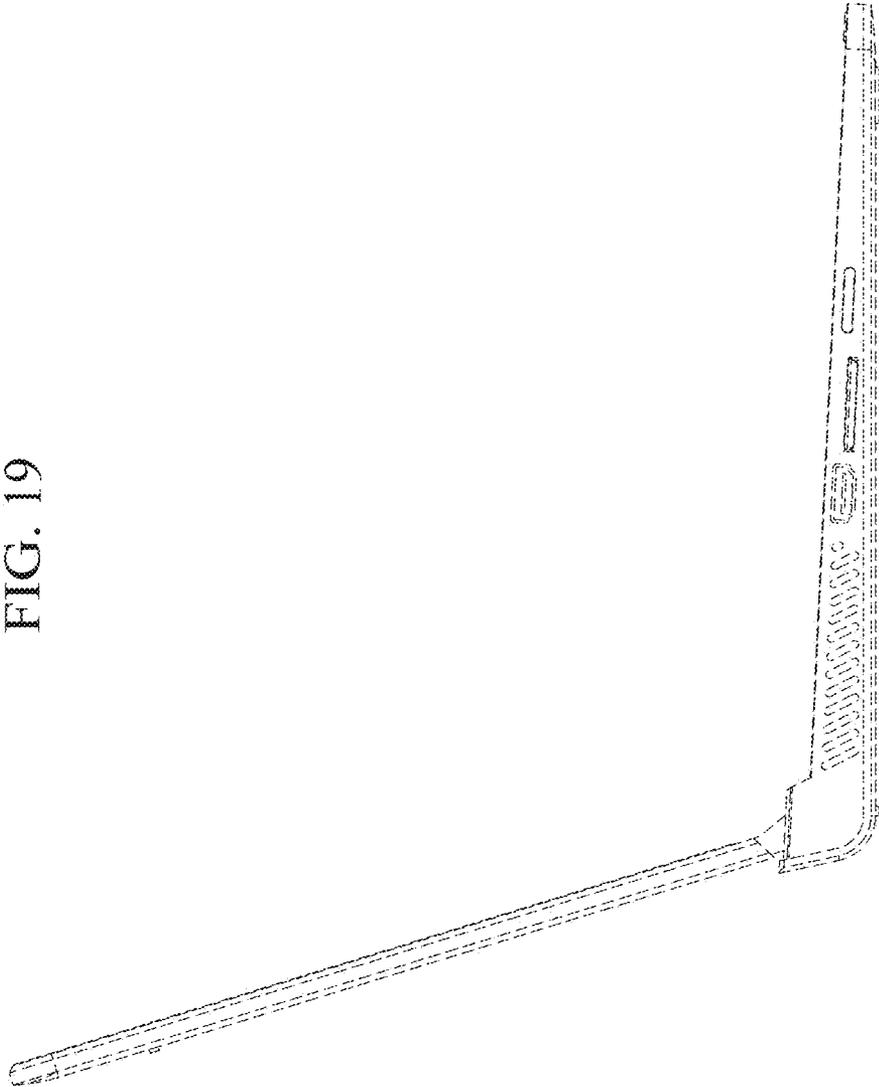


FIG. 20

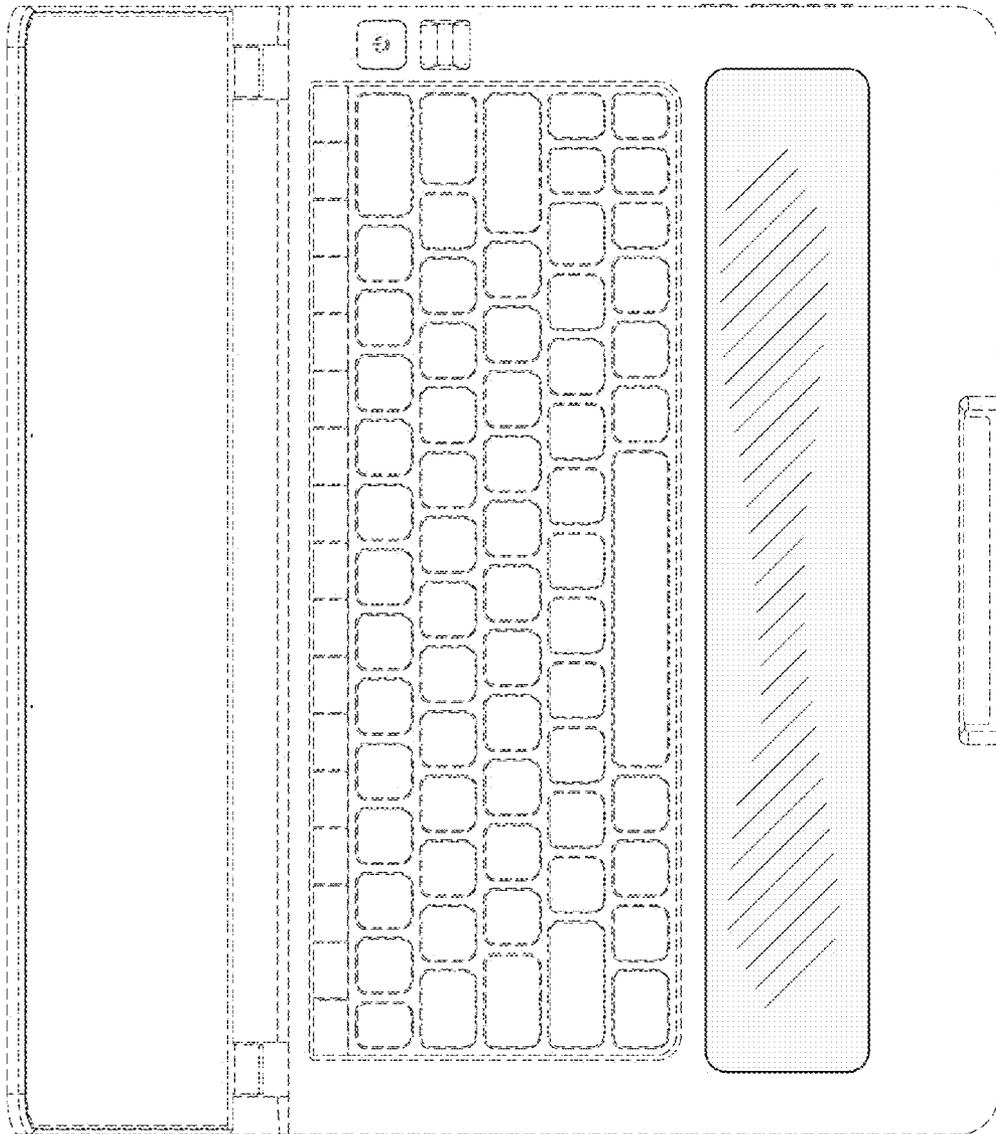


FIG. 21

