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(71) **Applicant** (for all designated States except US): **CASIO COMPUTER CO., LTD.** [JP/JP]; 6-2, Hon-machi 1-chome, Shibuya-ku, Tokyo 151-8543 (JP).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **MURAKI, Jun.**

(74) **Agent:** **KASHIMA, Hidemi**; 5-4-18, Ozenji, Nishi, Asao-ku, Kawasaki-shi, Kanagawa 215-0017 (JP).

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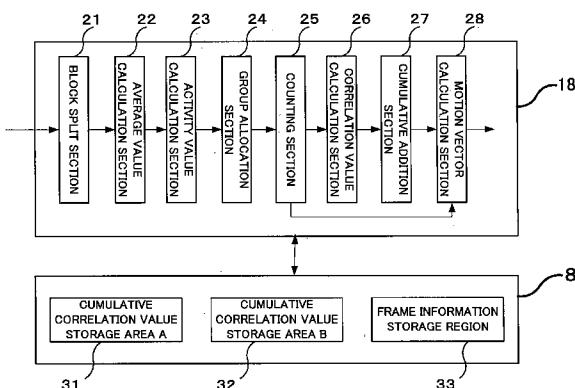
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(54) Title: IMAGING APPARATUS WITH MOTION VECTOR DETECTING FUNCTION



(57) **Abstract:** When frame data is acquired to a digital camera (1), a block split section (21) splits the frame data into a plurality of correlation detection blocks, an average value calculation section (22) calculates an average pixel value of each of the correlation detection blocks, and an activity calculation section (23) calculates a block activity value of each of the correlation detection blocks from the calculated average pixel value of each of the correlation detection blocks. Next, a group allocation section (24) allocates the individual correlation detection blocks to two groups on the basis of the calculated block activity value, and a motion vector for each group is calculated using the representative point matching method. Next, the motion vector calculation section (26, 27, 28) calculates the entire motion vector from the motion vectors for each group in accordance with the ratio between the numbers of calculation detection blocks in each group.

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DESCRIPTION**IMAGING APPARATUS WITH MOTION VECTOR DETECTING FUNCTION**Technical Field

5 The present invention relates to an image apparatus that can be used for a digital camera, a motion detection apparatus and a method of detecting a motion vector.

Background Art

10 Recently, an imaging apparatus, for example, a digital camera equipped with a hand shake correction function to prevent the shake of an object image caused by hand shake has appeared on the market. As an art to detect the hand shake amount required for correcting the hand shake, an art to detect a motion vector of the hand shake using the representative point matching method has been introduced.

15 In the art, for example, a plurality of motion vector detection regions are provided in the image, and the motion vector of each region is detected with the representative point matching method. Next, the detected motion vector in each region is evaluated according to the temporal change of the motion vector and other elements so as to acquire the reliability. Next, the vectors are selected and synthesized in accordance with the reliability, thereby to acquire a motion vector representing the entire image (refer to Japanese Laid-Open (Kokai) Patent Application No. H06-046316 (1994).

However, in the conventional imaging apparatus, in the case where a mobile object exists in such a manner that it covers all vector detection regions in the state where a background (a stationary object) occupies the most of the image, all of 5 the detected motion vectors are significantly affected by the mobile body. Accordingly, a motion vector which significantly differs from the motion vector of the background itself is acquired. As a result, a drawback occurs that a shake (hand shake) of the background which covers most of the image cannot be compensated 10 appropriately.

The present invention is actualized to resolve the foregoing drawback of the conventional art, and an object thereof is to provide an imaging apparatus that can detect an appropriate motion vector in accordance with the status of an object in the image, 15 a motion vector detection apparatus; and a method of detecting a motion vector.

Disclosure of Invention

In accordance with one aspect of the present invention, there is provided a motion vector detection apparatus is configured 20 to include: an acquisition means for acquiring frame data; an activity calculation means for calculating a block activity value on the basis of the frame data acquired by the acquisition means for each correlation detection block; an allocation means for allocating a plurality of correlation detection blocks to a 25 plurality of groups on the basis of the block activity value

of each of the correlation detection blocks calculated by the activity calculation means; a motion vector calculation means for calculating a motion vector for each group to which allocation has been performed by the allocation means; and a detection means for detecting the motion vector of the frame data by calculating a single motion vector on the basis of the motion vectors for each group calculated by the motion vector calculation means in accordance with the ratio between the correlation detection blocks which are allocated to a plurality of respective groups by the allocation means.

In accordance with another aspect of the present invention, there is provided an imaging apparatus comprising: an image pick-up means for picking up an image of an object and outputting image data; an activity calculation means for calculating a block activity value on the basis of the image data outputted from the image pick-up means; an allocation means for allocating a plurality of correlation detection blocks to a plurality of groups on the basis of the block activity value of each of the correlation detection blocks calculated by the activity calculation means; a motion vector calculation means for calculating a motion vector for each group to which allocation has been performed by the allocation means; a detection means for detecting a motion vector of the image data by calculating a single motion vector from the motion vectors for each group calculated by the motion vector calculation means in accordance with the ratio between the correlation detection blocks which are allocated to a plurality

of respective groups by the allocation means; and a camera shake compensation means for executing camera shake compensation processing to the image data on the basis of the motion vector of the image data detected by the detection means.

5 In accordance with another aspect of the present invention, there is provided a method of detecting a motion vector comprising the steps of: an acquisition step for acquiring frame data; an activity calculation step for calculating a block activity value for each correlation detection block from the frame data acquired
10 by the acquisition step; an allocation step for allocating a plurality of correlation detection blocks to a plurality of groups on the basis of the block activity value of each of the correlation detection blocks calculated by the activity calculation step; a motion vector calculation step for calculating a motion vector
15 for each group allocation has been performed by the allocation step; and a detection step for detecting the motion vector of the frame data by calculating a single motion vector from the motion vectors for each group calculated by the motion vector
20 calculation step in accordance with the ratio between the correlation detection blocks which are allocated by the allocation step to a plurality of groups.

25 In accordance with another aspect of the present invention, there is provided a program executed by a processor in an imaging apparatus for executing motion vector detection processing, comprising: an acquisition processing step of acquiring frame data; an activity calculation processing step of calculating

a block activity value for each correlation detection block from the frame data acquired by the acquisition processing step; an allocation processing step of allocating a plurality of correlation detection blocks to a plurality of groups on the 5 basis of the block activity value of each of the correlation detection blocks calculated by the activity calculation processing step; a motion vector calculation processing step of calculating a motion vector for each group to which allocation has been performed by the allocation processing step; and a 10 detection processing step of detecting the motion vector of the frame data by calculating a single motion vector from the motion vectors for each group calculated by the motion vector calculation processing step in accordance with the ratio between the correlation detection blocks which are allocated by the 15 allocation processing step to a plurality of groups.

The above and further novel features of the present invention will more fully appear from the following detailed description when the same is read in conjunction with the accompanying drawings. It is to be expressly understood, however, that the drawings 20 are for the purpose of illustration only and are not intended as a definition of the limits of the invention.

Brief Description of Drawings

FIG. 1 is a block diagram of a digital camera 1 in an embodiment of the present invention;

25 FIG. 2 is a functional block diagram of a CPU 18;

FIG. 3 shows details of each split correlation detection block;

FIG. 4A and FIG. 4B are diagrams for illustrating the outline of the invention;

5 FIG. 5 is a flowchart showing a hand shake correction processing operation of a digital camera according to the embodiment;

10 FIG. 6 is a flowchart showing a motion vector calculation processing operation of the digital camera according to the embodiment;

FIG. 7A, FIG. 7B and FIG. 7C are diagrams for illustrating a method of addition to a correlation value H and a cumulative correlation value S; and

15 FIG. 8 is a diagram for illustrating a cumulative correlation value S.

Best Mode for Carrying Out the Invention

Hereinafter, the best mode for carrying out the present invention will be described with reference to the drawings as an example of application of a motion vector detection apparatus 20 of the present invention to a digital camera.

A. Configuration of the Digital Camera

FIG. 1 is a block diagram showing the general electrical outline configuration of a digital camera 1 which actualizes a motion vector detection apparatus of the present invention.

25 The digital camera 1 includes an image pickup lens 2, a CCD

3, a vertical driver 4, a timing generator (TG) 5, a unit circuit 6, a DMA controller (hereinafter referred to as a DMA) 7, a memory 8, a DMA 9, an image generation section 10, a DMA 11, a DMA 12, a display section 13, a DMA 14, a compression section 15, a DMA 16, a flash memory 17, a CPU 18, and a bus 19.

The image pickup lens 2 projects light of an object to the CCD 3. The CCD 3 is scanned and driven by the vertical driver 4 so as to photo-electrically convert intensity of light of an object projected at every certain time interval and outputs it as an imaging signal (image data) to the unit circuit 6. In this case, since the CCD 3 has color filters of Bayer layout, the projected light of an object is photo-electrically converted to an amount of light of each color represented in the RGB value. The operation timings of the vertical driver 4 and the unit circuit 6 are controlled by the CPU 18 via the TG 5.

The TG 5, which is connected to the unit circuit 6, is configured with a Correlated Double Sampling (CDS) circuit for performing correlated double sampling of imaging signals outputted from the CCD 3 and holding them, an automatic gain control (AGC) circuit for performing automatic gain control of the sampled imaging signals, and an analog to digital (A/D) converter for converting the automatic-gain-controlled analog imaging signals to digital signals. The imaging signals of the CCD 3 go through the unit circuit 6 and are stored in the Bayer data (raw data) state in the memory 8 through control of the DMA 7.

The DMA 9 cuts out and reads out the Bayer data stored in the memory 8 on the basis of a cut-out area determined by the CPU 18, and outputs them to the image generation section 10.

The image generation section 10 performs pixel interpolation processing, gamma (γ) correction, white balance processing or the like, and processing of generating luminosity color difference signals (YUV data) to the image data of Bayer data sent from the DMA 9.

DMA 11 stores in the memory 8 the image data of the luminosity color difference signals generated in the image generation section 10. At this time, the image data of the luminosity color difference signals are stored in a region other than a region in which the above-described Bayer data are stored.

The DMA 12 reads out the image data of the YUV data stored in the memory 8, and outputs them to the display section 13.

The display section 13, including a color LCD and a drive circuit thereof, displays the image data outputted from the DMA 12.

The DMA 14 outputs the image data of the YUV data stored in the memory 8 while a movie is shot and recorded and the image data that has been compressed (the compressed image data) to the compression section 15, and stores the image data compressed by the compression section 15 and the image data that has been expanded to the memory 8.

The compression section 15 is a section for executing compression and expansion (for example, compression and expansion

in the MPEG format) of the image data.

The DMA 16 reads out the compressed image data stored in the memory 8 to record them in the flash memory 17, and stores the compressed image data recorded in the flash memory 17 to 5 the memory 8.

The CPU 18 is configured with a one-chip micro computer with a function of controlling individual sections of the digital camera 1. Particularly, it has a function of calculating a motion vector of frame data, which characterizes the present invention.

10 The memory 8 further records a program and data necessary for controlling the CPU 18, and has a storage region necessary for calculating the motion vector.

B. Function of the CPU 18

15 Next, the function of the CPU 18 which characterizes the present invention will be described.

The CPU 18 calculates a motion vector MV from each of the frame data (image data) outputted from the CCD 3 at every certain time interval by driving the CCD 3.

20 First of all, FIG. 2 shows a functional block diagram of the CPU 18 which characterizes the present invention and the details of the storage region of the memory 8.

25 The CPU 18 has functions such as a block split section 21, an average value calculation section 22, an activity calculation section 23, a group allocation section 24, a counting section 25, a correlation value calculation section 26, a cumulative addition section 27 and a motion vector calculation section 28

and the like.

The memory 8 has a cumulative correlation value storage area A 31, a cumulative correlation value storage area B 32 and a frame information storage region 33.

5 When an image is picked up by the CCD 3 and new Bayer data is stored in the memory 8, the CPU 18 acquires new frame data by reading out the new Bayer data from the memory 8. At this stage of acquiring the new frame data, the CPU 18 erases information stored in the cumulative correlation value storage area A 31 and the cumulative correlation value storage area B 32 of the memory 8, and sets the number of counts of the counting section 10 25 to 0.

When the block split section 21 acquires single frame data (current frame data) sent from the memory 8, it splits the acquired 15 frame data (Bayer data) into a plurality of correlation detection blocks. Since a single correlation detection block has 17×13 pixels, it splits all pixel values outputted from the CCD 3 into 17×13 pixels. FIG. 3 shows the details thereof. Since the address (x, y) of the center pixel of the split correlation 20 detection block is assumed to be (0, 0), the address (x, y) of the pixel A will be (6, 2), and the address (x, y) of the pixel B will be (7, -3).

The average value calculation section 22 calculates an 25 average pixel value of each of the plurality of correlation detection blocks into which the data has been split by the block split section 21. In this case, it calculates the average pixel

value only on the basis of G pixel values in the correlation detection block. That is, it calculates the average value of the G pixel values. In addition, the average value calculation section 22 outputs the calculated average pixel values to the 5 activity calculation section 23, and stores the calculated average pixel value of each of the correlation detection blocks. At this time, it also stores a representative pixel value of each of the correlation detection blocks in the frame information storage region 33. In this case, the pixel value of the center 10 pixel of the correlation detection block is assumed to be the representative pixel value.

Note that, the average value calculation section 22 may calculate the average pixel value only on the basis of the R pixel values or the B pixel values in the correlation detection 15 block, may calculate the average pixel value on the basis of all pixel values (RGB pixel values), may calculate the average pixel value on the basis of pixels at predetermined intervals, or may calculate the final average pixel value through combined use of various average pixel values. The point is, any value 20 may be used so long as it reflects the degree of change of the position in the temporal axis. In addition, only the average pixel value of each of the correlation detection blocks may be stored in the frame information storage region 33, without the 25 representative pixel value of each of the correlation detection blocks being stored. In this case, the average pixel value plays a role of the representative pixel value of the correlation

detection block. Alternatively, a pixel addition value determined by adding predetermined pixel values in the correlation detection block may be used in place of the pixel average value.

5 The activity calculation section 23 calculates a block activity value of each of the correlation detection blocks on the basis of the average pixel value of each of the correlation detection blocks calculated by the average value calculation section 22. This calculation is made by performing subtraction
10 between the average pixel value of each of the correlation detection blocks in the last frame before the current frame (the previous frame) and the average pixel value of each of the correlation detection blocks of the current frame, with the both correlation detection blocks being at the same position, and
15 determining the absolute value (or squaring) of the difference. Accordingly, the absolute value of the difference will be the block activity value. In addition, since the average pixel value of each of the correlation detection blocks of the previous frame has been stored in the frame information storage region 33, the
20 activity calculation section 23 is capable of calculating the block activity value of each of the correlation detection blocks by reading out the average pixel value of each of the correlation detection blocks of the previous frame from the frame information storage region 33.

25 When the block activity value of each of the correlation detection blocks is calculated, the group allocation section

24 determines whether or not the calculated block activity value is lower than the threshold. It allocates the correlation detection blocks whose block activity value is lower than the threshold to Group A, and the correlation detection blocks whose 5 block activity value is not lower than the threshold to Group B.

The counting section 25 counts the number of correlation detection blocks which are allocated to Group A, and the number of correlation detection blocks which are allocated to Group 10 B, respectively.

The correlation value calculation section 26, the cumulative addition section 27, and the motion vector calculation section 28 calculate, mainly using the representative point matching method, the motion vector of Group A and the motion vector of 15 Group B on the basis of the correlation detection blocks which are allocated to Group A and the correlation detection blocks which are allocated to Group B, respectively. Further, the motion vector calculation section 28 calculates the motion vector of the entire frame on the basis of the calculated motion vector 20 of Group A and the motion vector of Group B.

Specifically, the correlation value calculation section 26 calculates the correlation values H of the pixels from the G pixel values in each of the correlation detection blocks allocated to Group A and Groups B, respectively. The calculation of the 25 correlation values H of the pixels is made by performing subtraction between each of the G pixel values in each of the

correlation detection blocks of the current frame and the representative pixel value in each of the correlation detection blocks of the previous frame, with the both correlation detection block being at the same position, and squaring the difference.

5 Accordingly, the square of the difference will be the correlation value H of the pixels. In addition, since the representative pixel value of each of the correlation detection blocks of the previous frame has been stored in the frame information storage region 33, the correlation value calculation section 26 is capable
10 of calculating the correlation values H of the pixels in each of the correlation detection blocks by reading out the representative pixel value in each of the correlation detection blocks of the previous frame from the frame information storage region 33. Note that, in place of the square of the difference,
15 the absolute value of the difference may be determined.

The cumulative addition section 27 adds the correlation values H of the pixels in each of the correlation detection blocks which are allocated to Group A, with the pixels being at the same position, and stores the cumulative correlation value S determined by addition in the cumulative correlation value storage area A 31. In addition, it adds the correlation values H of the pixels in each of the correlation detection blocks which is allocated to Group B, with the pixels being at the same position, and stores the cumulative correlation value S determined by addition in the cumulative correlation value storage area B 32.
20
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The motion vector calculation section 28 detects a motion

vector MVa of Group A on the basis of the cumulative correlation value S which is the smallest of the cumulative correlation values S which have been stored in the cumulative correlation value storage area A 31. Similarly, it also detects a motion vector 5 MVb of Group B on the basis of the cumulative correlation value S which is the smallest of the cumulative correlation values S which have been stored in the cumulative correlation value storage area B 32.

Next, the motion vector calculation section 28 detects the 10 motion vector of the current frame on the basis of the detected motion vector of Group A and Group B (MVa, MVb). The detection of the motion vector of the current frame is made from the motion vector of Group A and Group B (MVa, MVb) in accordance with the ratio between the number of calculation detection blocks which 15 are allocated to Group A and the number of correlation detection blocks which are allocated to Group B.

C. Outline of the Present Invention

The outline of the present invention will be described before 20 describing the operation of the digital camera 1 according to the present embodiment.

The present invention calculates a motion vector of a portion in which the image has not significantly changed, such as a portion in which only a hand shake exists (for example, the background), and a motion vector of a portion, in which the image has 25 significantly changed, such as a portion in which the object significantly moves, respectively. Next, the present invention

calculates the motion vector of the frame in accordance with the areas which are covered by the portion in which the image has not significantly changed, and the portion in which the image has significantly changed in the frame, respectively.

5 FIG. 4A and FIG. 4B are diagrams showing the details of the correlation detection blocks which are allocated by the group allocation section 24. It is apparent that although the image of the previous frame and the image of the current frame include a mountain, a house and a person, the position of the person 10 differs between the previous frame and the current frame. That is, it is apparent that the movement of the object (the person) 15 is much larger than that of the hand shake.

Accordingly, the correlation detection blocks are allocated to Groups A and B using the block activity value and a threshold 20 of the correlation detection block, whereby the correlation detection blocks are allocated to portions in which only a hand shake exists and portions in which the object moves. That is, in the case of the correlation detection block in which the object moves, the correlation with the correlation detection block of the previous frame (correlation between the frames) decreases, that is, the block activity value increases (i.e., the difference 25 in the pixel average values increases). The magnitude of the block activity value is judged using the threshold.

Referring to FIG 4B, the correlation detection blocks filled in white of the current frame represent the correlation detection blocks which are allocated to Group A, and the correlation

5 detection blocks filled in black represent the correlation detection blocks which are allocated to Group B. That is, the correlation detection blocks filled in white represent the correlation detection blocks whose block activity value is smaller than the threshold, and the correlation detection blocks filled in black represent the correlation detection blocks whose block activity value is not smaller than the threshold.

10 Next, the motion vectors for Group A and Group B are calculated, respectively, and the entire motion vector is calculated in accordance with the ratio between the number of correlation detection blocks of Group A and the number of correlation detection blocks of Group B. This enables calculating the motion vector of the entire image in accordance with the ratio between the area of the hand shake and the area of the object with movement 15 in the image.

20 For example, as shown in FIG 4B, in the case where the portion with the hand shake (the background) occupies a larger area than the portion with the object with movement (the person) in the image, the motion vector focusing on the motion vector of the background is selected, as the motion vector of the frame is calculated, from the calculated motion vector of the background (the motion vector of Group A) and the motion vector of the object (the motion vector of Group B).

25 On the contrary, in the case where the portion with the object with movement (the person) occupies a larger area than the portion with the hand shake (background) in the image, the motion vector

focusing on the motion vector of the object is selected, as the motion vector of the frame is calculated, from the calculated motion vector of the background (the motion vector of Group A) and the motion vector of the object (the motion vector of Group B).

That is, the motion vector of the frame is calculated, focusing on the motion vector which occupies a larger area in the image of the frame.

D. Operation

Next, a hand shake compensation processing operation of the digital camera 1 according to the present embodiment will be described referring to a flowchart of FIG. 5.

First, when, a new image is picked up by the CCD 3 and new Bayer data is stored in the memory 8, for example, during the operation of movie shooting and recording, the CPU 18 acquires the new frame data (the i^{th} frame data) by reading out new Bayer data from the memory 8 (Step S1). The frame data may be sequentially acquired for each of the correlation detection blocks, while setting the timing at which the processing of reading out the new Bayer data from the memory 8 to the timing at which storage for one correlation detection block to the memory 8 is complete. In this case, however, for simplifying the description, the frame data for a single image is to be acquired, while setting the timing at which the processing of reading out the new Bayer data from the memory 8 to the timing at which storage for one image to the memory 8 is complete.

Next, the CPU 18 executes the motion vector calculation processing of the new frame data acquired in Step S1 (Step S2). The details will be described later.

When the motion vector calculation processing in Step S2 is complete, the CPU 18 determines a cut-out area for the i^{th} frame on the basis of the motion vector for the i^{th} frame calculated in Step S2 and the cut-out area used for trimming the $(i-1)^{\text{th}}$ frame (Step S3).

Subsequently, the CPU 18 controls the DMA 9 so as to cut out and read out the image data (Bayer data) for the i^{th} frame stored in the memory 8 according to the cut-out area for the i^{th} frame determined in Step S3. Further, the CPU 18 has the image generation section 10 output the trimmed image data (Step S4).

Subsequently, the CPU 18 controls the DMA 12 so as to read out the YUV data generated by the image generation section 10 and stored in the memory 8 (Step S5).

Further, the CPU 18 controls the DMAs 14, 16, and the compression section 15 so as to compress the YUV data stored in the memory 8 and record them in the flash memory 17 (Step S6).

The processing of Steps S1 to S6 as described above is repeatedly executed each time a new image is picked up by the CCD 3 and new Bayer data is stored in the memory 8.

Next, the details of the motion vector calculation processing operation of the digital camera 1 according to the present

embodiment (Step S2 in FIG. 5) will be described referring to the flowchart of FIG. 6.

First, when new frame data is acquired in Step S1 of FIG. 5, the CPU 18 sets J to 1 (J: the number of correlation detection blocks), Na to 0, and Nb to 0, and clears (erases) the information stored in the cumulative correlation value storage area A 31 and the cumulative correlation value storage area B 32 (Step S11).

Here, Na represents a count value of the correlation detection block (the correlation detection block allocated to Group A) whose block activity value is judged to be lower than the threshold, and Nb represents a count value of the correlation detection block (the correlation detection block allocated to Group B) whose block activity value is judged to be higher than the threshold.

Further, the block split section 21 splits the acquired frame (the i^{th} frame in this case) into a plurality of correlation detection blocks (Step S12). That is, since a single correlation detection block has 17×13 pixels, the block split section 21 splits all pixel values outputted from the CCD 3 into blocks each having 17×13 pixels.

Next, the CPU 18 starts calculation of the correlation value H of the J^{th} (in this case, the first, since $J=1$) correlation detection block (Step S13).

Next, the average value calculation section 22 calculates the average value of the pixel values in the J^{th} correlation

detection block (the average pixel value) (Step S14). Here, the average value of the G pixel values in the Jth correlation detection block is calculated.

Next, the average value calculation section 22 stores the calculated average pixel value of the Jth correlation detection block in the frame information storage region 33 of the memory 8, and also stores the representative pixel value of the Jth correlation detection block in the frame information storage region 33 (Step S15). The representative pixel value refers to a pixel value of the center pixel of the Jth correlation detection block, that is, the pixel value of the pixel the address (x, y) of which will be (0, 0). Note that, the average pixel value of the correlation detection block may be used as the representative pixel value.

The stored representative pixel value of the Jth correlation detection block and the block average value of the Jth correlation detection block are used when calculating the motion vector of the following frame (the (i+1)th frame), that is, in Step S6 and Step S9.

Next, the activity calculation section 23 calculates the Jth correlation detection block activity value (Step S16). The calculation is made as follows: reading out the average value of the G pixel values in the Jth correlation detection block of the previous frame (the (i-1)th frame) is stored in the frame information storage region, and determines the absolute value of the difference between the average value of the Jth correlation

detection block of the $(i-1)^{th}$ frame that has been read out and the average value of the J^{th} correlation detection block of the i^{th} frame calculated in Step S4. The absolute value of the difference is set as the block activity value.

5 Next, the group allocation section 24 judges whether or not the calculated block activity value is lower than the threshold (Step S17). That is, it judges whether to allocate the blocks to Group A or Group B in accordance with the calculated block activity value and the threshold.

10 In Step S17, when the calculated block activity value is judged to be lower than the threshold, the group allocation section 24 allocates the correlation detection blocks to Group A, and the counting section 25 sets the count of Na to +1 (Step S18).

15 Next, the correlation value calculation section 26 respectively calculates the correlation values H of the pixels (the square of the difference) from each of the G pixel values in the J^{th} correlation detection block and the representative pixel value of the J^{th} correlation detection block of the previous frame. The cumulative addition section 27 adds the calculated correlation value H of each of the pixels to each of the cumulative correlation value S stored in the cumulative correlation value storage area A 31 (Step S19) and stores the value, and the routine advances to Step S22. Note that, in the case where the cumulative correlation value S has not been stored in the cumulative correlation value storage area A 31, each of the calculated correlation values H of the pixels is stored as it is, and the

thus stored correlation value H becomes the cumulative correlation value S.

The calculation of the correlation value H will be described specifically as follows: the correlation value H of the pixels 5 is determined by subtracting the representative pixel value of the J^{th} correlation detection block of the $(i-1)^{th}$ frame stored in the frame information storage region 33 from each of the G pixel values of the J^{th} correlation detection block of the i^{th} frame, and by squaring each of the values determined by the 10 subtraction. For example, when it is assumed that there are five G pixels in the J^{th} correlation detection block, the number of the correlation values H of the pixels to be determined will be 5. This is because the representative pixel value of the J^{th} correlation detection block of the previous frame is subtracted 15 from the five G pixel values in the J^{th} correlation detection block of the acquired frame in Step S1 of FIG. 5, and each of the values determined by the subtraction is squared.

FIG. 7A indicates each of the G pixel values of the J^{th} detection block, and FIG. 7B shows the details of the correlation value 20 H of each of the pixels calculated from the J^{th} detection block. At this time, the address of the pixel on which the calculation 25 is made is used as the address of the calculated correlation value H of each of the pixels. That is, the address (x, y) of the correlation value H of the pixel calculated on the basis of the pixel A will be (6, 2), and the address (x, y) of the correlation value H of the pixel calculated on the basis of the

pixel B will be (7, -3).

Next, a specific description will be given on the addition of each of the calculated correlation values H on each of the cumulative correlation values S which have been stored in the 5 cumulative correlation value storage area A 31.

First of all, the cumulative correlation value S which has been stored in the cumulative correlation value storage area A 31 refers to a value determined by adding the calculated correlation values H of each of the pixels of each of the 10 correlation detection blocks, while the pixels are at the same position.

That is, the cumulative correlation value S is a value determined by selecting the correlation values H of the pixels whose address are the same out of the correlation values H of each of the pixels calculated in each of the correlation detection 15 blocks as shown in FIG. 8. The same number of the cumulative correlation values S exists as those of the correlation values H and the number of the G pixels in the correlation detection block.

20 At this time, the address of the correlation value H of the pixel which is determined by the addition is used for each cumulative correlation value S.

For example, the address (x, y) of the cumulative correlation 25 value S determined by adding the correlation values H of the pixels with the address (x, y) being (6, 2) will be (6, 2), and the address (x, y) of the cumulative correlation value S determined

by adding the correlation values H of the pixels with the address (x, y) being (7, -3) will be (7, -3).

FIG. 7C shows the details of the cumulative correlation value S, and the newly calculated correlation value H of the pixels is added to the cumulative correlation value S. At this time, the correlation value H of the pixels with the same address as that of the cumulative correlation value S is added to the cumulative correlation value S as shown in FIG. 7B.

On the other hand, when it is judged that the calculated block activity value is not smaller than the threshold in Step 10 S17, the group allocation section 24 allocates the correlation detection blocks to Group B, and the counting section 25 sets the count of Nb to +1 (Step S20).

Next, as is the case with Step S19, the correlation value calculation section 26 calculates the correlation values H of the pixels (the square of the difference) from each of the G pixel values in the Jth correlation detection block and the representative pixel value of the Jth correlation detection block of the previous frame, respectively. The cumulative addition section 27 adds each of the calculated correlation values H of the pixels to each of the cumulative correlation values S stored in the cumulative correlation value storage area B 32 and stores it (Step S21), and the routine advances to Step S22. Note that, in the case where the cumulative correlation value S has not been stored in the cumulative correlation value storage area B 32, the calculated correlation value H of each of the pixels

is stored as it is, and thus the stored correlation value H of the pixel will be the cumulative correlation value S.

When the routine advances to Step S22, the CPU 18 judges whether or not the correlation value H has been calculated from 5 all correlation detection blocks for the i^{th} frame (Step S22).

In Step S22, when it is judged that there is a correlation detection block of which the correlation value H of the pixel has not been calculated, J is set to J=1, and the routine returns to Step S13. This enables determining the correlation value H 10 of the pixels of the correlation detection block of the next number.

On the other hand, in Step S22, when it is judged that there is no correlation detection block of which the correlation value H has not been calculated, the motion vector calculation section 15 28 detects the position, that is, the address of the cumulative correlation value S which is the smallest of the cumulative correlation values S which have been stored in the cumulative correlation value storage area A 31 (Step S24). The detected address will be the motion vector MVa.

Next, the motion vector calculation section 28 detects the 20 position, that is, the address of the cumulative correlation value S which is the smallest of the cumulative correlation values S which have been stored in the cumulative correlation value storage area B 32 (Step S25). The detected address will be the 25 motion vector MVb.

Next, the CPU 18 detects the motion vector MV of the i^{th} frame

(Step S26). The detection of the motion vector of the i^{th} frame is made in such a manner that the motion vector MV of the current frame is detected from the motion vector of Group A and Group B (MV_A, MV_B) in accordance with the ratio between the numbers of correlation detection blocks which are allocated to Group A and the number of correlation detection blocks which are allocated to Group B. Specifically, the detection is made according to the following equation.

(Equation 1)

$$MV = (Na \times MV_A + Nb \times MV_B) / (Na + Nb)$$

Na: the number of correlation detection blocks which are allocated to Group A

Nb: the number of correlation detection blocks which are allocated to Group B

Note that, when the CPU 18 detects the motion vector MV of the i^{th} frame in Step S26, the routine moves to the processing of Step S3 of FIG. 5, and the CPU 18 executes the processing thereafter.

D. Effects

As described above, in the present embodiment, the block activity value of each of the split correlation detection blocks is calculated, each of the correlation detection blocks is allocated to either of two groups on the basis of the calculated block activity value and the threshold, the motion vector of each group is calculated for each allocated group using the representative point matching method, and the motion vector MV

of the current frame is detected from the motion vector of Group A and Group B in accordance with the number of blocks for each group. Therefore, it is possible to calculate an appropriate motion vector with simplified processing in accordance with the 5 ratio between the area of the portion in which the image has not significantly changed (for example, the hand shake portion) and the portion in which the image has changed significantly (for example, the portion with the object with movement) of the image. For example, in the case where the hand shake portion 10 (for example, the background portion) occupies the most area of the image, the motion vector focusing on the motion vector of the hand shake portion is calculated. In the case where the portion with the object with movement occupies the image, the motion vector focusing on the motion vector of the portion of 15 the object with movement is calculated.

In addition, since the motion vector for each group is calculated by the representative point matching method using the Bayer data (CCD data, raw data), the interpolation processing is eliminated and the processing time is reduced. In addition, 20 only the G pixel values are used to calculate the motion vector for each group with the representative point matching method. Accordingly, the number of pixels is halved and thus the number of computations is also halved, thereby further reducing the processing time.

25 E. Modifications

Although all G pixel values are used for calculating the

average pixel values and the correlation values H of the pixels in the above-described embodiment, thinned G pixel values may be used.

5 In addition, although the motion vector MV is detected for each frame in the above-described embodiment, the motion vector MV may be detected for each field.

10 In addition, although the average pixel value is calculated from each of the correlation detection blocks of the frame data in the above-described embodiment, the resultant data using the photometry circuit for AE may be used. That is, any value may be used so long as it reflects the change in the temporal axis at the position. In this case, the AE detection block must correspond to the correlation detection block.

15 In addition, although the group allocation section 24 allocates each of the correlation detection blocks to two groups in the above-described embodiment, it may allocate them to three groups or four groups. In this case, a plurality of thresholds are provided in accordance with the number of groups to which the blocks are to be allocated, the cumulative correlation value 20 storage area corresponding to the number of groups is provided, the motion vector is provided for each group, and a single motion vector MV is calculated from the motion vector for each group in accordance with the ratio between the numbers of calculation detection blocks for each group.

25 In addition, although the block activity value is calculated merely as the average value of the G pixel values in the

above-described embodiment, the average value may be calculated by performing weighting, and the value may be the block activity value. For example, when it is assumed that there are three pixel values, G1, G2 and G3, although the normal average value shall be $(G1 + G2 + G3) / 3$, it will become $(G1 + G2 \times 2 + G3) / 3$ in the case where the average value is calculated with weighing.

5 In addition, although the motion vector is detected using the entire image in the above-described embodiment, the motion vector may be detected using the image in some area (for example, 10 the focus area).

In addition, although the motion vector of the frame is detected on the basis of the Bayer data in the above-described embodiment, the motion vector of the frame may be detected on 15 the basis of the image data of luminosity color difference signal generated by the image generation section 10.

Further, although the motion vector of the frame is detected on the basis of the Bayer data in the above-described embodiment, the motion vector of the frame may be detected on the basis of raw data other than the Bayer data. The point is, any raw data 20 which comply with the color filter array provided on the image pickup elements, are acceptable.

In addition, although the description has been given on the case where the camera shake compensation processing is executed on movie data which is shot and recorded during the movie shooting 25 and recording in the above-described embodiment, the camera shake compensation processing may be executed on the movie data which

is displayed on the display section 13 while a through image which does not involve recording operation of the image data (finder image) may be executed. Alternatively, the camera shake compensation processing may be executed to the movie data replayed and displayed in the display section 13 by reading out the movie which is picked up by the CCD 3 and recorded in the flash memory 17.

In addition, although the hand shake compensation processing (FIG. 5) and the motion calculation processing of the frame (FIG. 10 6) is executed by the CPU 18 through software in the above-described embodiment, it may be actualized through hardware.

Further, although the description has been made in the case where the motion vector detection apparatus according to the present invention is applied to a digital camera in the 15 above-described embodiment, the present invention is not limited to this. It may be applied to a cell phone with camera, a PDA with camera, a PC with camera, an IC recorder with camera, a digital camera or the like. Further, it may be applied to a cell phone, a PDA, a PC, a television receiver, an image receiver 20 or the like without camera. The point is that any equipment which is capable of detecting the motion vector from the frame data is acceptable.

Furthermore, although the program of the digital camera which is an embodiment of the present invention is stored in the memory 25 (for example, ROM, etc.) of the digital camera, this imaging program is stored on a computer-readable medium and should also

be protected in the case of manufacturing, selling, etc. of only the program. In that case, the method of protecting the program with a patent will be realized by the form of the computer-readable medium on which the imaging program product is stored.

5 While the present invention has been described with reference to the preferred embodiments, it is intended that the invention be not limited by any of the details of the description therein but includes all the embodiments which fall within the scope of the appended claims.

CLAIMS

1. A motion vector detection apparatus comprising:

an acquisition means (21) for acquiring frame data;

an activity calculation means (23) for calculating a block activity value on the basis of the frame data acquired by the acquisition means (21) for each correlation detection blocks;

an allocation means (24) for allocating a plurality of correlation detection blocks to a plurality of groups on the basis of the block activity value of each of the correlation detection blocks calculated by the activity calculation means (23);

a motion vector calculation means (26, 27, 28) for calculating a motion vector for each group to which allocation has been performed by the allocation means (24); and

a detection means (28) for detecting the motion vector of the frame data by calculating a single motion vector on the basis of the motion vectors for each group calculated by the motion vector calculation means (26, 27, 28) in accordance with the ratio between the correlation detection blocks which are allocated to a plurality of respective groups by the allocation means (24).

2. The motion vector detection apparatus according to claim 1, wherein the activity calculation means (23) calculates the block activity value of the correlation detection block by determining a difference between a value on the basis of pixel

5 values in the correlation detection block of the current frame data and a value on the basis of pixel values in the correlation detection block of the previous frame data at the same position which corresponds to the correlation detection block of the current frame data.

3. The motion vector detection apparatus according to claim 2, wherein the activity calculation means (23) calculates the block activity value of the correlation detection block by determining a difference between an average value of the pixel 5 values in the correlation detection block of the current frame data and an average value of the pixel values in the correlation detection block of the previous frame data at the same position.

4. The motion vector detection apparatus according to claim 2, wherein the activity calculation means (23) calculates the block activity value of the correlation detection block by determining a difference between the value on the basis of pixel 5 values of some pixels in the correlation detection block of the current frame data and a value on the basis of pixel values of some pixels in the correlation detection block of the previous frame data at the same position.

5. The motion vector detection apparatus according to claim 2, wherein the activity calculation means (23) calculates the block activity value of the correlation detection block by

5 determining a difference between the value on the basis of the pixel values in the correlation detection block of current raw data and the value on the basis of the pixel values in the correlation detection block of previous raw data.

6. The motion vector detection apparatus according to claim 5, wherein the raw data is defined as Bayer data.

7. The motion vector detection apparatus according to claim 6, wherein the activity calculation means (23) calculates the block activity value of the correlation detection block using G pixel values in the correlation detection block.

8. The motion vector detection apparatus according to claim 1, wherein the allocation means (24) includes a judgment means (24) for judging whether or not the block activity value of the correlation detection block calculated by the activity calculation means (23) is lower than a threshold, and
5 the allocation means (24) allocates the correlation detection blocks whose block activity value is judged to be lower than the threshold by the judgment means (24) to a first group, and allocates the correlation detection blocks whose block activity value is judged to be higher than the threshold by the judgment means (24) to a second group.
10

9. The motion vector detection apparatus according to claim

1, wherein the motion vector calculation means (26, 27, 28) includes a correlation calculation means (26) for calculating a correlation value of pixels in the correlation detection block by determining a difference between pixel values in the correlation detection block of the current frame data and a representative pixel value of the correlation detection block of the previous frame data at the same position, and

5 calculates the motion vector for each group on the basis of the correlation value of the pixels in each of the correlation detection blocks calculated by the correlation calculation means (26).

10

10. The motion vector detection apparatus according to claim 9, wherein the correlation calculation means (26) calculates the correlation value of the pixels in each of the correlation detection blocks by determining a difference between some pixel values in the correlation detection block of the current frame data and the representative pixel value of the correlation detection block of the previous frame data at the same position.

5

11. The motion vector detection apparatus according to claim 9, wherein the correlation calculation means (26) calculates the correlation value of the pixels in the correlation detection block by determining a difference between the pixel values in the correlation detection block of the current raw data and the representative pixel value in the correlation detection block

5

of the previous raw data at the same position.

12. The motion vector detection apparatus according to claim 11, wherein the raw data is defined as Bayer data.

13. The motion vector detection apparatus according to claim 12, wherein the correlation calculation means (26) calculates the correlation value of the pixels in each of the correlation detection blocks by determining a difference between G pixel values in the correlation detection block of the current Bayer data and a representative G pixel value in the correlation detection block of the previous Bayer data at the same position.

14. The motion vector detection apparatus according to claim 9, wherein the motion vector calculation means (26, 27, 28) comprises a cumulative addition means (27) for adding the correlation values of the pixels of which pixel positions are the same in each of the correlation detection blocks calculated by the correlation calculation means (26), and calculates the motion vector for each group on the basis of the value for each group added by the cumulative addition means (27).

15. The motion vector detection apparatus according to claim 14, wherein the motion vector calculation means (26, 27, 28) calculates the motion vector on the basis of the pixel position of the smallest value out of the values that are determined by

5 addition of the cumulative addition means (27).

16. The motion vector calculation apparatus according to claim 1, wherein the detection means (28) detects the motion vector of the frame data by synthesizing the motion vector for each group calculated by the motion vector calculation means (26, 5 27, 28) in accordance with the ratio between the numbers of correlation detection blocks that are allocated by the allocation means (24) to a plurality of groups.

17. An imaging apparatus comprising:

an image pick-up means (2,3) for picking up an image of an object and outputting image data;

5 an activity calculation means (23) for calculating a block activity value on the basis of the image data outputted from the image pick-up means (2,3);

10 an allocation means (24) for allocating a plurality of correlation detection blocks to a plurality of groups on the basis of the block activity value of each of the correlation detection blocks calculated by the activity calculation means (23);

a motion vector calculation means (26, 27, 28) for calculating a motion vector for each group to which allocation has been performed by the allocation means (24);

15 a detection means (28) for detecting a motion vector of the image data by calculating a single motion vector on the basis

of the motion vectors for each group calculated by the motion vector calculation means (26, 27, 28) in accordance with the ratio between the correlation detection blocks which are 20 allocated to a plurality of respective groups by the allocation means (24); and

a hand shake correction means (18) for executing hand shake correction processing to the image data on the basis of the motion vector of the image data detected by the detection means (28).

18. The imaging apparatus according to claim 17, further comprising a record means (17) for recording the image data corrected by the hand shake correction means (18).

19. The imaging apparatus according to claim 17, further comprising a display means (13) for displaying the image data corrected by the hand shake correction means (18).

20. The imaging apparatus according to claim 17, further comprising:

a record means (17) for recording the image data outputted from the image pick-up means (2,3); and

5 an output means (16) for reading out and outputting the image data recorded in the record means (17),

wherein the activity calculation means (23), the allocation means (24), the motion vector calculation means (26, 27, 28), the detection means (28), and the hand shake correction means

10 (18) execute processing to the image data outputted from the output means (16).

21. The method of detecting a motion vector comprising the steps of:

an acquisition step (21) for acquiring frame data;
an activity calculation step (23) for calculating a block activity value for each correlation detection block on the basis of the frame data acquired by the acquisition step (21);

5 an allocation step (24) for allocating a plurality of correlation detection blocks to a plurality of groups on the basis of the block activity value of each of the correlation detection blocks calculated by the activity calculation step (23);

10 a motion vector calculation step (26, 27, 28) for calculating a motion vector for each group allocation having been performed by the allocation step (24); and

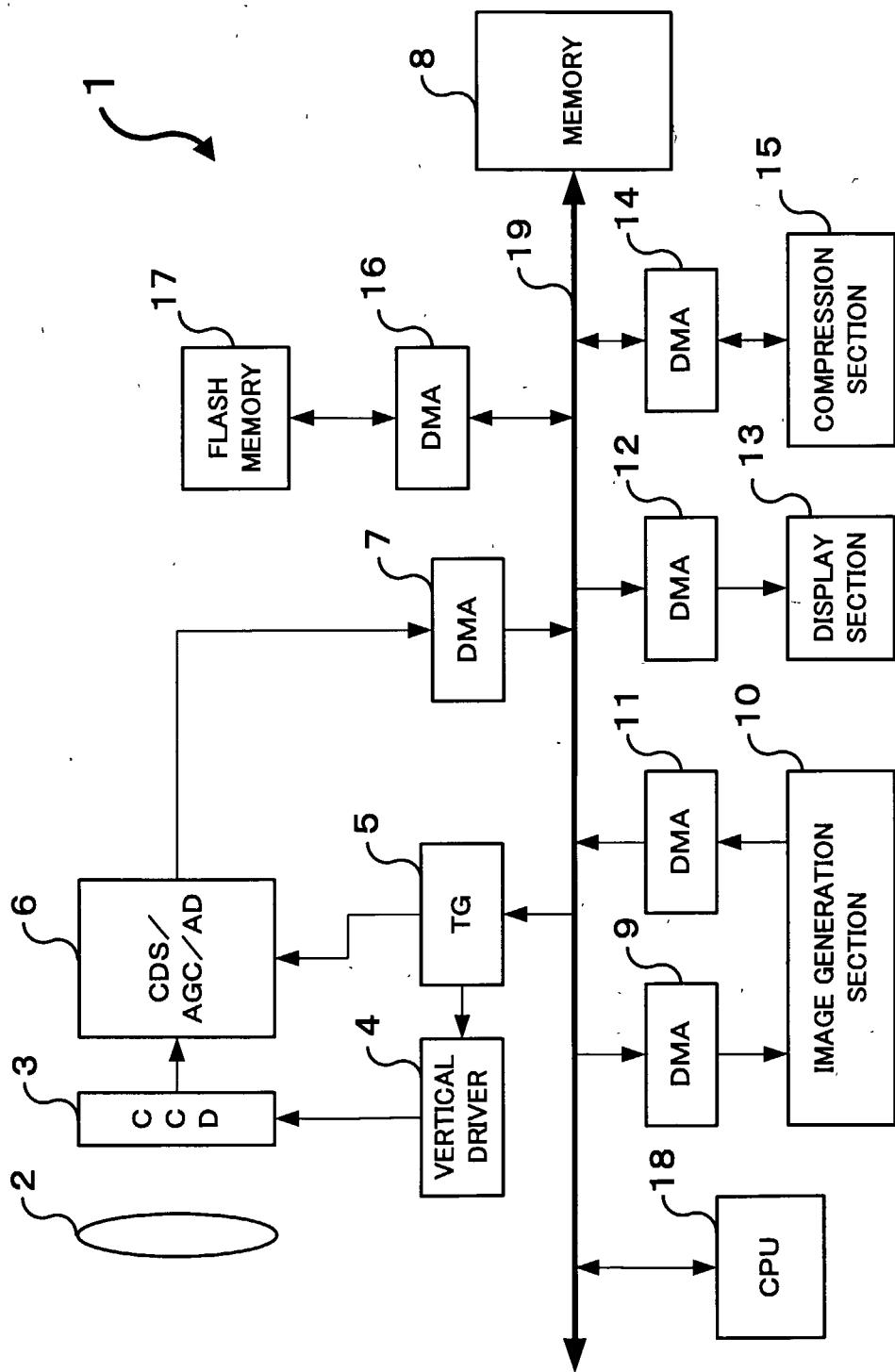
15 a detection step (28) for detecting the motion vector of the frame data by calculating a single motion vector on the basis of the motion vectors for each group calculated by the motion vector calculation step (26, 27, 28) in accordance with the ratio between the correlation detection blocks which are allocated by the allocation step (24) to a plurality of groups.

22. A program executed by a processor in an imaging apparatus for executing motion vector detection processing, comprising:

an acquisition processing step (21) of acquiring frame data;
an activity calculation processing step (23) of calculating
5 a block activity value for each correlation detection block on
the basis of the frame data acquired by the acquisition processing
step (21);
an allocation processing step (24) of allocating a plurality
of correlation detection blocks to a plurality of groups on the
10 basis of the block activity value of each of the correlation
detection blocks calculated by the activity calculation
processing step (23);
a motion vector calculation processing step (26, 27, 28)
of calculating a motion vector for each group to which allocation
15 has been performed by the allocation processing step (24); and
a detection processing step (28) of detecting the motion
vector of the frame data by calculating a single motion vector
on the basis of the motion vectors for each group calculated
by the motion vector calculation processing step (26, 27, 28)
20 in accordance with the ratio between the correlation detection
blocks which are allocated by the allocation processing step
to a plurality of groups.

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FIG. 1



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FIG. 2

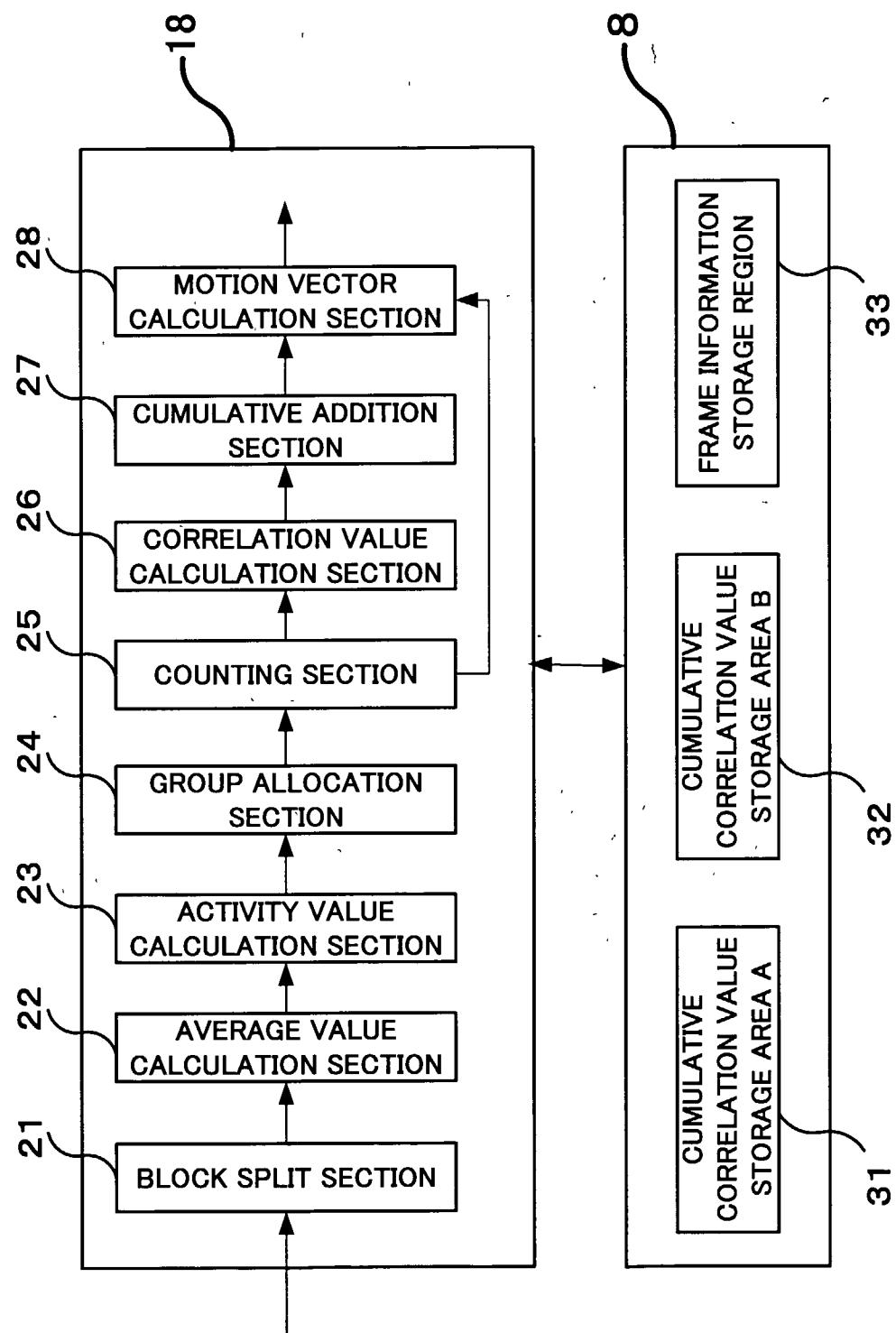
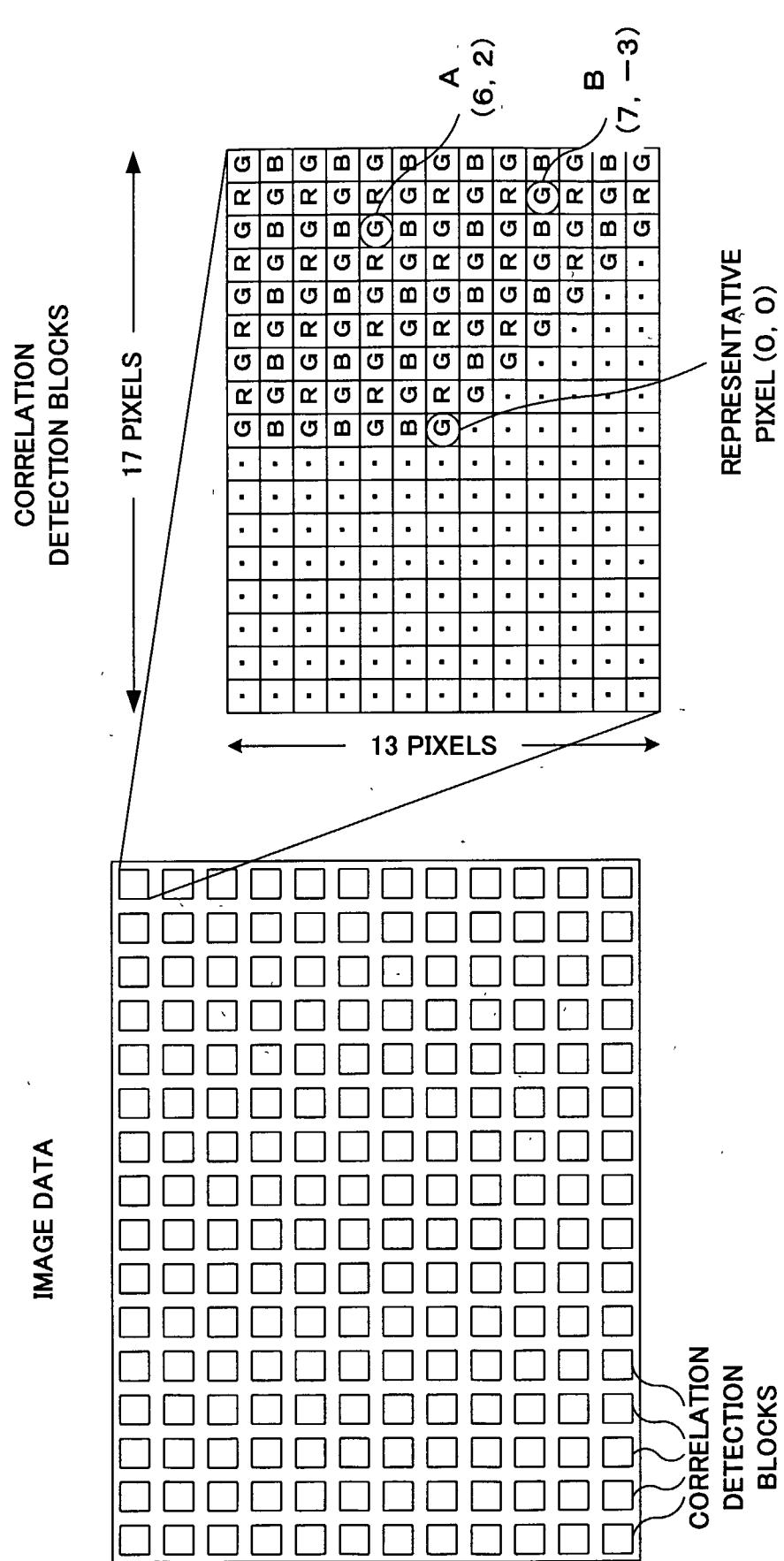
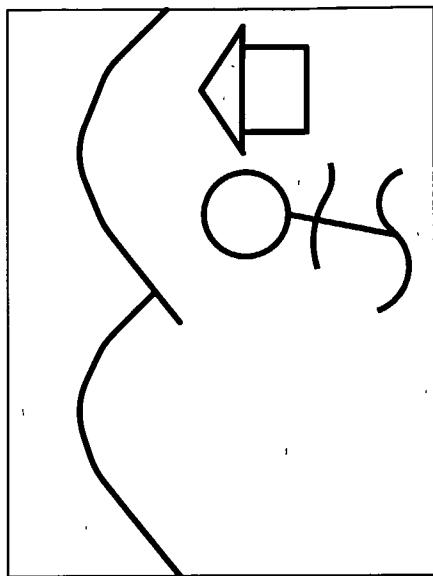


FIG. 3



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CURRENT FRAME



PREVIOUS FRAME

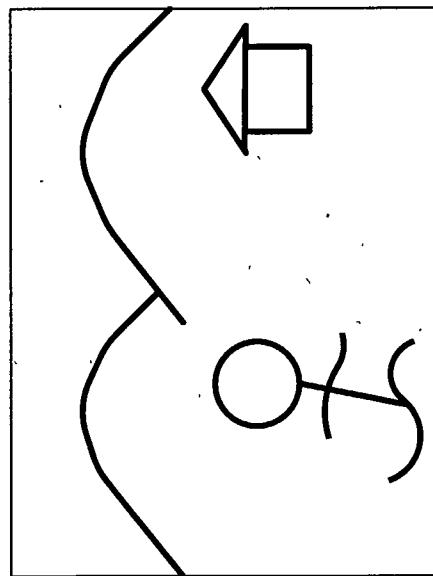


FIG. 4A

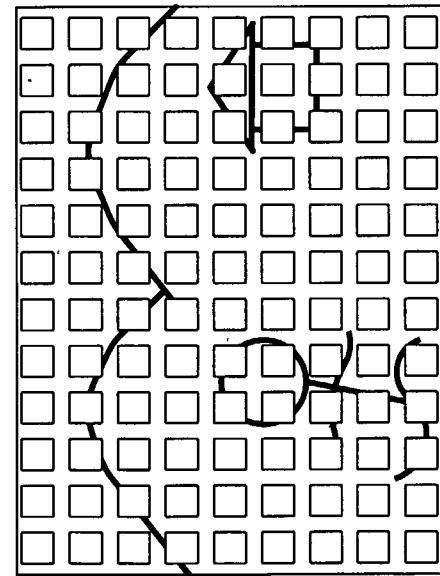
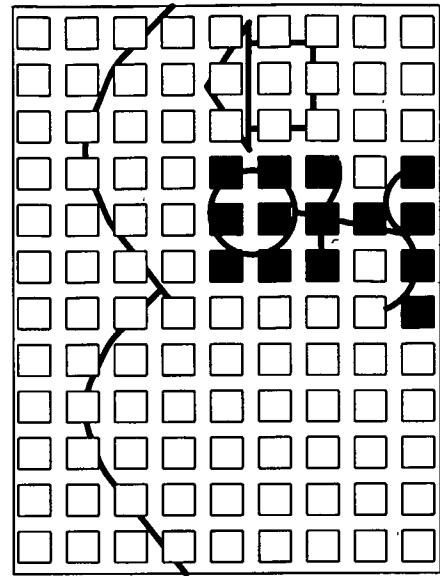
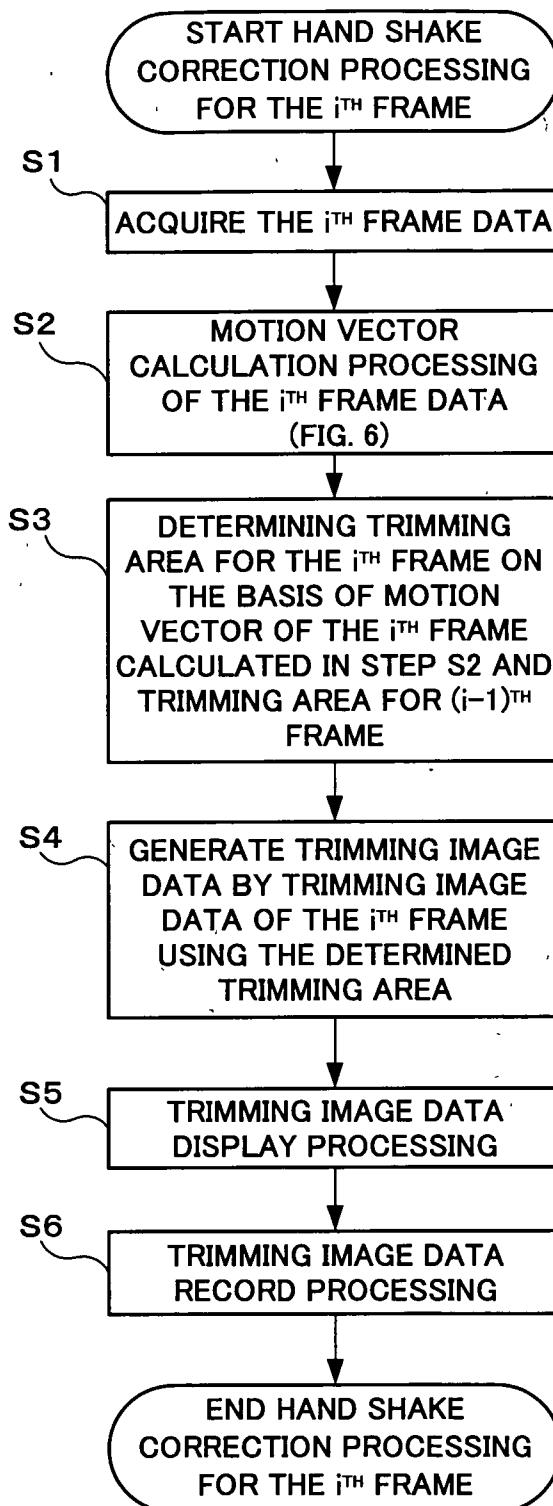


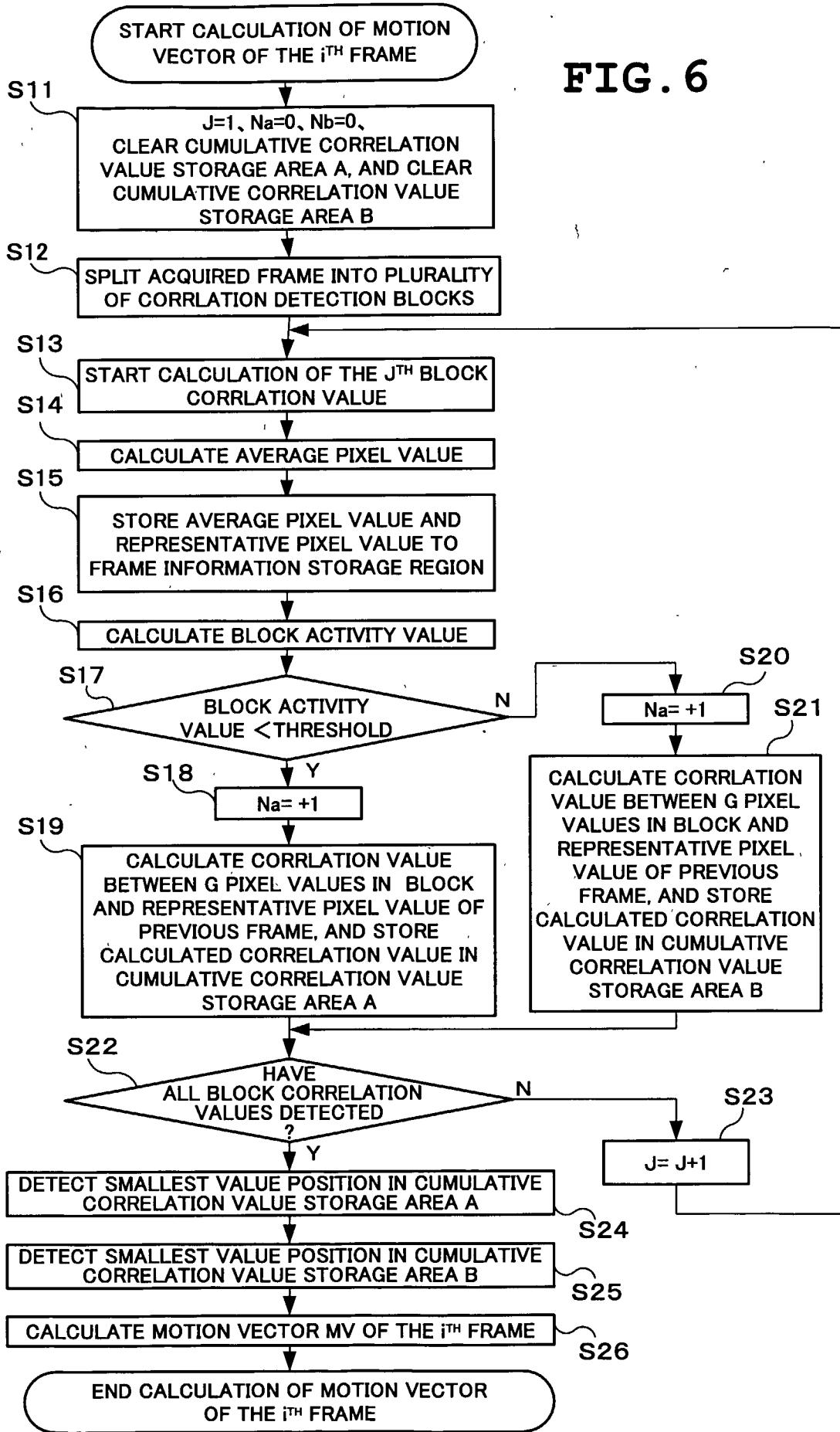
FIG. 4B

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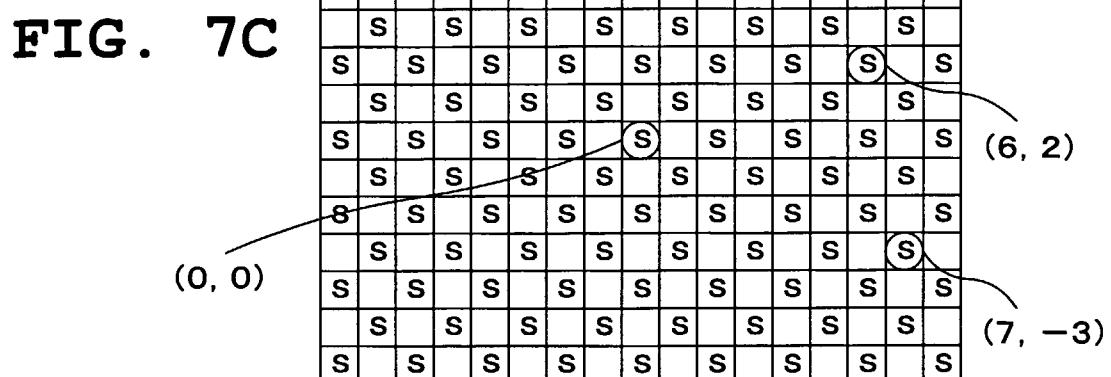
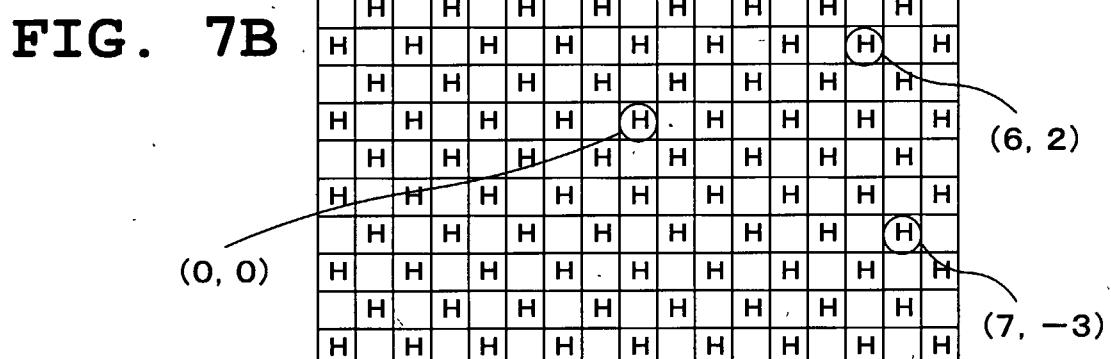
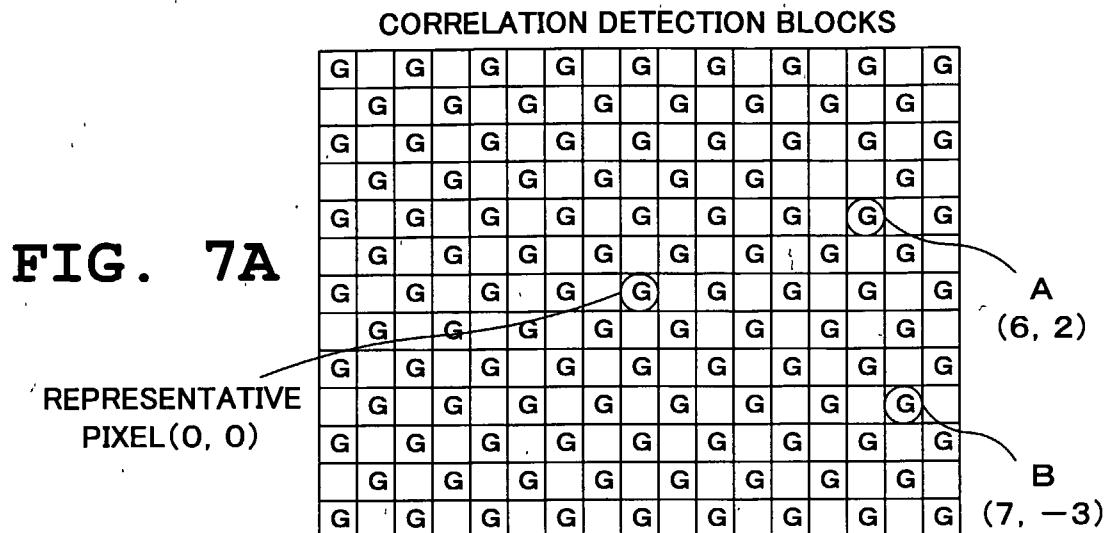
FIG. 5



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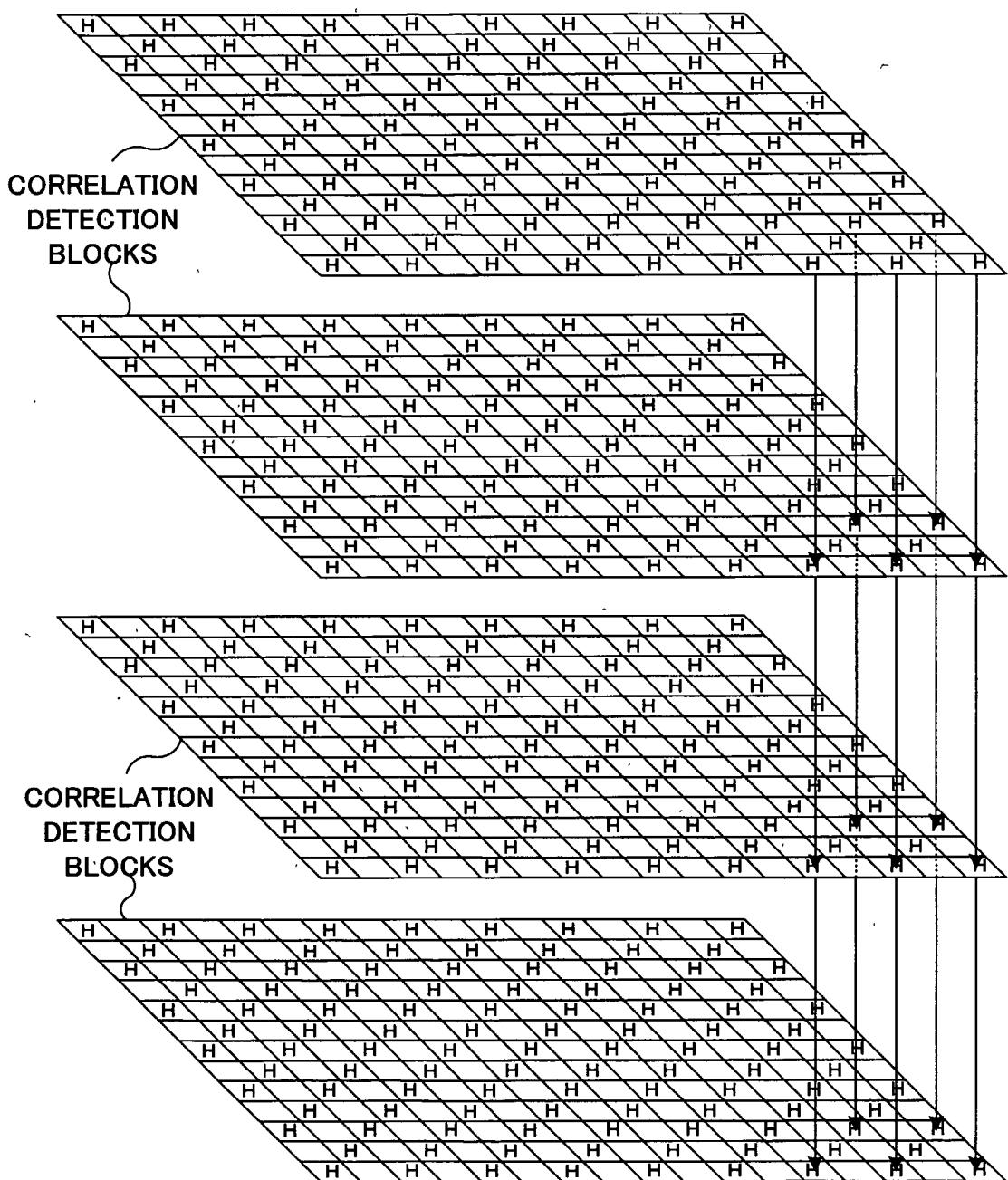


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FIG. 8



INTERNATIONAL SEARCH REPORT

International application No

PCT/JP2006/319817

A. CLASSIFICATION OF SUBJECT MATTER
 INV. H04N7/26 H04N5/232 H04N5/14

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04N G06T

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 1 377 040 A (ST MICROELECTRONICS SRL [IT]) 2 January 2004 (2004-01-02) page 6, line 1 - page 9, line 55; figures 4,5	1-22
X	EP 0 762 774 A2 (SONY CORP [JP]) 12 March 1997 (1997-03-12) column 11, line 52 - column 14, line 42; figure 11; compound 17	1-22
A	EP 0 789 487 A2 (MATSUSHITA ELECTRIC IND CO LTD [JP] MATSUSHITA ELECTRIC IND CO LTD) 13 August 1997 (1997-08-13) page 31, line 2 - page 32, line 33; figures 79-82	1-22

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents :

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- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
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- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
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Date of the actual completion of the international search

Date of mailing of the international search report

3 January 2007

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Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Schinnerl, Arnold

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/JP2006/319817

Patent document cited in search report	Publication date	Patent family member(s)			Publication date
EP 1377040	A 02-01-2004	US	2004001147	A1	01-01-2004
EP 0762774	A2 12-03-1997	US	5835138	A	10-11-1998
EP 0789487	A2 13-08-1997	NONE			