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(54) Title: ELECTRONIC STICKERS WITH RE-USABILITY ENHANCEMENTS

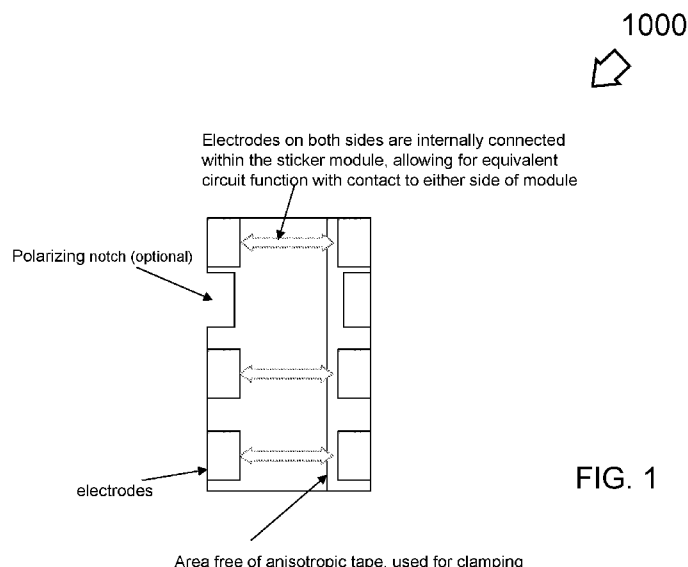


FIG. 1

(57) Abstract: Electronic sticker technology that enables assembly of circuits at ambient temperature without the use of any special tools.

ELECTRONIC STICKERS WITH RE-USABILITY ENHANCEMENTS

BACKGROUND

Field of the Disclosure

[0001] Aspects of the disclosure relate in general to electronic sticker technology that enables assembly of circuits at ambient temperature without the use of any special tools.

Description of the Related Art

[0002] An electrical network is an interconnection of electrical components (e.g. batteries, resistors, inductors, capacitors, switches) or a model of such an interconnection, consisting of electrical elements (e.g. voltage sources, current sources, resistances, inductances, capacitances).

[0003] An electrical circuit is a network consisting of a closed loop, giving a return path for the current. Linear electrical networks, a special type consisting only of sources (voltage or current), linear lumped elements (resistors, capacitors, inductors), and linear distributed elements (transmission lines), have the property that signals are linearly superimposable. They are thus more easily analyzed, using powerful frequency domain methods such as Laplace transforms, to determine DC response, AC response, and transient response.

SUMMARY

[0004] Embodiments include electronic sticker technology that enables assembly of circuits at ambient temperature without the use of any special tools.

[0005] An electronic sticker embodiment comprises a flexible substrate and a first wiring element. The first wiring element is bonded to the flexible substrate with an anisotropic conductive tape film. The anisotropic conductive tape film conducts current through a thickness of the anisotropic conductive tape film but not through a width or length of the anisotropic conductive tape film. The flexible substrate is polyimide.

The first wiring element is metal wire, metal foil tape, metal fabric, metal thread, metal foils, metal inks, suspensions of conductive nanoparticles, carbon-based inks or conductive film.

[0006] A method of manufacturing an electronic sticker includes cutting an anisotropic adhesive into an anisotropic adhesive panel. The anisotropic adhesive panel having a first side and a second side. The first side of the anisotropic adhesive panel is protected with a first wax paper. The second side of the anisotropic adhesive panel is protected with a second wax paper. The second wax paper has a lower surface energy than the first wax paper. The anisotropic adhesive panel is die-cut using a full-release technique. The die-cutting forms a pattern featuring alignment holes. A fully-assembled electronics circuit module is held in place on a flexible substrate with a vacuum chuck that has been machined with cavities to accommodate a profile on a first side of the flexible substrate. The vacuum chuck contains pins that pierce both the flexible substrate and the anisotropic adhesive panel. The second wax paper is peeled back, semi-exposing the anisotropic adhesive panel. The semi-exposed anisotropic adhesive panel is aligned to the pins of the vacuum chuck. The anisotropic adhesive panel and the fully-assembled electronics circuit module is laminated to the flexible substrate with a roller. The roller applies a temperature of at least 30°C and a pressure of at least 15psi for a period of at least five seconds to affect the lamination process. The anisotropic adhesive panel, the fully-assembled electronics circuit module, and the flexible substrate is held in an oven at approximately 40°C for at least half an hour.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] The figures below illustrate electronic sticker technology that enables assembly of circuits at ambient temperature without the use of any special tools.

[0008] FIG. 1 illustrates a clamp and test variant embodiment

[0009] FIG. 2 depicts an embodiment used in a circuit.

[0010] FIG. 3.illustrates an embodiment where the circuit has been proven to be tested and the design is ready to be finalized

[0011] FIG. 4 shows a multiple-stick embodiment.

DETAILED DESCRIPTION

[0012] Aspects of the present disclosure include electronic sticker technology to enable the assembly of circuits at ambient temperature without the use of any special tools. Through the use of isotropically or (more typically) anisotropically conductive adhesives, functional circuit elements are combined with wire elements using nothing more than a peel-and-stick motion.

[0013] One aspect of electronic sticker technology is the absence of any requirement for soldering, brazing, or other melting process involving elevated temperatures. A further aspect of electronic sticker technology is the absence of any splicing, wire wrapping, connectorization, or permanent requirement for clamping or pressure to maintain contact between circuit elements. These two properties enable electronic stickers to be used with a broad range of materials that would otherwise melt or burn, such as but not limited to paper, plastics, live tissue, plants, and fabrics.

[0014] Embodiment stickers are as flexible as paper. While not all circuit stickers are as flexible the described embodiments, the particular material system enables most of embodiment stickers to be easily placed on curved surfaces, such as cardboard tubes, cups, and glasses. While prior art stickers use thin FR-4 as their base material, this material is still too rigid to easily conform on non-flat surfaces.

Embodiments of the present disclosure use a polyimide (PI) substrate with coverlay, which accounts for enhanced flexibility and robustness to repeated flexing.

[0015] Wiring elements used with electronic sticker technology may comprise any conductive or semi-conductive material which is compatible with conductive adhesives. Examples of viable wiring elements include metal wire, metal foil tape, metal fabric, metal thread, metal foils, metal inks, suspensions of conductive nanoparticles, and carbon-based inks or films. These conductive elements have been demonstrated with electronic sticker technology using combinations of manual application, computer numerically controlled (CNC) additive (e.g. 3D printing), screen-printed, photolithographically defined, and/or CNC subtractive techniques (e.g. milling).

[0016] Embodiment electronic stickers use anisotropic conductive tape films to bond the polyimide (PI) substrates to wiring elements. Anisotropic conductive tape films have the property that they only conduct electricity in one dimension (typically the “Z direction”, or through the thickness of the film, but not laterally along the width or length of the film). This allows the film to be deposited uniformly across a series of electrodes without causing adjacent electrodes to short circuit. Other stickers are known to use isotropic conductive tapes. Since isotropic tapes can conduct electricity laterally, these films require isolation to prevent short circuits between adjacent contacts. The isolation can be done either before or after processing. For example, in one embodiment, the films may be cut into small pieces or arrays of dots and subsequently applied to the electrodes. In another embodiment, the film may be deposited in a uniform sheet and then stripped back using a combination of mechanical or chemical processing techniques.

[0017] Embodiments of electronic stickers also provide the stickers to users on panels with sacrificial backings, similar to conventional decorative stickers. As a result, embodiment stickers typically lack any tabs which retain them to the backing or other framework, thus allowing for a hassle-free peel-and-stick experience. The

panels and/or backings are typically sized larger than the electronic sticker, so that users can easily grab onto the backing. Other stickers cut the backing to the same size as the sticker, thus requiring users to pick at an edge to work the backing free of the sticker.

[0018] Significantly, the absence of soldering does not mean that the stickers are incompatible with soldering or other high-temperature processes. In fact, embodiment stickers use PI as its substrate and its melting point is far above that of most solders. Thus, embodiment stickers are explicitly designed to also support direct soldering, if the user requires it to achieve certain mechanical reliability or environmental robustness goals.

[0019] The solderability of the embodiment also means we can use conventional, low-cost mass-manufacturing techniques to pre-attach components to the substrate, thereby creating electronic circuit sticker modules of almost arbitrary complexity. While other electronic stickers use lower-cost, restricted-temperature plastics such as polyethylene, polyethylene terephthalate (e.g. mylar), or polystyrene as their substrates, these materials are incompatible with standard solders and therefore cannot directly use conventional mass-manufacturing techniques for electronic products. Instead they must use low-temperature solders or conductive epoxies, which often have inferior electrical and mechanical properties to conventional solder. This limits the ability to integrate advance circuits such as those involving RF and microcontrollers.

[0020] In many cases, electronic stickers are cheap enough to be considered disposable. If a circuit is not working correctly or needs to be modified, the original sticker is typically discarded, and a new one is placed down. While the used sticker can have its adhesive refurbished via a combination of cleaning with organic solvent and reapplication of anisotropic adhesive film, the process is typically not worth the effort, and the process also requires a certain degree of manual skill to execute successfully.

[0021] However, the opportunity to create modules of arbitrary complexity also implies that the value of an electronic sticker module may

increase to a point where it is no longer economically feasible to be considered disposable. Our electronic sticker process enables the creation of circuit sticker modules that incorporate a variety of microcontrollers, wireless radios, sensors, and/or transducers of fairly high value. Therefore, embodiments include electronic stickers which can be temporarily or repeatedly bonded into a circuit for testing or for educational/experimentation/demonstration purposes.

[0022] Re-usable electronic stickers may be manufactured using the following method described below.

[0023] The non-reusable electronic stickers previously manufactured feature the non-selective deposition of anisotropic tape across entire panels of flexible circuit materials.

[0024] Reusable electronic sticker embodiments are extremely low cost and may be compatible with “reel-to-reel” or continuous lamination processing techniques.

[0025] The manufacturing process embodiment is semi-continuous. Rolls of anisotropic adhesive are first cut into large panels, and protected using wax paper on both sides. One of the wax papers has a much lower surface energy than the other, to cause the paper to preferentially release on one side. The panels are then die-cut using a full-release technique (e.g. the cutting blade goes all the way through the adhesive material). This creates holes in the tape, which effects the selective deposition of anisotropic tape on the ultimate substrate. The die cutting pattern features alignment holes to ensure proper alignment during the later lamination stages. Furthermore, the arrangement of the electronic modules’ electrodes is designed to avoid large continuous holes in the panels, or slivers of material less than 2mm. This ensures that the tape has sufficient robustness to survive lamination.

[0026] After cutting comes lamination. The fully-assembled electronics circuit module is held in place with an optionally heated vacuum chuck that has been machined with cavities to accommodate the profile of the parts on the non-laminate side of the flex material. The vacuum chuck contains pins that pierce both the electronic flex substrate

and the anisotropic tape sheet, ensuring proper alignment. The lower-surface energy wax paper is peeled back, and the semi-exposed anisotropic tape is aligned to the pins on the chuck. A soft, optionally heated roller is used to apply a temperature of at least 30°C and a pressure of at least 15psi for a period of at least five seconds to affect the lamination process. After lamination, the assemblies are held in an oven at approximately 40°C for at least half an hour to accelerate curing. The temperature may vary between 32°C and 48°C.

[0027] The temperatures specified above are not very high, but important. The problem is that often factories are not heated, and in the winter the factory temperatures drop to below 10°C and the adhesives will fail to bond. Hence in lieu of heated tooling, some embodiments simply use a heated room, but it is often more cost-efficient to just heat the tools than the entire factory.

[0028] Sticker design method “clamp & test”: There are two design patterns for anisotropic adhesive deposition which we are developing. The first is called a “clamp & test” variant. The idea of this pattern is to enable unlimited re-use of the stickers during prototyping and testing by offering a stiffened edge of the sticker that is free of any adhesive. See diagrams below for details of the disclosure.

[0029] FIG. 1 illustrates basic anatomy of the clamp & test variant. Electrodes on opposite sides are electrically connected within the sticker itself.

[0030] FIG. 2. illustrates using a variant in a circuit. The wires are first laid down; they may either be brought to an edge for easy access, or simply laid out in parallel long tracks, and then folded at the region where the electronic sticker should be placed. The sticker is then held in place with a clip; the wax backing on the adhesive is not removed. For applications where folding is not desired, the tape can also be held in place with weight coated in a compliant material that helps ensure good contact. This form allows the sticker to be re-used indefinitely.

[0031] In another embodiment, instead of being held in place with a clip, the sticker is held in a two-ring, three-ring, or other plural-

ring binder. In such an embodiment, multiple stickers may be attached within the binder, and allowed to make electrical contact with each other.

[0032] Additionally, the sticker may be used with free-hanging alligator clips onto the adhesive-free electrodes. This use case does not require the paper backing at all, and is anticipated to be popular with educators and hobbyists with needs for “quick and dirty” prototypes.

[0033] FIG. 3 illustrates a step once the circuit has been proven to be tested and the design is ready to be finalized. The clip is removed. The wax paper backing is removed from the adhesive region, and the sticker is placed into a permanent position over the same electrodes used for testing. This is the reason why the electrodes on both sides of the module have parallel pinouts – a user can test using one side, and make permanent with the other.

[0034] Sticker design method “multiple-stick”: The second variant of the re-usable sticker we are developing is called the multiple-stick variant. This variant introduces multiple sacrificial electrodes that are clipped off every time the sticker needs to be re-used. This embodiment requires no clamping and/or folding of the paper for testing. However, the number of times the sticker can be re-used is limited by the number of sacrificial electrodes.

[0035] FIG. 4 illustrates an example of a multiple-stick variant. Anisotropic tape is only applied in the hatched regions, over exposed electrode lines. Each tape island has its own protective wax backing. In the case above, there are only two electrodes but the sticker can have more. In this scenario, the user would first peel-and-stick using the electrodes at (1). When the sticker is desired to be re-used, the sticker is cut along line (2), and electrodes at (3) are exposed and stuck into a new circuit. Finally, the sticker can be cut along line (4) and used one last time by exposing the electrodes at (5).

[0036] It is important to note that the multiple re-use technique is simply a method of decorating an electrode, and is thus compatible with the “clamp & test” variant. Specifically, the electrodes on

the clamp & test variant can be extended to be longer and have adhesive applied selectively to create the similar form presented here.

[0037] Use of selective stiffening. In all cases, additional polyimide stiffeners (strips of PI film ~0.3mm in thickness) may be laminated onto the flexible substrate to improve the robustness of the clamping or re-use points. This is particularly relevant for the “clamp & test” variant, because without the stiffener, free-hanging alligator clips can easily tear into and damage the thin, flexible circuit material.

[0038] The previous description of the embodiments is provided to enable any person skilled in the art to practice the disclosure. Thus, the present disclosure is not intended to be limited to the embodiments shown herein, but is to be accorded the widest scope consistent with the principles and features disclosed herein.

WHAT IS CLAIMED IS:

1. An electronic sticker comprising:

a flexible substrate;

a first wiring element bonded to the flexible substrate with an anisotropic conductive tape film, the anisotropic conductive tape film conducting current through a thickness of the anisotropic conductive tape film but not through a width or length of the anisotropic conductive tape film.

2. The electronic sticker of claim 1, wherein the flexible substrate is polyimide.

3. The electronic sticker of claim 2, wherein the first wiring element is metal wire, metal foil tape, metal fabric, metal thread, metal foils, metal inks, suspensions of conductive nanoparticles, carbon-based inks or conductive film.

4. A method of manufacturing an electronic sticker comprising:

cutting an anisotropic adhesive into an anisotropic adhesive panel, the anisotropic adhesive panel having a first side and a second side;

protecting the first side of the anisotropic adhesive panel with a first wax paper;

protecting the second side of the anisotropic adhesive panel with a second wax paper, the second wax paper having a lower surface energy than the first wax paper;

die-cutting the anisotropic adhesive panel using a full-release technique, the die-cutting forming a pattern featuring alignment holes;

holding a fully-assembled electronics circuit module in place on a flexible substrate with a vacuum chuck that has been machined with cavities to accommodate a profile on a first side of the flexible substrate,

the vacuum chuck containing pins that pierce both the flexible substrate and the anisotropic adhesive panel;

peeling back the second wax paper, semi-exposing the anisotropic adhesive panel;

aligning the semi-exposed anisotropic adhesive panel to the pins of the vacuum chuck;

laminating the anisotropic adhesive panel and the fully-assembled electronics circuit module to the flexible substrate with a roller;

holding the anisotropic adhesive panel, the fully-assembled electronics circuit module and the flexible substrate in an oven.

5. The method of claim 4, wherein the roller applies a temperature of at least 30°C.

6. The method of claim 5, wherein the roller further applies a pressure of at least 15psi.

7. The method of claim 6, wherein the roller is applied for a period of at least five seconds.

8. The method of claim 7, wherein the holding in the oven is at approximately 40°C.

9. The method of claim 8, wherein the holding in the oven for at least half an hour.

10. The method of claim 9, wherein the vacuum chuck is heated.

1000

Electrodes on both sides are internally connected within the sticker module, allowing for equivalent circuit function with contact to either side of module

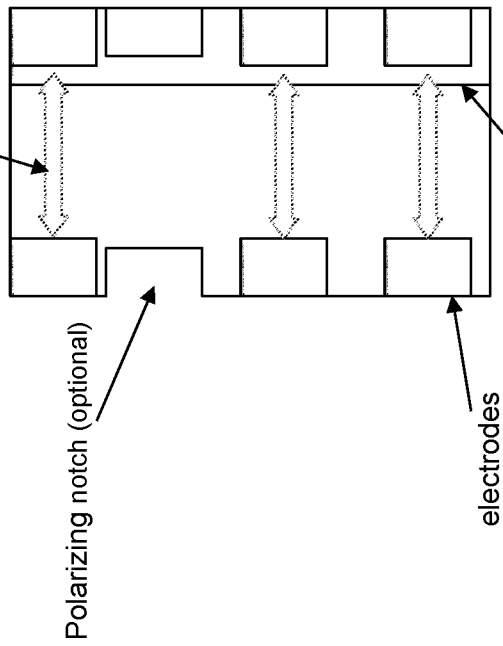


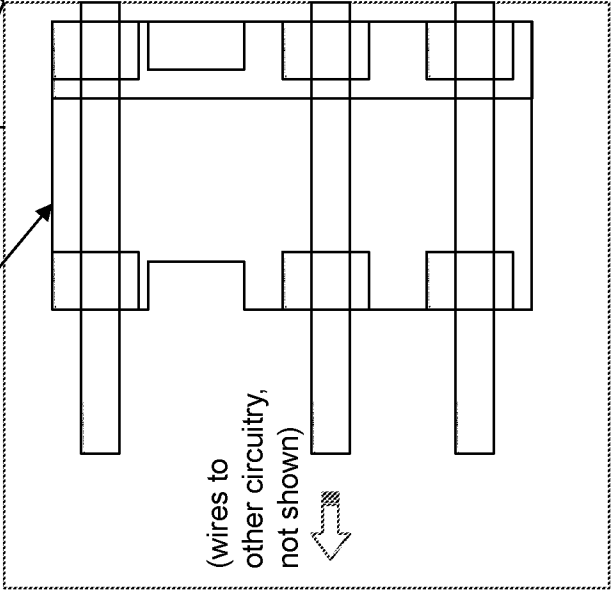
FIG. 1

2000



Substrate folded for access to clamp non-sticky electrodes in place; optionally, wires simply run to edge of substrate

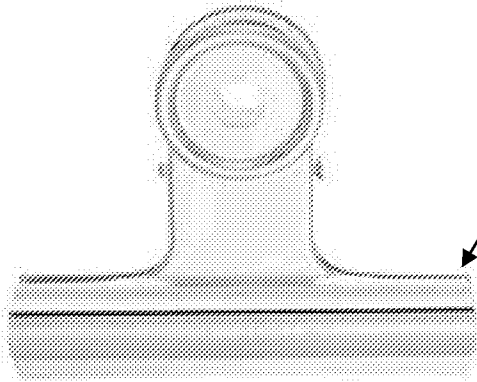
Wax paper backing still protecting Anisotropic tape during testing



(wires to other circuitry, not shown)



FIG. 2



Clip to hold sticker in place

3000

Wax paper backing now removed, sticker is permanently attached to the circuit

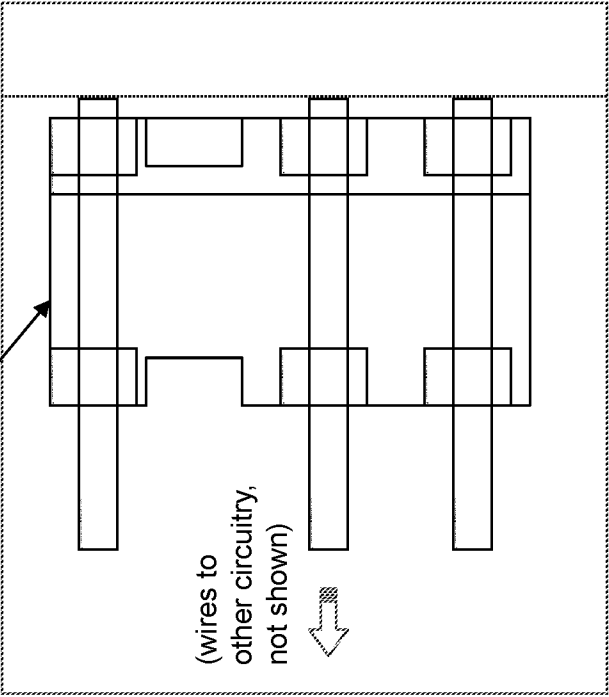


FIG. 3

4000

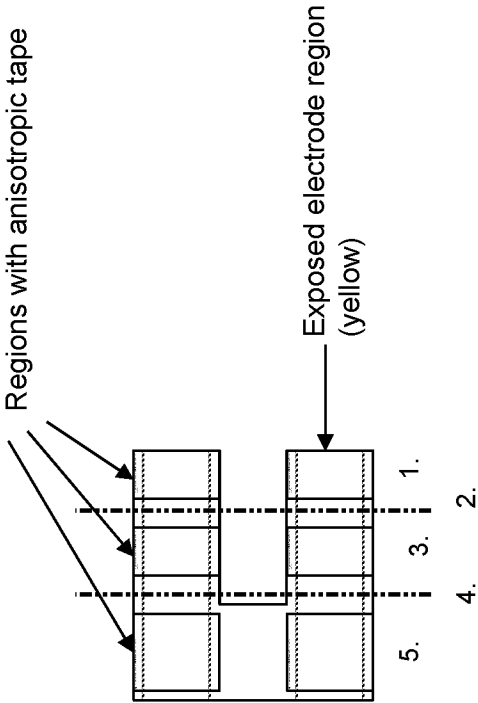


FIG. 4

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US 16/60606

A. CLASSIFICATION OF SUBJECT MATTER

IPC(8) - H05K 1/00; H01L 27/00; H01B 1/20; H01L 23/52; H01R 4/04; H05K 3/32; H05K 3/36 (2017.01)

CPC - H05K 3/323; H01B 1/20; H01R 4/04; H05K 3/361; H01L 2224/27436; H01L 2224/83851

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC(8) - H05K 1/00; H01L 27/00; H01B 1/20; H01L 23/52; H01R 4/04; H05K 3/32; H05K 3/36 (2017.01)

CPC - H05K 3/323; H01B 1/20; H01R 4/04; H05K 3/361; H01L 2224/27436; H01L 2224/83851

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Patents and non-patent literature (classification, keyword; search terms below)

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
PatBase, Google Scholar (NPL), Google Patents; search terms: electronic sticker, flexible substrate, wiring element, bonded, anisotropic conductive tape film, conduct, current, thickness, polyimide, metal wire, metal foil, carbon-based ink, conductive film

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2013/0192356 A1 (DE GRAFF et al.) 01 August 2013 (01.08.2013) para [0013], [0021], [0067], [0117], [0126], [0128], [0134]-[0135]; abstract	1-2
X	US 9,030,724 B2 (AGRAWAL et al.) 12 May 2015 (12.05.2015) col 1, ln 19-23; col 2, ln 27-32; col 6, ln 36-38; col 8, ln 31-39, 54, 66-67; col 16, ln 10-15; col 17, ln 41-43; col 53, ln 66-67; col 54, ln 1-2; col 55, ln 40-42; col 56, ln 33-53, 58-65	1, 3
A	US 4,606,962 A (REYLEK et al.) 19 August 1986 (19.08.1986) entire document, especially col 2, ln 20-38	1-3
A	Miyachi, "Anisotropic Conductive Adhesive Bonding" (August 2010) [online] [retrieved on 21 February 2017]. Retrieved from the Internet <URL: http://www.amadamiyachieurope.com/cmdata/documents/White-paper-ACF-Bonding-Technology-08-10.pdf > pg 1-2	1-3
A	US 2015/0310771 A1 (ATKINSON et al.) 29 October 2015 (29.10.2015) para [0004]-[0005], [0066], [0074], [0100]	1-3

☐ Further documents are listed in the continuation of Box C.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

21 February 2017

Date of mailing of the international search report

30 MAR 2017

Name and mailing address of the ISA/US

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PCT OSP: 571-272-7774

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 16/60606

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:
This application contains the following inventions or groups of inventions which are not so linked as to form a single general inventive concept under PCT Rule 13.1. In order for all inventions to be examined, the appropriate additional examination fees must be paid.

- Please see extra sheet for continuation -

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of additional fees.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:
1-3

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- ☐ The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- ☐ No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 16/60606

Continuation of: Box NO III. Observations where unity of invention is lacking

Group I: Claims 1-3, drawn to an electronic sticker comprising a first wiring element bonded to the flexible substrate with an anisotropic conductive tape film, the anisotropic conductive tape film conducting current through a thickness of the anisotropic conductive tape film but not through a width or length of the anisotropic conductive tape film.

Group II: Claims 4-10, drawn to a method comprising cutting an anisotropic adhesive into an anisotropic adhesive panel, the anisotropic adhesive panel having a first side and a second side; protecting the first side of the anisotropic adhesive panel with a first wax paper; protecting the second side of the anisotropic adhesive panel with a second wax paper, the second wax paper having a lower surface energy than the first wax paper; die-cutting the anisotropic adhesive panel using a full-release technique, the die-cutting forming a pattern featuring alignment holes; holding a fully-assembled electronics circuit module in place on a flexible substrate with a vacuum chuck that has been machined with cavities to accommodate a profile on a first side of the flexible substrate, the vacuum chuck containing pins that pierce both the flexible substrate and the anisotropic adhesive panel; peeling back the second wax paper, semi-exposing the anisotropic adhesive panel; aligning the semi-exposed anisotropic adhesive panel to the pins of the vacuum chuck; laminating the anisotropic adhesive panel and the fully-assembled electronics circuit module to the flexible substrate with a roller; holding the anisotropic adhesive panel, the fully-assembled electronics circuit module and the flexible substrate in an oven.

The inventions listed as Groups I-II do not relate to a single general inventive concept under PCT Rule 13.1 because, under PCT Rule 13.2, they lack the same or corresponding special technical features for the following reasons:

Special Technical Features

Group II does not require an electronic sticker comprising a first wiring element bonded to the flexible substrate with an anisotropic conductive tape film, the anisotropic conductive tape film conducting current through a thickness of the anisotropic conductive tape film but not through a width or length of the anisotropic conductive tape film, as required by Group I.

Group I does not require a method comprising cutting an anisotropic adhesive into an anisotropic adhesive panel, the anisotropic adhesive panel having a first side and a second side; protecting the first side of the anisotropic adhesive panel with a first wax paper; protecting the second side of the anisotropic adhesive panel with a second wax paper, the second wax paper having a lower surface energy than the first wax paper; die-cutting the anisotropic adhesive panel using a full-release technique, the die-cutting forming a pattern featuring alignment holes; holding a fully-assembled electronics circuit module in place on a flexible substrate with a vacuum chuck that has been machined with cavities to accommodate a profile on a first side of the flexible substrate, the vacuum chuck containing pins that pierce both the flexible substrate and the anisotropic adhesive panel; peeling back the second wax paper, semi-exposing the anisotropic adhesive panel; aligning the semi-exposed anisotropic adhesive panel to the pins of the vacuum chuck; laminating the anisotropic adhesive panel and the fully-assembled electronics circuit module to the flexible substrate with a roller; holding the anisotropic adhesive panel, the fully-assembled electronics circuit module and the flexible substrate in an oven, as required by Group II.

Shared Common Features

Groups I-II share the technical feature of an electronic sticker comprising a flexible substrate and an anisotropic adhesive. However, this shared technical feature does not represent a contribution over prior art, because the shared technical feature is disclosed by US 2013/0192356 A1 to De Graff et al. (hereinafter "De Graff"). De Graff teaches an electronic device (para [0021]; claim 1; test data sheet comprising flexible substrate, adhesive, and electronic circuitry for sensing, measuring, or recording technical parameters) in the form of an electronic sticker (para [0021]; test data sheet may be in the form of an electronic sticker), comprising a flexible substrate (para [0021]; claim 1), and an anisotropic adhesive (para [0126]; anisotropic conductive film connection).

As the common technical features were known in the art at the time of the invention, these cannot be considered a special technical feature that would otherwise unify the groups.

Groups I-II therefore lack unity under PCT Rule 13 because they do not share a same or corresponding special technical feature.