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(71) Applicant: **SUNPOWER CORPORATION** [US/US]; 77
Rio Robles, San Jose, California 95134 (US).

(72) Inventor: **CASWELL, Nathaniel Alexis**; 999 Evelyn Ter-
race West, Apt. 17, Sunnyvale, California 94086 (US).

(74) Agent: **SCHMIDT, Mark E.**; Innovation Counsel LLP,
2880 Lakeside Drive, Suite 200, Santa Clara, California
95054 (US).

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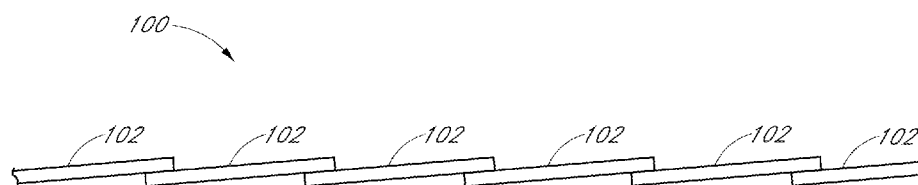


FIG. 1

(57) Abstract: A high efficiency configuration for a solar cell module comprises solar cells arranged in a shingled manner to form super cells, which may be arranged to efficiently use the area of the solar module, reduce series resistance, and increase module efficiency. Removing a defective solar cell from a super cell may be difficult, however. It may therefore be advantageous to bypass defective solar cells in a super cell rather than remove and replace them. A bypass conductor may be applied to the rear surface of the super cell to bypass one or more defective solar cells in a super cell or in a solar module comprising super cells.



SYSTEMS AND METHODS FOR REWORKING SHINGLED SOLAR CELL MODULES

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This patent application claims benefit of priority from U.S. Patent Application No. 15/177,093 titled “Systems and Methods for Reworking Shingled Solar Cell Modules” filed June 8, 2016, which is incorporated herein by reference in its entirety.

BACKGROUND

[0002] Photovoltaic (PV) cells, commonly known as solar cells, are devices for conversion of solar radiation into electrical energy. Generally, solar radiation impinging on the surface of, and entering into, the substrate of a solar cell creates electron and hole pairs in the bulk of the substrate. The electron and hole pairs migrate to p-doped and n-doped regions in the substrate, thereby creating a voltage differential between the doped regions. The doped regions are connected to conductive regions on the solar cell to direct an electrical current from the cell to an external circuit. When PV cells are combined in an array such as a PV module, the electrical energy collected from all of the PV cells can be combined in series and parallel arrangements to provide power with a desired voltage and current.

BRIEF DESCRIPTION OF THE DRAWINGS

[0003] The figures described below depict various aspects of the system and methods disclosed herein. It should be understood that each figure depicts an embodiment of a particular aspect of the disclosed system and methods, and that each of the figures is intended to accord with a possible embodiment thereof. Further, wherever possible, the following description refers to the reference numerals included in the following figures, in which features depicted in multiple figures are designated with consistent reference numerals.

[0004] Fig. 1 shows a cross-sectional view of a string of series-connected solar cells arranged in a shingled manner with the ends of adjacent solar cells overlapping and electrically connected to form a super cell;

[0005] Fig. 2 shows the front surface metallization pattern of an example rectangular solar cell that may be used in a super cell;

[0006] Fig. 3 shows an example solar cell rear surface metallization pattern suitable for use with the defective solar cell bypass techniques discussed herein;

[0007] Fig. 4 shows an example rectangular solar module comprising six rectangular super cells, each of which has a length approximately equal to the length of the long sides of the solar module;

[0008] Figs. 5A-5I show example illustrations of defective cell bypass conductors partially or completely bypassing defective solar cells in a super cell;

[0009] Fig. 6 shows examples of solar modules with one or more completely bypassed solar cells and/or defective solar cells;

[0010] Figs. 7A-7D show example IV curves of solar modules with one or more completely bypassed solar cells and/or defective solar cells; and

[0011] Figs. 8A-8B depict example inspection and reworking methods for reworking a super cell in accordance with the presently described embodiments.

SUMMARY

[0012] Embodiments may include a photovoltaic module comprising: a first super cell comprising a plurality of solar cells, each having a rear surface, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein at least one of the plurality of solar cells is a first defective solar cell; and a bypass conductor coupled to the rear surface of a first solar cell in the first super cell and coupled to the rear surface of a second solar cell in the first super cell disposed after the first defective solar cell in series, wherein the bypass conductor is

adapted to bypass the first defective solar cell by conducting electricity from the rear surface of the first solar cell in the first super cell to the rear surface of the second solar cell in the first super cell.

[0013] Embodiments may also include an apparatus comprising: a plurality of solar cells, including a first solar cell and a second solar cell, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein at least one of the plurality of solar cells is a defective solar cell, wherein each solar cell has a rear surface having at least one set of contact pads (e.g., a plurality of sets of contact pads); a bypass conductor coupled to at least one set of contact pads of the first solar cell and at least one set of contact pads of the second solar cell, wherein the bypass conductor bypasses the defective solar cell.

[0014] Embodiments may further include, an apparatus comprising: a plurality of solar cells, including a first solar cell and a second solar cell, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein one or more of the plurality of solar cells is one or more defective solar cells, wherein each solar cell has a rear surface; and a bypass conductor coupled to the rear surface of the first solar cell and coupled to the rear surface of the second solar cell; wherein the bypass conductor is adapted to short circuit the one or more defective solar cells by conducting electricity from the rear surface of the first solar cell to the rear surface of the second solar cell.

DETAILED DESCRIPTION

[0015] The following detailed description is merely illustrative in nature and is not intended to limit the embodiments of the subject matter of the application or uses of such embodiments. As used herein, the word “exemplary” means “serving as an example, instance, or illustration.” Any implementation described herein as exemplary is not necessarily to be construed as preferred or advantageous over other implementations. Furthermore, there is no intention to be bound by any expressed or implied

theory presented in the preceding technical field, background, brief summary or the following detailed description.

[0016] This specification includes references to “one embodiment” or “an embodiment.” The appearances of the phrases “in one embodiment” or “in an embodiment” do not necessarily refer to the same embodiment. Particular features, structures, or characteristics may be combined in any suitable manner consistent with this disclosure.

[0017] Terminology. The following paragraphs provide definitions and/or context for terms found in this disclosure (including the appended claims):

[0018] “Comprising.” This term is open-ended. As used in the appended claims, this term does not foreclose additional structure or steps.

[0019] “Configured To.” Various units or components may be described or claimed as “configured to” perform a task or tasks. In such contexts, “configured to” is used to connote structure by indicating that the units/components include structure that performs those task or tasks during operation. As such, the unit/component can be said to be configured to perform the task even when the specified unit/component is not currently operational (e.g., is not on/active). Reciting that a unit/circuit/component is “configured to” perform one or more tasks is expressly intended not to invoke 35 U.S.C. §112, sixth paragraph, for that unit/component.

[0020] “First,” “Second,” etc. As used herein, these terms are used as labels for nouns that they precede, and do not imply any type of ordering (e.g., spatial, temporal, logical, etc.). For example, reference to a “first” silicon solar cell does not necessarily imply that this silicon solar cell is the first silicon solar cell in a sequence; instead the term “first” is used to differentiate this silicon solar cell from another silicon solar cell (e.g., a “second” silicon solar cell).

[0021] “Based On.” As used herein, this term is used to describe one or more factors that affect a determination. This term does not foreclose additional factors that may affect a determination. That is, a determination

may be solely based on those factors or based, at least in part, on those factors. Consider the phrase “determine A based on B.” While B may be a factor that affects the determination of A, such a phrase does not foreclose the determination of A from also being based on C. In other instances, A may be determined based solely on B.

[0022] “Coupled” – The following description refers to elements or nodes or features being “coupled” together. As used herein, unless expressly stated otherwise, “coupled” means that one element/node/feature is directly or indirectly joined to (or directly or indirectly communicates with) another element/node/feature, and not necessarily mechanically.

[0023] “Inhibit” – As used herein, inhibit is used to describe a reducing or minimizing effect. When a component or feature is described as inhibiting an action, motion, or condition it may completely prevent the result or outcome or future state completely. Additionally, “inhibit” can also refer to a reduction or lessening of the outcome, performance, and/or effect which might otherwise occur. Accordingly, when a component, element, or feature is referred to as inhibiting a result or state, it need not completely prevent or eliminate the result or state.

[0024] In addition, certain terminology may also be used in the following description for the purpose of reference only, and thus are not intended to be limiting. For example, terms such as “upper”, “lower”, “above”, and “below” refer to directions in the drawings to which reference is made. Terms such as “front”, “back”, “rear”, “side”, “outboard”, and “inboard” describe the orientation and/or location of portions of the component within a consistent but arbitrary frame of reference which is made clear by reference to the text and the associated drawings describing the component under discussion. Such terminology may include the words specifically mentioned above, derivatives thereof, and words of similar import.

[0025] In the following description, numerous specific details are set forth, such as specific operations, in order to provide a thorough understanding of embodiments of the present disclosure. It will be apparent to one skilled in

the art that embodiments of the present disclosure may be practiced without these specific details. In other instances, well-known techniques are not described in detail in order to not unnecessarily obscure embodiments of the present disclosure.

[0026] Fig. 1 shows a cross-sectional view of a string of series-connected solar cells 102 arranged in a shingled manner with the ends of adjacent solar cells overlapping and electrically connected to form a super cell 100. Each solar cell 102 comprises a semiconductor diode structure and electrical contacts to the semiconductor diode structure by which electric current generated in solar cell 102 when it is illuminated by light may be provided to an external load.

[0027] In the examples described in this specification, each solar cell 102 is a crystalline silicon solar cell having front (sun side) surface and rear (shaded side) surface metallization patterns providing electrical contact to opposite sides of an n-p junction, the front surface metallization pattern is disposed on a semiconductor layer of n-type conductivity, and the rear surface metallization pattern is disposed on a semiconductor layer of p-type conductivity. However, any other suitable solar cells employing any other suitable material system, diode structure, physical dimensions, or electrical contact arrangement may be used instead of or in addition to solar cells 102 in the solar modules described in this specification. For example, the front (sun side) surface metallization pattern may be disposed on a semiconductor layer of p-type conductivity, and the rear (shaded side) surface metallization pattern disposed on a semiconductor layer of n-type conductivity.

[0028] Referring again to Fig. 1, in super cell 100 adjacent solar cells 102 are conductively bonded to each other in the region in which they overlap by an electrically conducting bonding material that electrically connects the front surface metallization pattern of one solar cell to the rear surface metallization pattern of the adjacent solar cell. Suitable electrically conducting bonding materials may include, for example, electrically conducting adhesives and electrically conducting adhesive films and adhesive

tapes, and conventional solders. Preferably, the electrically conducting bonding material provides mechanical compliance in the bond between the adjacent solar cells that accommodates stress arising from mismatch between the coefficient of thermal expansion (CTE) of the electrically conducting bonding material and that of the solar cells (e.g., the CTE of silicon). To provide such mechanical compliance, in some variations the electrically conducting bonding material is selected to have a glass transition temperature of less than or equal to about 0°C. To further reduce and accommodate stress parallel to the overlapping edges of the solar cells arising from CTE mismatch, the electrically conductive bonding material may optionally be applied only at discrete locations along the overlapping regions of the solar cells rather than in a continuous line extending substantially the length of the edges of the solar cells.

[0029] The thickness of the electrically conductive bond between adjacent overlapping solar cells formed by the electrically conductive bonding material, measured perpendicularly to the front and rear surfaces of the solar cells, may be for example less than about 0.1 mm. Such a thin bond reduces resistive loss at the interconnection between cells, and also promotes flow of heat along the super cell from any hot spot in the super cell that might develop during operation. The thermal conductivity of the bond between solar cells may be, for example, $\geq$ about 1.5 Watts / (meter K).

[0030] Additionally, because the profitability of production of solar modules 400 comprising super cells 100 may depend on high volume of production with low margins, it may be important to repair or rework super cells 100 where one or more solar cells 102 is defective as discussed herein. However because of the delicacy of the super cell 100 (e.g., the thinness of the solar cells 102, the relative strength of the electrically conductive bond, etc.), removing a solar cell 102 from a super cell 100 may be difficult and expensive. Additionally, inserting a replacement solar cell 102 may also be difficult and expensive. Accordingly, it may be advantageous to bypass solar

cells 102 to avoid or mitigate defects as discussed herein rather than removing the solar cells 102.

[0031] Fig. 2 shows the front surface metallization pattern 200 of an example rectangular solar cell 102 that may be used in a super cell 100. Other shapes for solar cell 102 may also be used, as suitable (e.g., a square solar cell 102, a solar cell 102 with one or more chamfered corners). In the illustrated example the front surface metallization pattern 200 of solar cell 102 includes a bus bar 202 positioned adjacent to the edge of one of the long sides of solar cell 102 and running parallel to the long sides for substantially the length of the long sides, and fingers 204 attached perpendicularly to the bus bar and running parallel to each other and to the short sides of solar cell 102 for substantially the length of the short sides. The example front surface metallization pattern 200 of Fig. 2 also includes an optional end conductor 208 that interconnects fingers 204 at their far ends, opposite from bus bar 202. The width of end conductor 208 may be about the same as that of a finger 204, for example. End conductor 208 interconnects fingers 204 to electrically bypass cracks that may form between bypass conductor 206 and end conductor 208, and thereby provides a current path to bus bar 202 for regions of solar cell 102 that might otherwise be electrically isolated by such cracks.

[0032] In the example of Fig. 2 solar cell 102 has a length of about 156 mm, a width of about 26 mm, and thus an aspect ratio (length of short side / length of long side) of about 1:6. Six such solar cells may be prepared on a standard 156 mm x 156 mm dimension silicon wafer, then separated (diced) to provide solar cells as illustrated. In other variations, eight solar cells 102 having dimensions of about 19.5 mm x 156 mm, and thus an aspect ratio of about 1:8, may be prepared from a standard silicon wafer. More generally, solar cells 102 may have aspect ratios of, for example, about 1:2 to about 1:20 and may be prepared from standard size wafers or from wafers of any other suitable dimensions.

[0033] Fig. 3 shows an example solar cell rear surface metallization pattern 300 suitable for use with the defective solar cell bypass techniques

discussed herein. The rear surface metallization pattern 300 comprises a continuous aluminum electrical contact 302, a plurality of silver contact pads 304 arranged parallel to and adjacent the edge of a long side of the rear surface of the solar cell, and a plurality of hidden tap contact pads 306 arranged in two rows parallel to the long sides of the solar cell and approximately centered on the rear surface of the solar cell. While Fig. 3 shows three sets of hidden tap contact pads 306, each set having two contact pads 306 (one in each row), it will be understood that any number of sets of hidden tap contact pads 306 may be used (e.g., one, two, three, four, or more) and that each set may include any number of contact pads (e.g., one, two, three, four, or more). As discussed in International Publication WO 2015/183827, which is hereby incorporated by reference herein in its entirety, these hidden tap contact pads 306 may be used for coupling components such as bypass diodes to the rear surface of a solar cell 102. However, it may only be advantageous to couple bypass diodes to a minority of solar cells 102 in a solar module 400 (e.g., one bypass diode for every 21 solar cells 102). On the other hand, it may be advantageous for the rear surface metallization pattern 300 to be the same for each solar cell 102 (e.g., to save costs, to simplify manufacturing processes, etc.). Accordingly, a majority of solar cells 102 may have hidden tap contact pads 306 that are not coupled to bypass diodes and therefore not being used. The inventor discovered another use for these hidden tap contact pads 306: bypassing defective solar cells 502 as discussed herein.

[0034] Fig. 4 shows an example rectangular solar module 400 comprising six rectangular super cells 100, each of which has a length approximately equal to the length of the long sides of the solar module. The super cells are arranged as six parallel rows with their long sides oriented parallel to the long sides of the module. A similarly configured solar module may include more or fewer rows of such side-length super cells than shown in this example. Each super cell in this example (and in several of the following examples) comprises 72 rectangular solar cells each having a width approximately equal to 1/6 the width of a 156 mm square or pseudo square

wafer. Any other suitable number of rectangular solar cells of any other suitable dimensions may also be used. In this example the front surface terminal contacts of the super cells are electrically connected to each other with flexible interconnects 402 positioned adjacent to and running parallel to the edge of one short side of the module. The rear surface terminal contacts of the super cells are similarly connected to each other by flexible interconnects 402 positioned adjacent to and running parallel to the edge of the other short side, behind the solar module. The rear surface interconnects would be hidden or partially hidden from view in the top surface view of Fig. 4, but both the front and rear surface interconnects 402 are shown in Fig. 4 to indicate their location in the module. This arrangement electrically connects the six module-length super cells in parallel.

[0035] Figs. 5A-5I show example illustrations of defective cell bypass conductors 504A-504C, 506A-506C, 508, 510, 512, or 514 used to partially or completely bypass defective solar cells 502 in a super cell 100. As discussed herein, the defective solar cells 502 may be “partially bypassed” or “completely bypassed.” It will be understood that a “partially bypassed” defective solar cell 502 does not contribute any power to the electrical output of the solar module 400. A partially bypassed defective solar cell 502 conducts electricity from one neighboring solar cell 102 along the rear surface metallization pattern 300 and then through the defective cell bypass conductor 504A-504C, 506A-506C, 508, 510, 512, or 514 to the next neighboring solar cell 102 in series. The defective cell bypass conductor 504A-504C, 506A-506C, 508, 510, 512, or 514 provides a low resistance path so that current does not have to pass through the defective solar cell 502. By providing this path, the defective solar cell 502 is shorted and will not generate power. Because a partial bypass uses the rear surface metallization pattern 300 to conduct electricity, the rear surface metallization pattern 300 must be intact enough to allow electrical conductivity. Additionally, a partial bypass may not be appropriate if a significant portion of the defective solar cell 502 is missing (also referred to herein as a “chip”). It will be understood that a “completely

bypassed” defective solar cell 502 will also not contribute any electrical power to the electrical output of the solar module 400 because the completely bypassed defective solar cell 502 will be short circuited by a defective cell bypass conductor 504A-504C, 506A-506C, 508, 510, 512, or 514 that is coupled to the rear surface metallization pattern 300 of one neighboring solar cell 102 and coupled to the rear surface metallization pattern 300 of the other neighboring solar cell 102. Thus, no current flows along the rear surface metallization pattern 300 of the defective solar cell 502, instead flowing along the low resistance path (i.e., the defective cell bypass conductor 504A-504C, 506A-506C, 508, 510, 512, or 514) between the rear surface metallization patterns 300 of the two neighbor solar cells 102, shorting both the cracked cell and one of the neighbors. Accordingly, in a complete bypass, both the defective solar cell 502 and one of the neighboring solar cells 102 is shorted and do not contribute any electrical power to the output of the solar module 400.

[0036] Even though completely bypassing a defective solar cell 502 also necessarily involves bypassing a solar cell 102 that is not defective, it may nonetheless be advantageous to completely bypass defective solar cells 502 because conducting electricity through the rear surface metallization pattern 300 of the defective solar cell 502 may decrease performance (e.g., reliability, durability) of the solar module 400. Additionally, before a defective solar cell 502 may be partially bypassed, it may be advantageous to inspect the rear surface metallization pattern 300 (e.g., by human technician, by machine) to ensure that the rear surface metallization pattern 300 is sufficient for current to flow across it. This inspection may be costly to perform (e.g., costly in time, costly in labor, costly in capital costs of a separate machine), and therefore it may be advantageous to completely bypass a defective solar cell 502 instead.

[0037] The defective cell bypass conductors 504A-504C, 506A-506C, 508, 510, 512, or 514 may be made of any of a number of conductive materials such as metal (e.g., copper, silver, aluminum, etc.), conductive

composite, or conductive polymers. The defective cell bypass conductors 504A-504C, 506A-506C, 508, 510, 512, or 514 may be coupled to the hidden tap contact pads 306 of the various solar cells 102 and defective solar cells 502 by any of a number of known techniques (e.g., by welding, by electrically conductive adhesive, etc.).

[0038] Each of Figs. 5A-5I includes a view of the rear surface metallization pattern 300 of portions of three super cells 100A, 100B, and 100C. While only six silicon solar cells 102 are shown in each super cell 100 shown on Figs. 5A-5I, it will be understood that each super cell 100 could include fewer or greater numbers of silicon solar cells 102 (e.g., 72 silicon solar cells 102 as shown in Fig. 4). Each of Figs. 5A-5I shows at least one defective solar cell 502 and one or more bypass conductors 504A-504C, 506, 508, 510, 512, or 514. As discussed herein, the rear surface metallization pattern 300 of each solar cell 102 includes at least one set of hidden tap contact pads 306 (e.g., a plurality of sets of hidden tap contact pads 306) which may be used to bypass defective solar cells 502. For visual clarity, each such set of hidden tap contact pads is represented by a single contact pad 306 in Figs. 5A-5I. As noted above, however, a set of hidden tap contact pads may include any number of hidden tap contact pads 306 (e.g., one, two, three, four, or more). While only three sets of hidden tap contact pads 306 are shown in the illustrated examples, a greater number or lesser number of sets may be present. Hence, it will be understood that there may be any number of hidden tap contact pads 306 (e.g., one, two, three, four, or more) included in the rear surface metallization pattern 300 of each solar cell 102 and defective solar cell 502. Additionally, each defective solar cell 502 in Figs. 5A-5I is shown having a crack on the left side, it will be understood that the defective solar cell 502 may be defective in other ways as discussed herein.

[0039] A defective solar cell 502 may include one or more variances from acceptable parameters for a solar cell 102. Such variances may include a crack in the substrate, front surface metallization pattern 200, and/or rear surface metallization pattern 300 of defective solar cell 502; a chip in the

defective solar cell 502 (i.e., a piece of the defective solar cell 502 is missing); a malformed front surface metallization pattern 200 of the defective solar cell 502; a malformed rear surface metallization pattern 300 of the defective solar cell 502; an electrical short in the defective solar cell 502; an out-of-specification voltage of the defective solar cell 502; an out-of-specification power output of the defective solar cell 502; poor electrical conductivity from the defective solar cell 502 to another solar cell 102; etc. In short, if the performance (e.g., IV curve, power output, reliability, etc.) of the super cell 100 may be improved by bypassing a particular solar cell 102, that solar cell 102 may be a defective solar cell 502 and it may be advantageous to bypass that particular solar cell 102 as discussed herein.

[0040] Fig. 5A shows three super cells 100A, 100B, and 100C, each having at least one defective solar cell 502 and a defective cell bypass conductor 504A, 504B, or 504C. The defective solar bypass conductors 504A, 504B, and 504C may be made of a single, continuous piece of conductive material or made of multiple pieces of conductive material that are conductively bonded to each other (e.g., by welding, by electrically conductive adhesive). Super cell 100A includes a defective solar cell 502 third in series from the top. The defective solar cell 502 in the super cell 100A is completely bypassed by a bypass conductor 504A coupled to a plurality of sets of the hidden tap contact pads 306 of the solar cell 102A and coupled to a plurality of sets of the hidden tap contact pads 306 of the solar cell 102A'. Accordingly, current flowing through the super cell 100A will completely bypass the defective solar cell 502 because current will flow from the solar cell 102A through the bypass conductor 504A to the solar cell 102A'.

[0041] Super cell 100B includes a defective solar cell 502 third in series from the top. The defective solar cell 502 in the super cell 100B is partially bypassed by a bypass conductor 504B coupled to a plurality of sets of the hidden tap contact pads 306 of the defective solar cell 502 and coupled to a plurality of sets of the hidden tap contact pads 306 of the solar cell 102B. Electricity is conducted from the solar cell 102B through the bypass conductor

504B to the defective solar cell 502. Current then passes from the rear surface metallization pattern 300 of the defective solar cell 502 to the front surface metallization pattern 200 of the solar cell 102B' as discussed herein.

[0042] Super cell 100C includes a first defective solar cell 502 third in series from the top and a second defective solar cell 502 fourth in series from the top. The first and second defective solar cells 502 in the super cell 100C are bypassed by a bypass conductor 504C coupled to a plurality of sets of the hidden tap contact pads 306 of the solar cell 102C and coupled to a plurality of sets of the hidden tap contact pads 306 of the solar cell 102C'. Accordingly, current flowing through the super cell 100C will completely bypass the first and second defective solar cells 502 because current will flow from the solar cell 102C through the bypass conductor 504C to the solar cell 102C'. It will be understood that more than two defective solar cells 502 (e.g., three, four, five, or more) may be bypassed in this way.

[0043] Fig. 5B shows three super cells 100A, 100B, and 100C, each having one completely bypassed defective solar cell 502 and one or more defective cell bypass conductors 506A-506C. Super cell 100A includes a defective solar cell 502 third in series from the top. The defective solar cell 502 in the super cell 100A is completely bypassed by a bypass conductor 506A coupled to one set of the hidden tap contact pads 306 of the solar cell 102A and coupled to one set of the hidden tap contact pads 306 of the solar cell 102A'. While Fig. 5B shows the bypass conductor 506A coupled to the set of hidden tap contact pads 306 in the middle of solar cell 102A and coupled to the set of silver hidden contact pads 306 in the middle of solar cell 102A', it will be understood that any of the sets of hidden tap contact pads 306 of solar cell 102A may be so coupled to any of the sets of silver hidden contact pads 306 of solar cell 102A' via the bypass conductor 506A. Accordingly, current flowing through the super cell 100A will completely bypass the defective solar cell 502 because current will flow from the solar cell 102A through the defective cell bypass conductor 506A to the solar cell 102A'. It

will be understood that more than one defective solar cells 502 (e.g., two, three, four, five, or more) may be bypassed in this way.

[0044] Super cell 100B includes a defective solar cell 502 third in series from the top. The defective solar cell 502 in the super cell 100B is completely bypassed by a pair of defective cell bypass conductors 506B connecting two sets of the hidden tap contact pads 306 of the solar cell 102B to two sets of hidden tap contact pads 306 of the solar cell 102B'. While Fig. 5B shows the sets of hidden tap contact pads 306 on the left and right of solar cell 102B coupled to the sets of silver hidden contact pads 306 on the left and right of solar cell 102B' by the pair of defective cell bypass conductors 506B, it will be understood that any two of the sets of hidden tap contact pads 306 of solar cell 102B may be so coupled to any two of the sets of silver hidden contact pads 306 of solar cell 102B'. Accordingly, current flowing through the super cell 100B will completely bypass the defective solar cell 502 because current will flow from the solar cell 102B through the defective cell bypass conductors 506B to the solar cell 102B'. It will be understood that more than one defective solar cells 502 (e.g., two, three, four, five, or more) may be bypassed in this way.

[0045] Super cell 100C includes a defective solar cell 502 third in series from the top. The defective solar cell 502 in the super cell 100C is completely bypassed by a trio of defective cell bypass conductor 506C coupled to three sets of the hidden tap contact pads 306 of the solar cell 102C and coupled to three sets of hidden tap contact pads 306 of the solar cell 102C'. Accordingly, current flowing through the super cell 100C will completely bypass the defective solar cell 502 because current will flow from the solar cell 102C through the defective cell bypass conductors 506C to the solar cell 102C'. It will be understood that more than one defective solar cell 502 (e.g., two, three, four, five, or more) may be bypassed in this way. Of course, if there are more than three sets of silver hidden contact pads 306 used in the rear surface metallization pattern 300, more than three straight defective cell bypass conductors may be used.

[0046] Fig. 5C shows three super cells 100A, 100B, and 100C, each having one partially bypassed defective solar cell 502 and one or more defective cell bypass conductors 506A-506C. Super cell 100A includes a defective solar cell 502 fourth in series from the top. The defective solar cell 502 in the super cell 100A is partially bypassed by a single defective cell bypass conductor 506A coupled to one set of the hidden tap contact pads 306 of the solar cell 102A and coupled to the hidden tap contact pads 306 of the defective solar cell 502. Electricity is conducted from the solar cell 102A through the single defective cell bypass conductor 506A to the defective solar cell 502. Current then passes from the rear surface metallization pattern 300 of the defective solar cell 502 to the front surface metallization pattern 200 of the solar cell 102A' as discussed herein.

[0047] While Fig. 5C shows the bypass conductor 506A coupled to the set of hidden tap contact pads 306 in the middle of solar cell 102A and coupled to the set of silver hidden contact pads 306 in the middle of defective solar cell 502, it will be understood that any of the sets of hidden tap contact pads 306 of solar cell 102A may be so coupled to any of the sets of hidden tap contact pads 306 of the defective solar cell 502 if the defective cell bypass conductor 506A allows for a current path to bypass the defects(s). Similarly, a pair of defective cell bypass conductors 506B as in super cell 100B or a trio of defective cell bypass conductors 506C as in super cell 100C may be used. Of course, if there are more than three sets of silver hidden contact pads 306 used in the rear surface metallization pattern 300, more than three straight defective cell bypass conductors may be used.

[0048] Fig. 5D shows three super cells 100A, 100B, and 100C, with super cell 100A having a defective solar cell 502 third in series from the top and a single multi-row defective cell bypass conductor 508. The multi-row defective cell bypass conductor 508 may be coupled to the defective solar cell 502 and the solar cells 102 third in series from the top in super cells 100B and 100C via one or more of the sets of silver hidden contact pads 306 of each cell in the row. Accordingly, the multi-row defective cell bypass conductor 508

may create a current path around the defect in the defective solar cell 502, partially bypassing the defective solar cell 502.

[0049] Fig. 5E shows three super cells 100A, 100B, and 100C, with super cell 100A having a defective solar cell 502 fourth in series from the top and a pair of multi-row defective cell bypass conductors 508 and 508'. The multi-row defective cell bypass conductor 508 may be coupled to the solar cells 102A, 102B, and 102C via one or more of the sets of silver hidden contact pads 306 of each cell in the row. The multi-row defective cell bypass conductor 508' may be coupled to the defective solar cell 502 and solar cells 102B' and 102C' via one or more of the sets of silver hidden contact pads 306 of each cell in the row. Accordingly, the multi-row defective cell bypass conductors 508 and 508' may create a current path around the defect in the defective solar cell 502, thereby partially bypassing the defective solar cell 502.

[0050] Fig. 5F shows three super cells 100A, 100B, and 100C, with super cell 100A having a defective solar cell 502 fourth in series from the top and a pair of multi-row defective cell bypass conductors 508 and 508'. The multi-row defective cell bypass conductor 508 may be coupled to the solar cells 102A, 102B, and 102C via one or more of the sets of silver hidden contact pads 306 of each cell in the row. The multi-row defective cell bypass conductor 508' may be coupled to the solar cells 102A', 102B', and 102C' via one or more of the sets of silver hidden contact pads 306 of each cell in the row. Accordingly, the multi-row defective cell bypass conductors 508 and 508' may create a current path around the defect in the defective solar cell 502, thereby completely bypassing the defective solar cell 502.

[0051] Fig. 5G shows three super cells 100A, 100B, and 100C, with super cell 100A having a defective solar cell 502 fourth in series from the top and a multi-row defective cell bypass conductor 510. The multi-row defective cell bypass conductor 510 may be made of a single, continuous piece of conductive material or made of multiple pieces of conductive material that are conductively bonded to each other (e.g., by welding, by electrically conductive

adhesive). The multi-row defective cell bypass conductor 510 may be coupled to the solar cells 102A, 102B, and 102C via one or more of the sets of silver hidden contact pads 306 of each cell in the row. The multi-row defective cell bypass conductor 510 may also be coupled to the defective solar cell 502 and solar cells 102B' and 102C' via one or more of the sets of silver hidden contact pads 306 of each cell in the row. Additionally, it will be understood that the multi-row defective cell bypass conductor 510 could be coupled to the solar cells 102A'', 102B'', and 102C'' instead of to the defective solar cell 502 and solar cells 102B' and 102C' as shown. In such an embodiment, the defective solar cell 502 would be completely rather than partially bypassed. Accordingly, the multi-row defective cell bypass conductor 510 may create a current path around the defect in the defective solar cell 502, either by partially or completely bypassing defective solar cell 502.

[0052] Fig. 5H shows three super cells 100A, 100B, and 100C, with super cell 100A having a defective solar cell 502 fourth in series from the top and a multi-row defective cell bypass conductor 512. The multi-row defective cell bypass conductor 514 may be made of a single, continuous piece of conductive material or made of multiple pieces of conductive material that are conductively bonded to each other (e.g., by welding, by electrically conductive adhesive). The multi-row defective cell bypass conductor 512 may be coupled to solar cells 102A, 102B, 102C, and 102A' as shown in Fig. 5H via one or more of the sets of silver hidden contact pads 306 of each cell. Accordingly, the multi-row defective cell bypass conductor 512 may create a current path around the defect in the defective solar cell 502 by completely bypassing the defective solar cell 502 and the other solar cells 102 fourth in series from the top of super cells 100B and 100C.

[0053] Fig. 5I shows three super cells 100A, 100B, and 100C, with super cell 100A having a defective solar cell 502 fourth in series from the top and a multi-row defective cell bypass conductor 514. The multi-row defective cell bypass conductor 514 may be coupled to solar cells 102A, 102B, 102C, 102A' and defective solar cell 502 via one or more of the sets of silver hidden

contact pads 306 of each cell. Accordingly the multi-row defective cell bypass conductor 514 may create a current path around the defect in the defective solar cell 502 by partially bypassing the defective solar cell 502.

[0054] Fig. 6 shows examples of solar modules with one or more bypassed solar cells 102 and/or defective solar cells 502. In Fig. 6, bypassed solar cells solar cells 102 and/or defective solar cells 502 are represented by being blacked out. Solar module 600 has one partially bypassed defective solar cell 502 in the second super cell 100 from the left. Solar module 602 has an entire partially bypassed row of solar cells 102 including one or more defective solar cells 502. The solar module 602 could comprise super cells 100 each having an individually partially bypassed defective solar cell 502 (i.e., bypassed with a defective cell bypass 504B) or with a multi-row defective cell bypass 510 that partially bypasses an entire row of defective solar cells 502 and/or solar cells 102. Solar module 604 has a completely bypassed defective solar cell 502 and a bypassed solar cell 102 in the second super cell 100 from the left. Solar module 606 has two bypassed rows of solar cells 102 including one or more defective solar cells 502. The solar module 606 could comprise super cells 100 each having a completely bypassed defective solar cell 502 (i.e., bypassed with a defective cell bypass 504A) or with a multi-row defective cell bypass 510 that completely bypasses two entire rows of defective solar cells 502 and/or solar cells 102. Solar module 608 has one partially bypassed defective solar cell 502 in each super cell 100 in different rows. Solar module 610 includes two partially bypassed defective solar cells 502 in each super cell 100 in different rows. Similarly, a solar module can include super cells 100 that include more than two (e.g., three, four, or more) partially bypassed or completely bypassed defective solar cells 502 in different rows. Such defective solar cells 502 may be partially bypassed or completely bypassed. As discussed below, bypassing a defective solar cell 502 or a solar cell 102 will decrease the power output of a super cell 100 and will also decrease the voltage of the maximum power point (MPP) of that super cell 100.

[0055] Figs. 7A-7C show example IV (current-voltage) curves of solar modules with one or more completely bypassed solar cells 102 and/or defective solar cells 502. The X-axis of each curve represents the voltage of the solar module and the Y-axis of each curve represents the current output of the solar module at the voltage on the X-axis. Each IV curve includes a (reference) IV curve of a solar module with no bypassed solar cells 102 and an IV curve of a solar module with one or more defective solar cells 502 and/or bypassed solar cells 102. The dot on each IV curve represents the MPP of the solar module to which the IV curve corresponds. The MPP is the voltage at which the solar module generates the maximum amount of power under current operating conditions. In some embodiments, a controller may be coupled to the solar module and may adjust the operating voltage of the solar module to maintain maximum power (“MPP tracking”) generation under changing conditions.

[0056] Graph 700 (Fig. 7A) shows an IV curve 702 of a reference solar module with no bypassed solar cells 102 and IV curve 704 of a solar module 600 (Fig. 6) with one bypassed defective solar cell 502. As can be seen from the graph 700, the MPP voltage of the curve 704 is slightly lower than the MPP voltage of the curve 702. Because the current through the solar module at the MPP voltage for both the reference solar module and the solar module 600 is the same (e.g., to prevent damaging a super cell 100 with too much current flow), the total power generated by the solar module 600 at MPP voltage will be lower than the reference solar module. It will also be understood that because the super cells 100 of the solar module 600 are in parallel, the voltage across each super cell 100 (including the super cell 100 with the completely bypassed defective solar cell 502) will be identical. Accordingly, the MPP voltage for the super cell 100 with the bypassed defective solar cell 502 will also be the voltage across the other super cells 100, resulting in a reduction of power generation potential by the other super cells 100.

[0057] Graph 706 (Fig. 7B) shows the IV curve 702 of a reference solar module with no bypassed solar cells 102 and an IV curve 708 of a solar module 602 or 608 (Fig. 6) with one bypassed solar cell (either solar cells 102 or defective solar cells 502) in each super cell 100. As can be seen from the graph 706, the MPP voltage of the curve 708 is lower than the MPP voltage of the curve 702 by a larger amount than the curve 704 from graph 700. Because the current through the solar module at the MPP voltage for both the reference solar module and the solar module 602 or 608 is the same, the total power generated by the solar module 602 or 608 at the MPP voltage will be lower than the reference solar module. However, because there is a bypassed solar cell in each super cell 100 of the solar modules 602 and 608, the MPP voltage for each super cell 100 in the solar modules 602 and 608 is identical. Accordingly, there is no reduction of power generation potential of super cells not including a bypassed solar cell, as occurs in the solar module 600.

[0058] Graph 710 (Fig. 7C) shows the IV curve 702 of a reference solar module with no bypassed solar cells 102 and an IV curve 712 of a solar module 604 (Fig. 6) with two bypassed defective solar cell 502 in a single super cell 100. As can be seen from the graph 710, the MPP voltage of the curve 712 is lower than the MPP voltage of the curve 702. Because the current through the solar module at the MPP voltage for both the reference solar module and the solar module 604 is the same, the total power generated by the solar module 604 at the MPP voltage will be lower than the reference solar module. It will also be understood that because the super cells 100 of the solar module 604 are in parallel, the voltage across each super cell 100 (including the super cell 100 with the two completely bypassed defective solar cells 502) will be identical. Accordingly, the MPP voltage for the super cell 100 with the two bypassed defective solar cells 502 will also be the voltage across the other super cells 100, resulting in a reduction of power generation potential by the other super cells 100.

[0059] Graph 714 (Fig. 7D) shows the IV curve 702 of a reference solar module with no bypassed solar cells 102 and IV curve 708 of a solar

module 606 or 610 (Fig. 6) with two bypassed solar cell (either solar cells 102 or defective solar cells 502) in each super cell 100. As can be seen from the graph 714, the MPP voltage of the curve 708 is lower than the MPP voltage of the curve 702 by a larger amount than the curve 704 from graphs 700, 706, and 710. Because the current through the panel at the MPP voltage for the both the reference solar module and the solar module 606 or 610 is the same, the total power generated by the solar module 606 or 610 at the MPP voltage will be lower than the reference solar module. However, because there is a bypassed solar cell in each super cell 100 of the solar modules 606 and 610, the MPP voltage for each super cell 100 in the solar modules 606 and 610 is identical. Accordingly, there is no reduction of power generation potential of super cells not including a bypassed solar cell, as occurs in the solar module 600 or 604.

[0060] Referring now to Fig. 8A, a block diagram shows an inspection and reworking method 800 for reworking a super cell 100 with one or more defective solar cells. Before the method 800 commences, a number of super cells 100 are assembled. Once a number of super cells 100 (e.g., six super cells 100) are assembled, the super cells 100 may be inspected to determine whether there are one or more defective solar cells 502 in the super cell 100 at block 802. The inspection may include one or more of visual inspection, electroluminescence testing, or photoluminescence testing. A visual inspection may be performed by a human technician examining the super cell 100 to identify one or more defective solar cells 502. Additionally or alternatively, visual inspection may be performed by a computer coupled to a camera. Electroluminescence testing may include applying a charge to a super cell 100 and measuring (e.g., with a computer coupled to a camera, with a human technician) the light pattern emitted by the super cell 100 to identify one or more defects. Photoluminescence testing may include applying a homogenous wavelength of light (e.g., 800 nm red light) to the super cell 100 and measuring (e.g., with a computer coupled to a camera, with a human

technician) the light pattern emitted by the super cell 100 to identify one or more defects.

[0061] If no defects are detected, the super cells 100 may be assembled into a solar module 400 at block 804. Alternatively, if one or more defective solar cells 502 are detected in the super cell 100, the super cell 100 may be set aside to be assembled into a solar module 400 comprising super cells 100 with defective solar cells 502 on the same rows (e.g., each super cell 100 has a defective solar cell 502 fifth in series from the top and a defective solar cell 502 twentieth in series from the top).

[0062] The solar module 400 may then be inspected to determine whether there are defective solar cells 502 in the solar modules. As discussed herein, the inspection may be one or more of visual inspection or electroluminescence testing. If one or more defective solar cells 502 are detected, at block 808 one or more bypass conductors (e.g., 504A-504C, 506A-506C, 508, 510, 512, or 514) may be applied to the rear surface of super cell(s) 100 to bypass (partially or completely) the defective solar cells 502 in accordance with the present disclosure.

[0063] Referring now to Fig. 8B, a block diagram shows an inspection and reworking method 820 for reworking a super cell 100 with one or more defective solar cells 502. In contrast to the method 800, the method 820 may include reworking super cells 100 by installing one or more bypass conductors (e.g., 504A-504C, 506A-506C, 508, 510, 512, or 514) before assembling the super cells 100 into a solar module 400. Accordingly, at block 822 the super cells 100 are inspected (e.g., visual inspection, electroluminescence testing) and defective solar cells 502 are identified. At block 824, one or more bypass conductors may be applied to the rear surface of super cell(s) 100 to bypass (partially or completely) the defective solar cells 502 in accordance with the present disclosure. At block 826, the super cells 100 may be assembled into a solar module 400. As discussed herein, a solar module 400 may include super cells 100 with bypassed defective solar cells 502 and super cells 100 with no bypassed solar cells (e.g., the solar modules 600 or 604). Alternatively, a solar

module 400 may include only super cells with bypassed defective solar cells 502 (e.g., solar modules 608 or 610).

[0064] Although specific embodiments have been described above, these embodiments are not intended to limit the scope of the present disclosure, even where only a single embodiment is described with respect to a particular feature. Examples of features provided in the disclosure are intended to be illustrative rather than restrictive unless stated otherwise. The above description is intended to cover such alternatives, modifications, and equivalents as would be apparent to a person skilled in the art having the benefit of this disclosure.

[0065] The scope of the present disclosure includes any feature or combination of features disclosed herein (either explicitly or implicitly), or any generalization thereof, whether or not it mitigates any or all of the problems addressed herein. Accordingly, new claims may be formulated during prosecution of this application (or an application claiming priority thereto) to any such combination of features. In particular, with reference to the appended claims, features from dependent claims may be combined with those of the independent claims and features from respective independent claims may be combined in any appropriate manner and not merely in the specific combinations enumerated in the appended claims.

CLAIMS

What is claimed is:

1. A photovoltaic module comprising:
 - a first super cell comprising a plurality of solar cells, each having a rear surface, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein at least one of the plurality of solar cells is a first defective solar cell; and
 - a bypass conductor coupled to the rear surface of a first solar cell in the first super cell and coupled to the rear surface of a second solar cell in the first super cell disposed after the first defective solar cell in series,
 - wherein the bypass conductor is adapted to bypass the first defective solar cell by conducting electricity from the rear surface of the first solar cell in the first super cell to the rear surface of the second solar cell in the first super cell.
2. The photovoltaic module of claim 1 wherein the first solar cell in the first super cell is the first defective solar cell.
3. The photovoltaic module of claim 1 wherein the first solar cell in the first super cell is disposed before the first defective solar cell in series.
4. The photovoltaic module of any of claims 1-3 wherein the first defective solar cell includes one or more of a crack in the first defective solar cell, a chip in the first defective solar cell, a malformed front surface metallization pattern on the first defective solar cell, a malformed rear surface metallization pattern on the first defective solar cell, an electrical short on the first defective solar cell, an out-of-specification voltage of the first defective solar cell, an out-of-specification power output of the first defective solar cell, poor electrical conductivity from the first defective solar cell to another solar cell.
5. The photovoltaic module of any of claims 1-4:

wherein the rear surface of the first solar cell in the first super cell comprises a first plurality of sets of hidden tap contact pads,

wherein the rear surface of the second solar cell in the first super cell comprises a second plurality of sets of hidden tap contact pads, and

wherein the bypass conductor is coupled to at least one of the first plurality of sets of hidden tap contact pads and at least one of the second plurality of sets of hidden tap contact pads.

6. The photovoltaic module of claim 5 wherein the bypass conductor is coupled to more than one of the first plurality of sets of contact pads and more than one of the second plurality of contact pads.
7. The photovoltaic module of claim 5 wherein the bypass conductor comprises multiple pieces such that the bypass conductor does not couple any of the first plurality of sets of contact pads together.
8. The photovoltaic module of claim 5 wherein the bypass conductor comprises multiple pieces such that the bypass conductor does not couple any of the second plurality of sets of contact pads together.
9. The photovoltaic module of any of claims 1-8 further comprising:
 - a second super cell comprising a plurality of solar cells, each having a rear surface, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein at least one of the plurality of solar cells is a second defective solar cell; and
 - a second bypass conductor coupled to the rear surface of a first solar cell in the second super cell and coupled to the rear surface of a second solar cell in the second super cell disposed after the second defective solar cell in series,
 - wherein the second bypass conductor is adapted to bypass the second defective solar cell by conducting electricity from the rear surface of the first solar cell in the second super cell to the rear surface of the second solar cell in the second super cell, and
 - wherein the first super cell and the second super cell are coupled in electrical parallel.

10. The photovoltaic module of any of claims 1-8 further comprising:

a second super cell comprising a plurality of solar cells, each having a rear surface, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series,

wherein the first super cell and the second super cell are connected in electrical parallel and are disposed side-by-side such that each of the plurality of solar cells of the first super cell is aligned in a row with a solar cell in the plurality of solar cells of the second super cell, and

wherein the bypass conductor is further coupled to the rear surface of a first solar cell in the second super cell and coupled to the rear surface of a second solar cell in the second super cell disposed after the first solar cell in the second super cell in series.

11. An apparatus comprising:

a plurality of solar cells, including a first solar cell and a second solar cell, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein at least one of the plurality of solar cells is a defective solar cell, wherein each solar cell has a rear surface having a plurality of sets of contact pads;

a bypass conductor coupled to at least one of the plurality of sets of contact pads of the first solar cell and at least one of the plurality of sets of contact pads of the second solar cell, wherein the bypass conductor bypasses the defective solar cell.

12. The apparatus of claim 11 wherein the first solar cell is the defective solar cell and the second solar cell is disposed after the defective solar cell in series.

13. The apparatus of claim 11 wherein the first solar cell is disposed before the defective solar cell in series and the second solar cell is disposed after the defective solar cell in series.

14. The apparatus of any of claims 11-13 wherein the defective solar cell includes one or more of a crack in the first defective solar cell, a chip in the first defective solar cell, a malformed front surface metallization

pattern on the first defective solar cell, a malformed rear surface metallization pattern on the first defective solar cell, an electrical short on the first defective solar cell, an out-of-specification voltage of the first defective solar cell, an out-of-specification power output of the first defective solar cell, poor electrical conductivity from the first defective solar cell to another solar cell.

15. The apparatus of any of claims 11-14 wherein the bypass conductor is coupled to more than one of plurality of sets of contact pads of the first solar cell and more than one of the plurality of sets of contact pads of the second solar cell.

16. The apparatus of any of claims 11-14 wherein the bypass conductor comprises multiple pieces such that the bypass conductor does not couple any of the plurality of sets of contact pads of the first solar cell together.

17. The apparatus of any of claims 11-14 wherein the bypass conductor comprises multiple pieces such that the bypass conductor does not couple any of the plurality of sets of contact pads of the second solar cell together.

18. An apparatus comprising:

a plurality of solar cells, including a first solar cell and a second solar cell, arranged with sides of adjacent solar cells overlapping in a shingled manner and conductively bonded to each other in series wherein one of more of the plurality of solar cells is one or more defective solar cells, wherein each solar cell has a rear surface; and

a bypass conductor coupled to the rear surface of the first solar cell and coupled to the rear surface of the second solar cell;

wherein the bypass conductor is adapted to short circuit the one or more defective solar cells by conducting electricity from the rear surface of the first solar cell to the rear surface of the second solar cell.

19. The apparatus of claim 18 wherein the first solar cell in the plurality of solar cells is a first defective solar cell of the one or more defective

solar cells and the second solar cell is disposed after the first defective solar cell in series.

20. The apparatus of claim 18 wherein the first solar cell is disposed before the one or more defective solar cells in series and the second solar cell is disposed after the one or more defective solar cells in series.

1/13

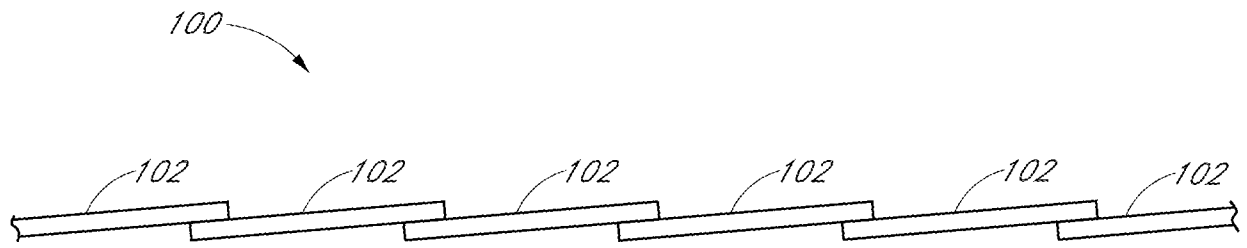


FIG. 1

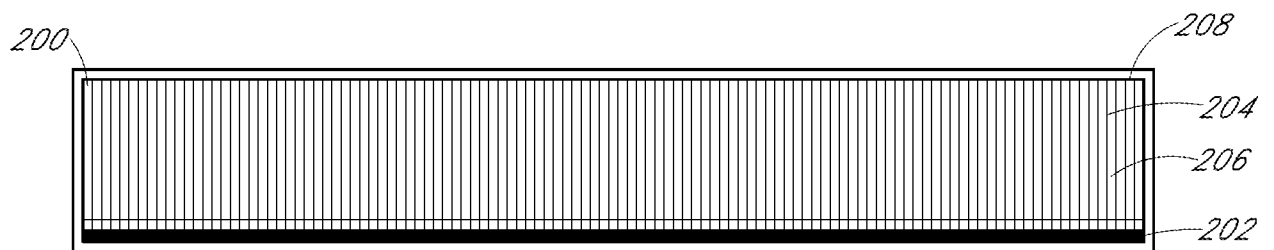


FIG. 2

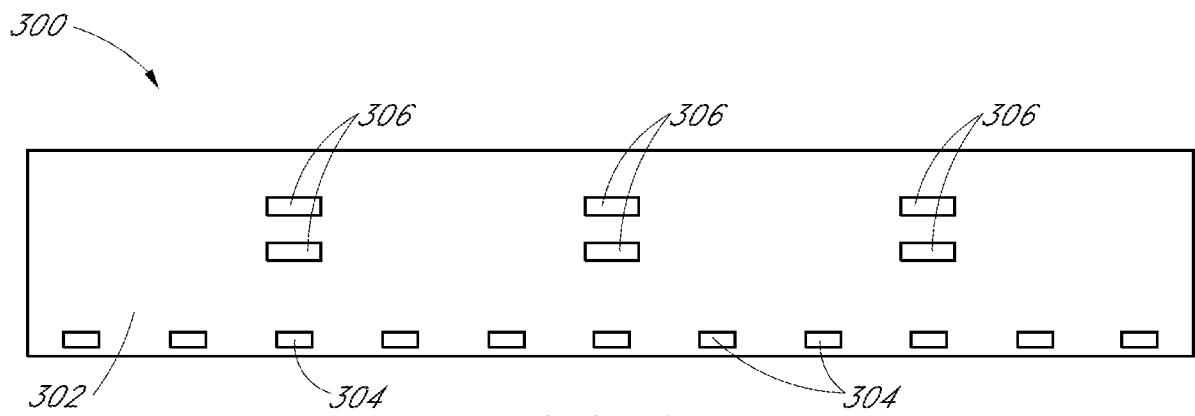


FIG. 3

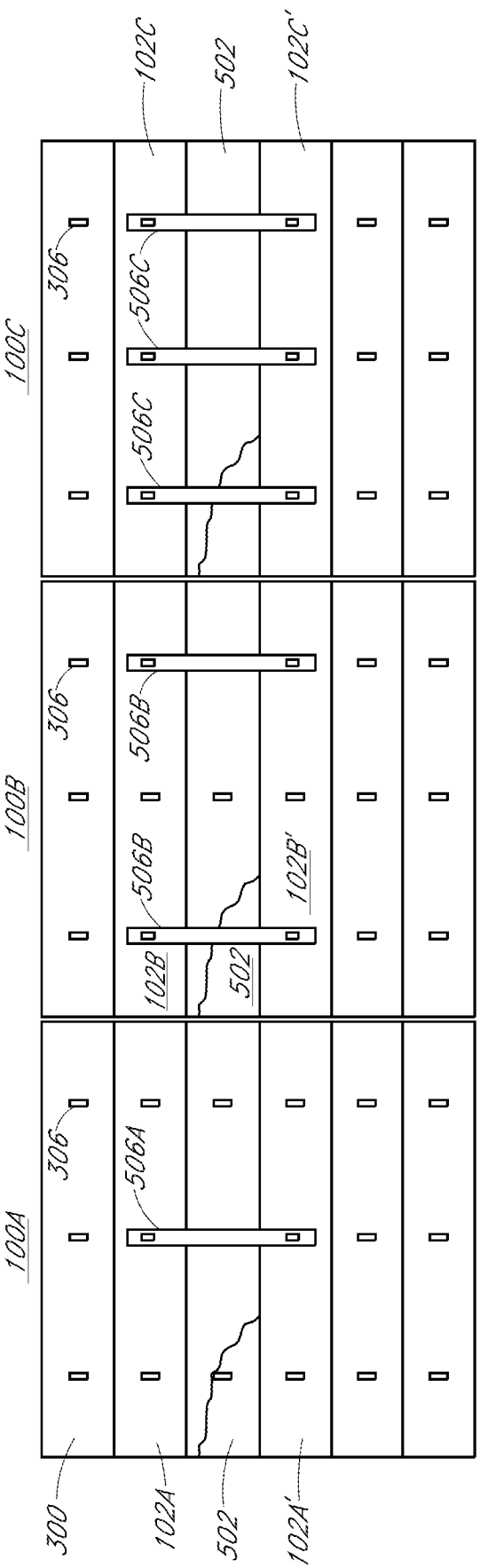
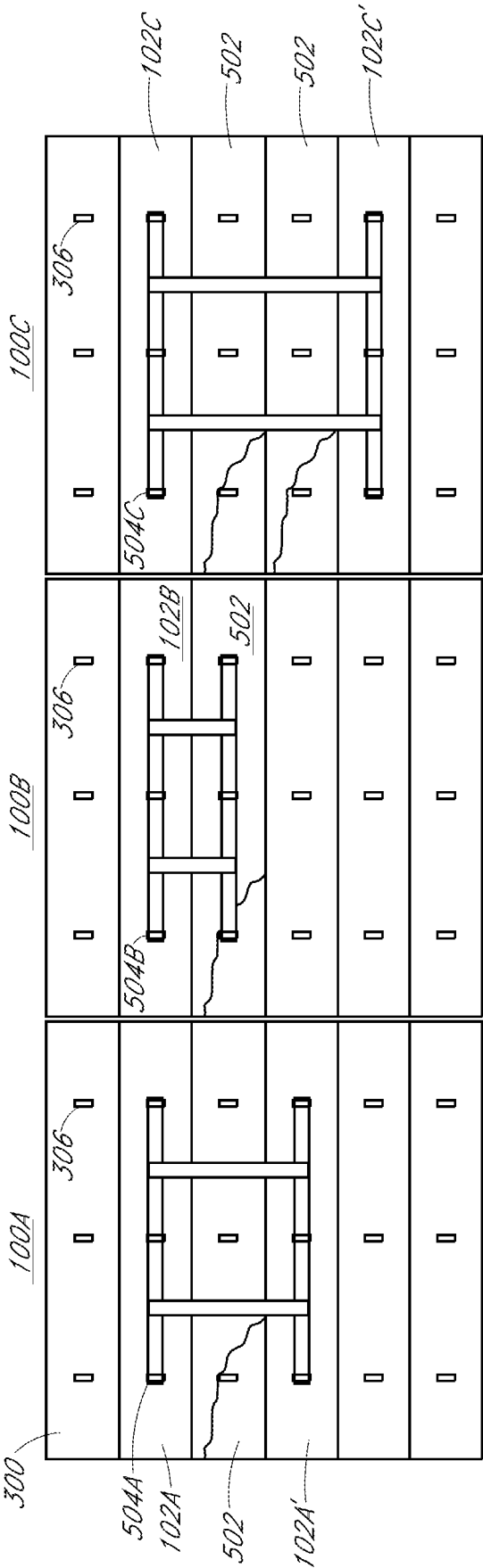
400 

402

-100

-402

FIG. 4



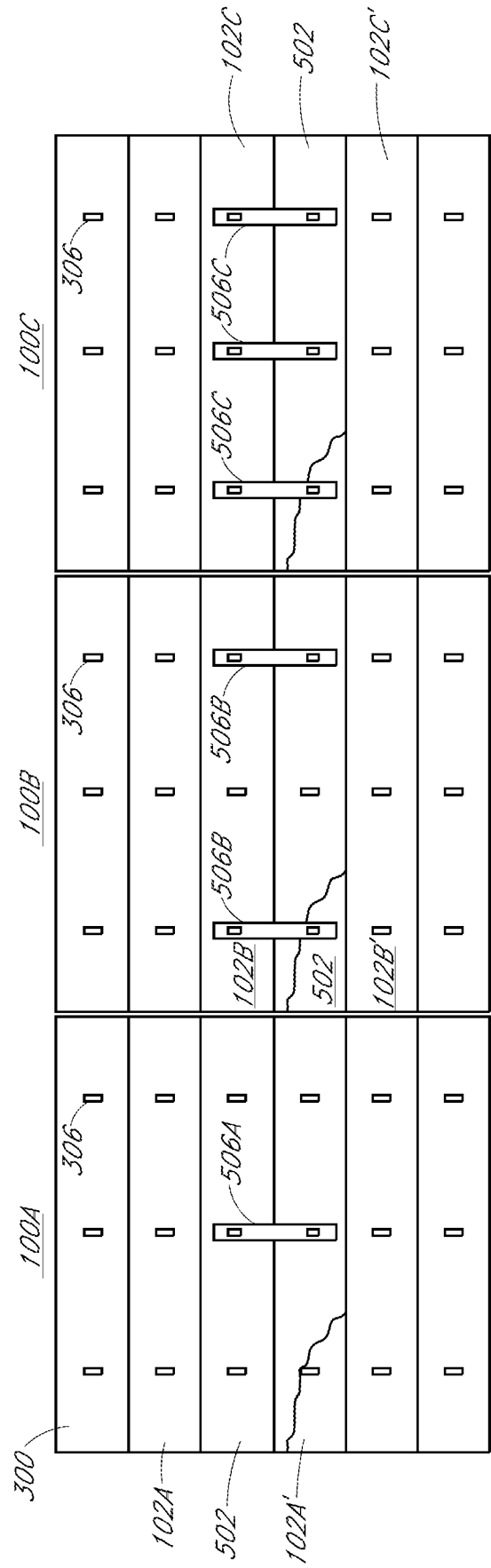


FIG. 5C

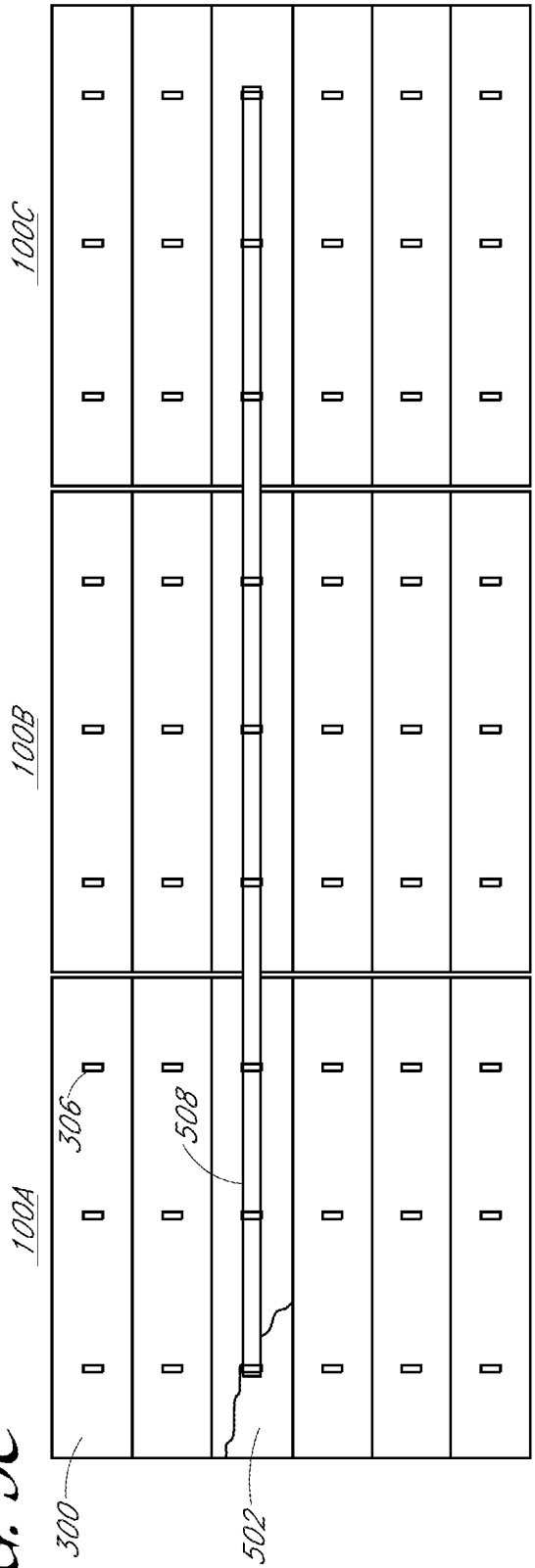


FIG. 5D

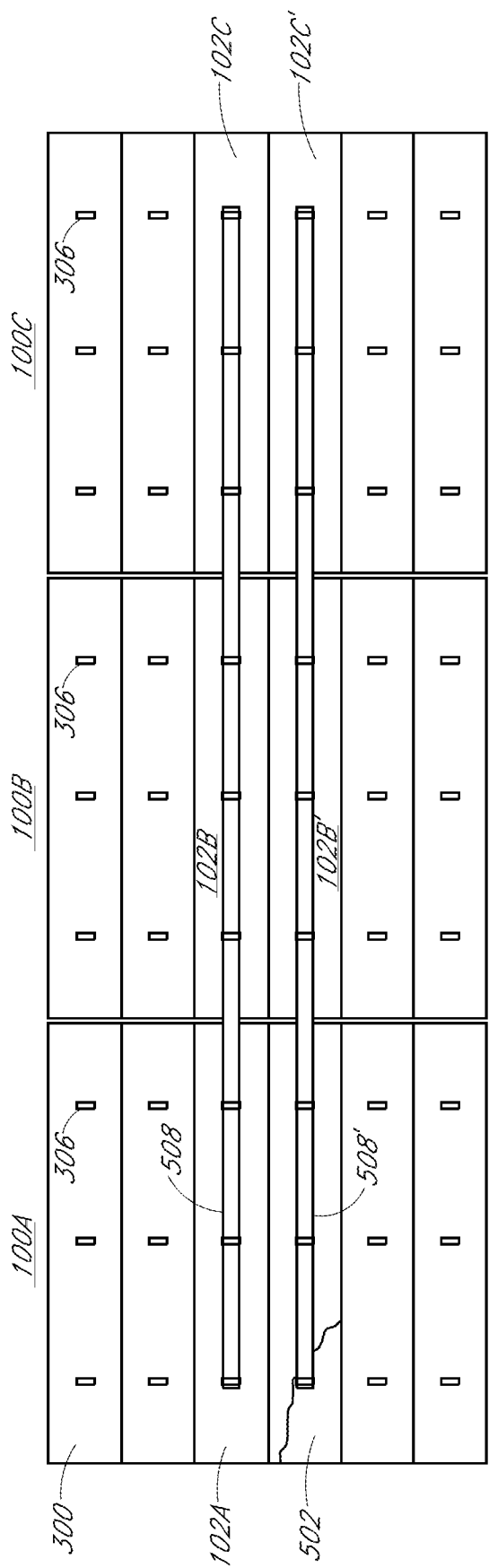


FIG. 5E

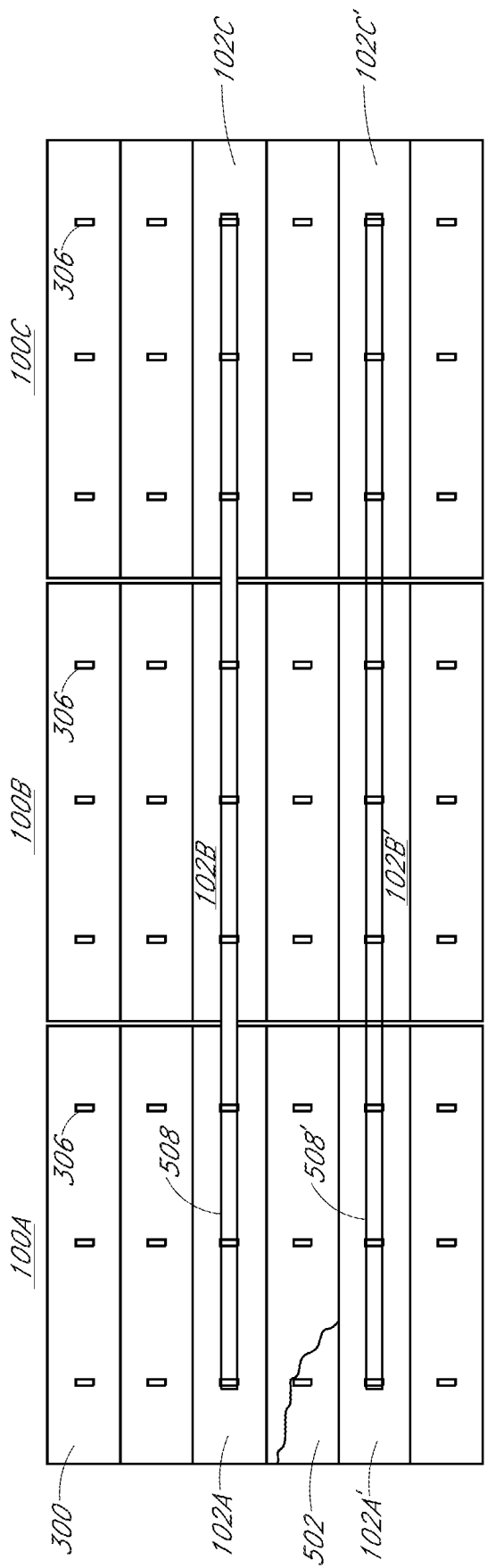
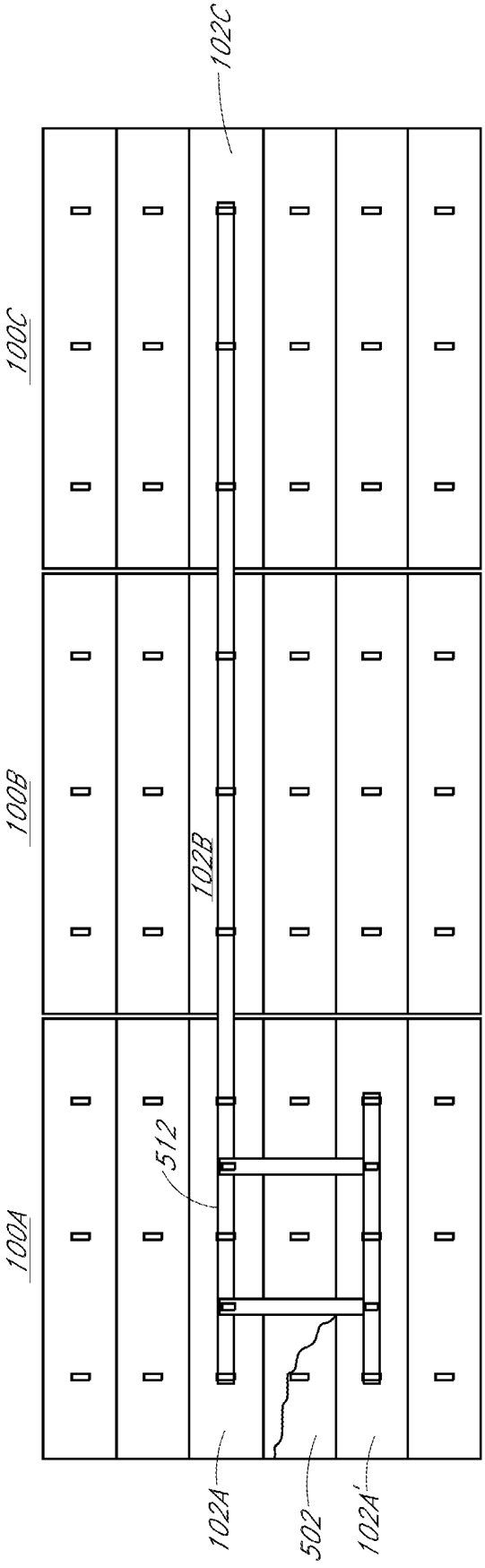
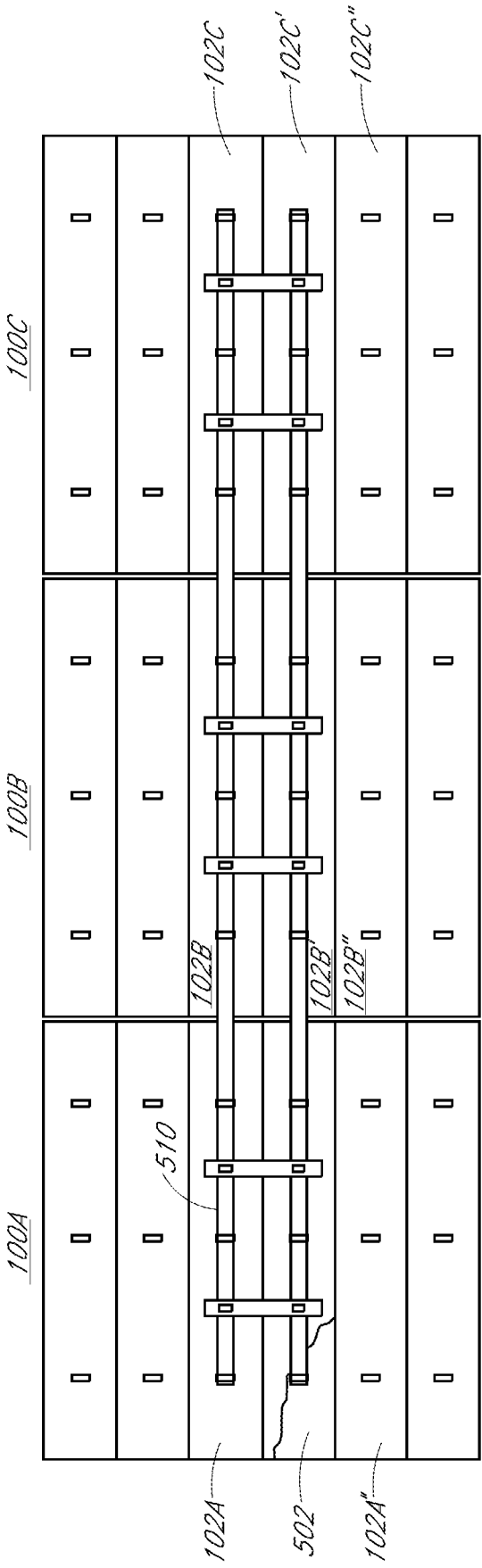


FIG. 5F



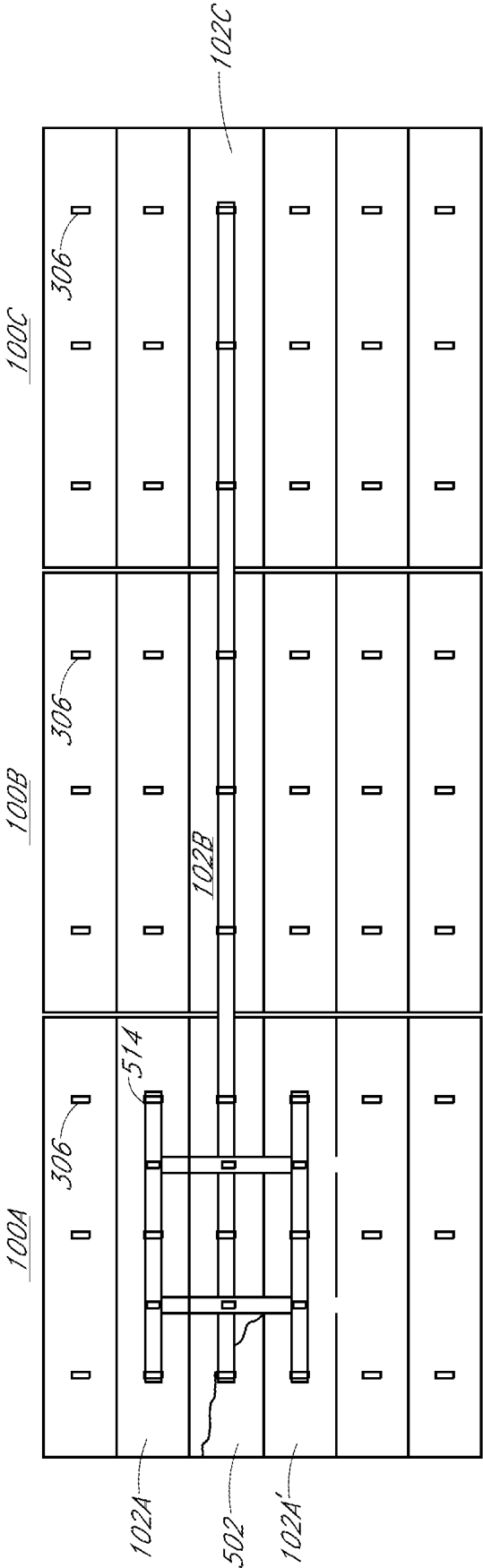
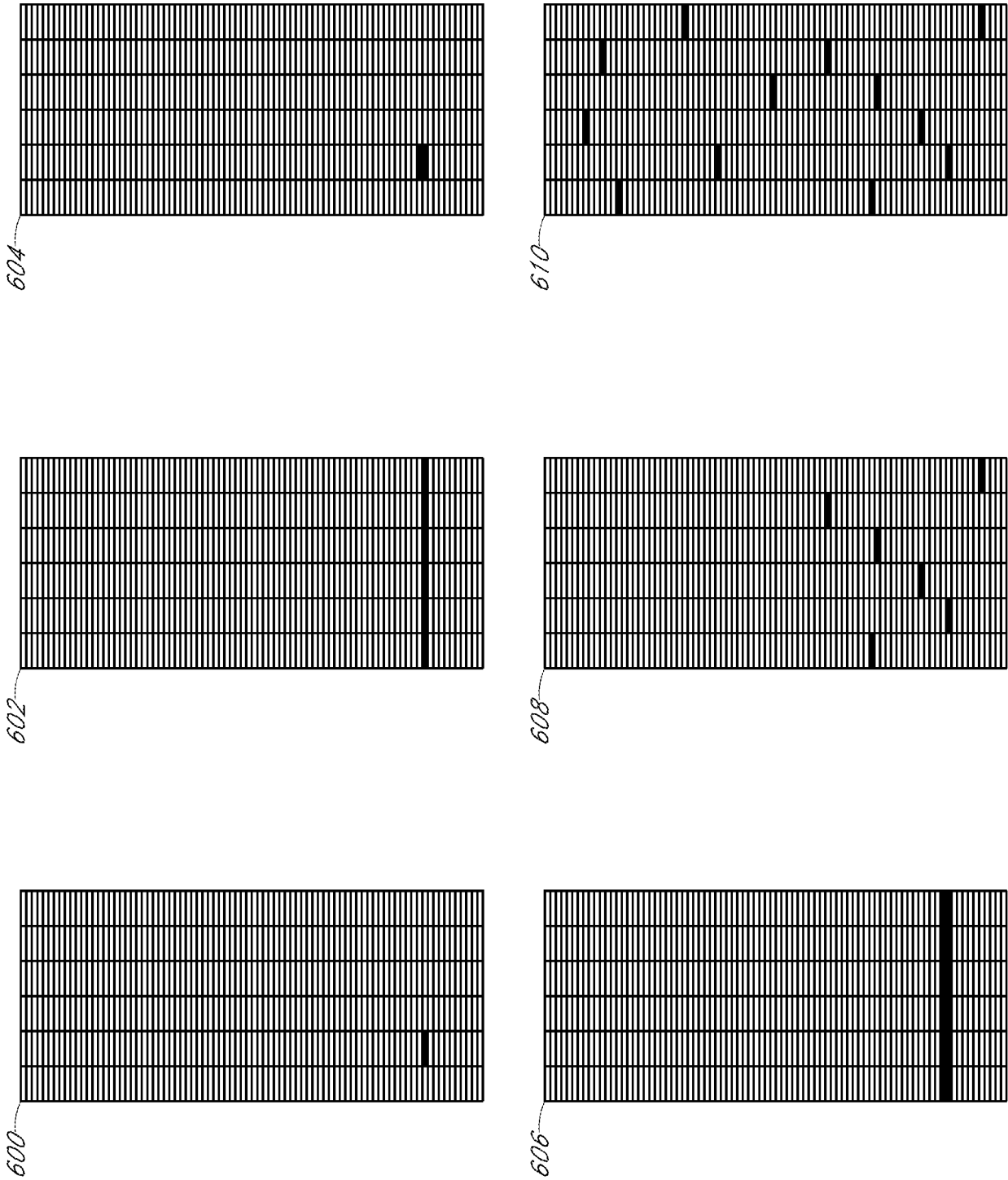


FIG. 51

FIG. 6



700

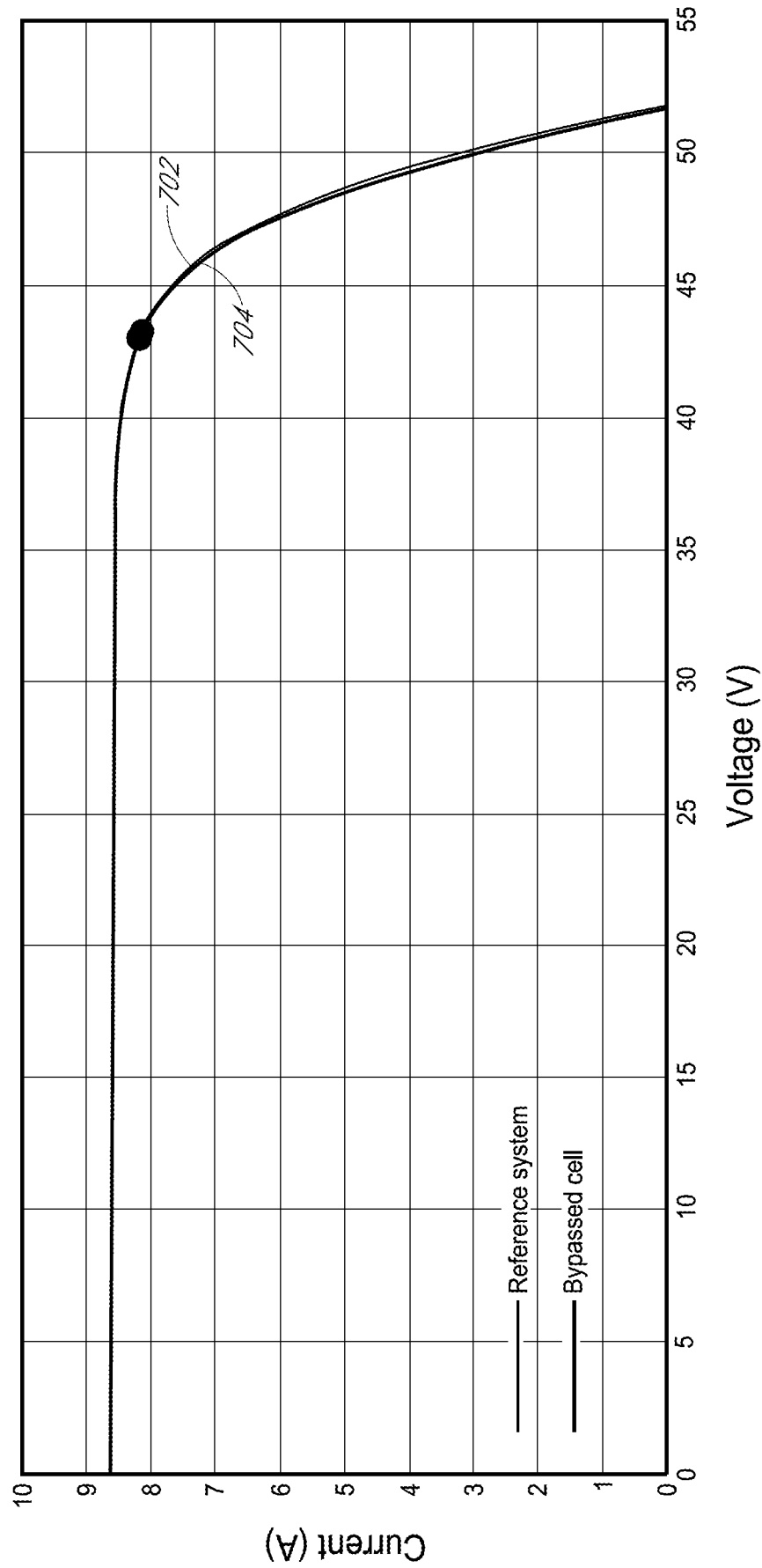


FIG. 7A

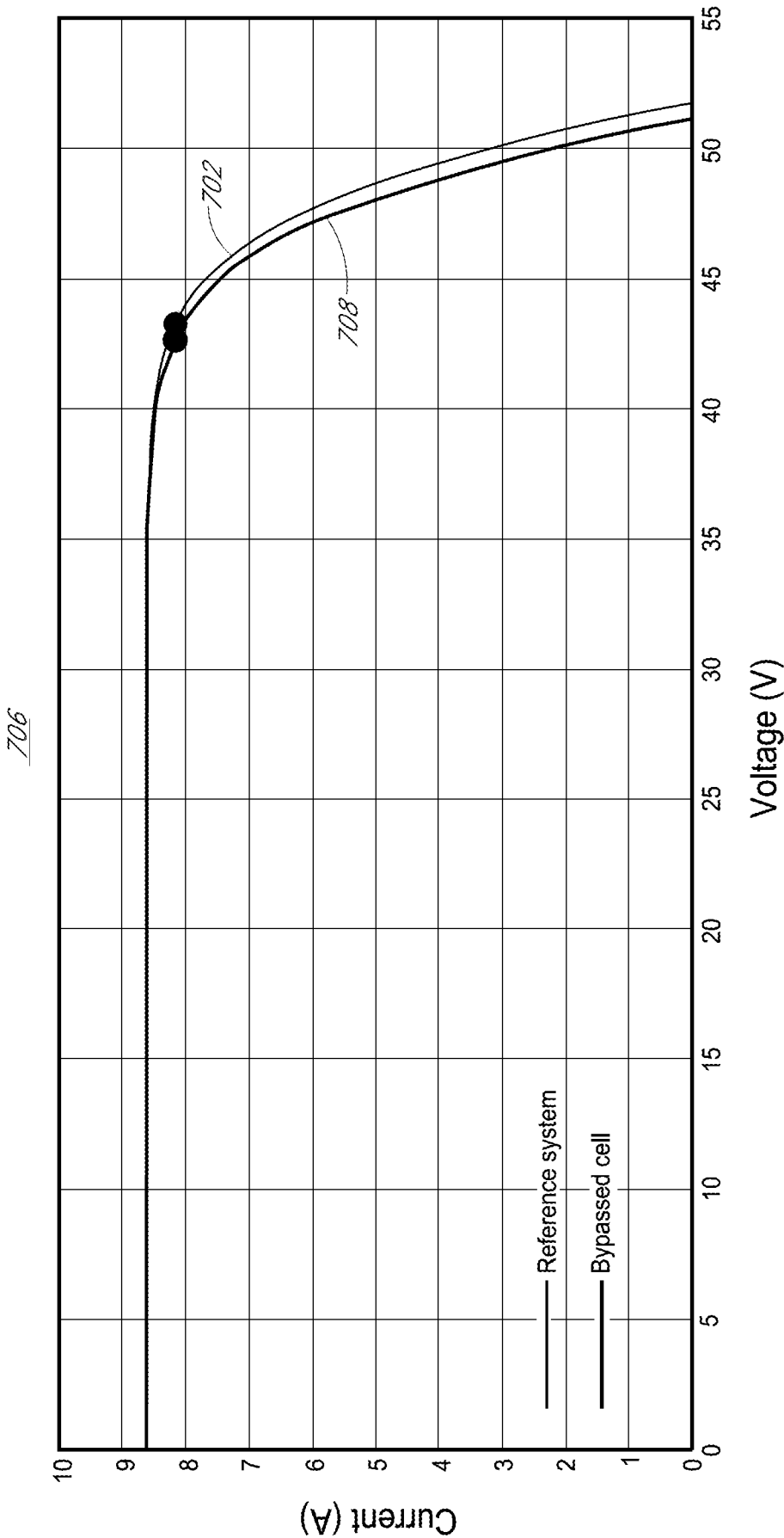


FIG. 7B

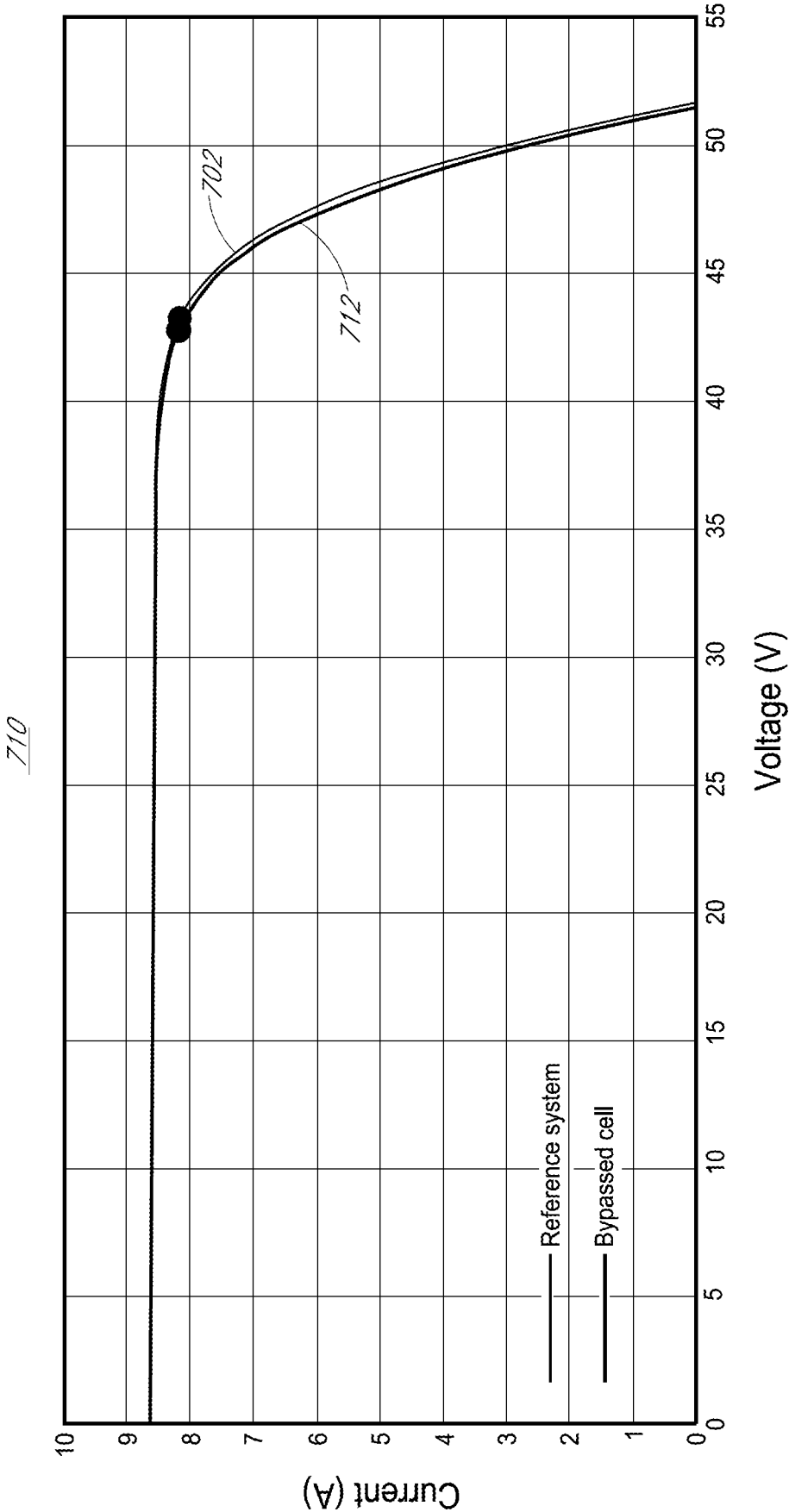


FIG. 7C

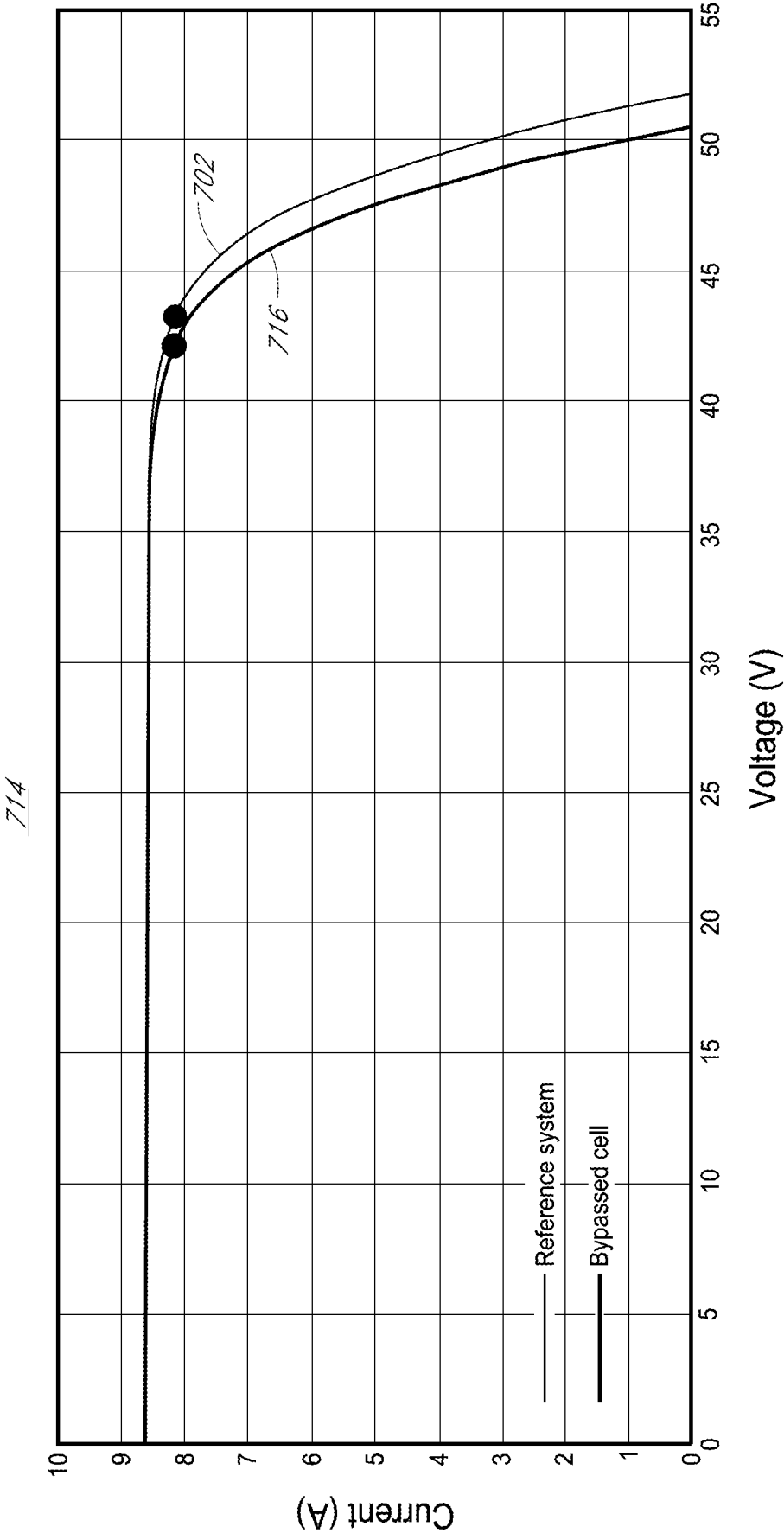
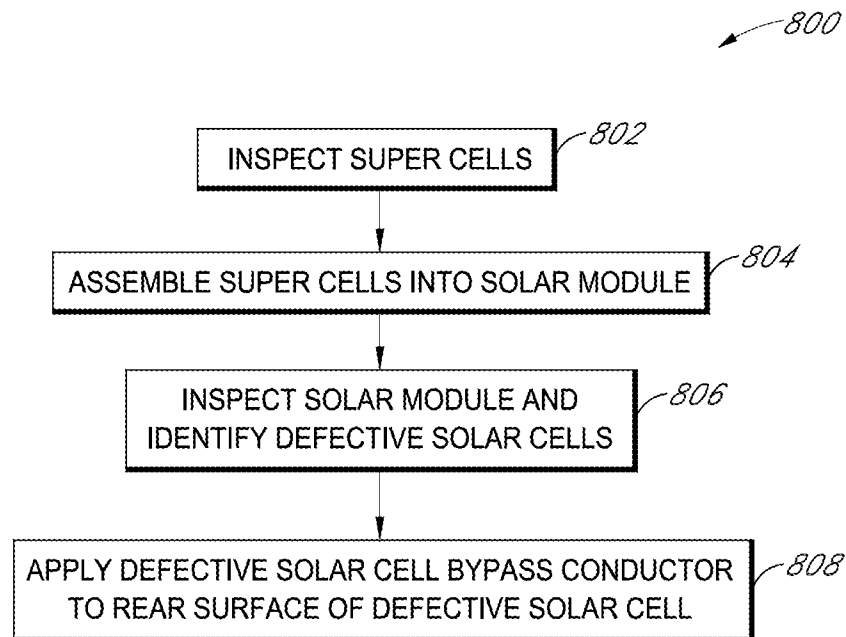
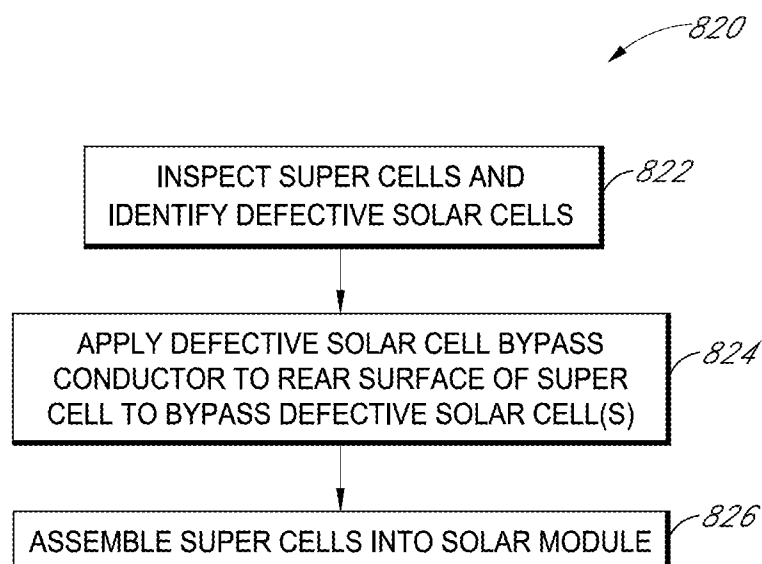


FIG. 7D

13/13

*FIG. 8A**FIG. 8B*

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US2016/047624

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:
3. ☒ Claims Nos.: 5-10, 15-17
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of additional fees.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☐ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- ☐ The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- ☐ No protest accompanied the payment of additional search fees.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US2016/047624

A. CLASSIFICATION OF SUBJECT MATTER IPC(8) - H01L 31/05; H01L 31/18; H02S 40/36 (2016.01) CPC - H01L 31/0516; H01L 31/042; H01L 31/0504 (2016.08) According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC - H01L 31/05; H01L 31/18; H02S 40/36 CPC - H01L 31/0516; H01L 31/042; H01L 31/0504 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched USPC:136/244, 246, 251, 256; 438/59, 66 (keyword delimited) Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Orbit, Google Patents, Google Scholar Search terms used: solar cells, bypass conductor, rear surface, defective cells, back surface, shingle, series, photovoltaic		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2009/0229596 A1 (SHIN et al) 17 September 2009 (17.09.2009) entire document	1-4, 11-14, 18-20
Y	US 2014/0124014 A1 (COGENRA SOLAR, INC.) 08 May 2014 (08.05.2014) entire document	1-4, 11-14, 18-20
A	US 2013/0206221 A1 (GANNON et al) 15 August 2013 (15.08.2013) entire document	1-4, 11-14, 18-20
A	US 2012/0161801 A1 (HASEGAWA) 28 June 2012 (28.06.2012) entire document	1-4, 11-14, 18-20
A	US 6,476,313 B2 (KAWANO) 05 November 2002 (05.11.2002) entire document	1-4, 11-14, 18-20
☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search 12 October 2016		Date of mailing of the international search report 27 OCT 2016
Name and mailing address of the ISA/ Mail Stop PCT, Attn: ISA/US, Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450 Facsimile No. 571-273-8300		Authorized officer Blaine R. Copenheaver PCT Helpdesk: 571-272-4300 PCT OSP: 571-272-7774