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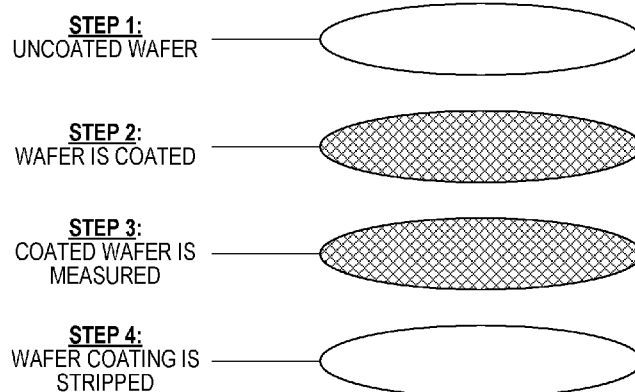
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(54) Title: REMOVABLE OPAQUE COATING FOR ACCURATE OPTICAL TOPOGRAPHY MEASUREMENTS ON TOP SURFACES OF TRANSPARENT FILMS



WAFER MEASURING PROCESS

FIG. 3

(57) Abstract: A method of using removable opaque coating for accurate optical topography measurements on top surfaces of transparent films includes: depositing a highly reflective coating onto a top surface of a wafer, measuring topography on the highly reflective coating, and removing the highly reflective coating from the wafer. The highly reflective coating includes an organic material. The highly reflective coating comprises a refractive index value between one and two. The highly reflective coating comprises a complex wavelength greater than one at six-hundred and thirty-five nanometers. The highly reflective coating reflects at least twenty percent of incident light. The highly reflective coating when deposited maintains an underlayer pattern topography at a resolution of forty by forty micrometers. The highly reflective coating does not cause destructive stress to the wafer.



(84) **Designated States** (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

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## **REMOVABLE OPAQUE COATING FOR ACCURATE OPTICAL TOPOGRAPHY MEASUREMENTS ON TOP SURFACES OF TRANSPARENT FILMS**

### **CROSS REFERENCE TO RELATED APPLICATIONS**

**[0001]** This application claims priority under 35 U.S.C. §119 from U.S. Provisional Application Number 62/748,300, entitled “REMOVABLE OPAQUE COATING FOR ACCURATE OPTICAL TOPOGRAPHY MEASUREMENTS ON TOP SURFACES OF TRANSPARENT FILMS,” filed on October 19, 2018, the subject matter of which is incorporated herein by reference.

### **TECHNICAL FIELD**

**[0002]** The described embodiments relate generally to optical topography measurement and more particularly to improved optical topography measurements on top surfaces of transparent films using removable opaque coating.

### **BACKGROUND INFORMATION**

**[0003]** Optical topography measurements on transparent films on semiconductor wafers are impacted by transparent films effects affecting the accuracy of the measurement. Existing techniques used to compensate for transparent films effects are based on correction models which require additional information about the optical properties and thickness of the film(s). If this additional information about films properties is incorrect or incomplete, the correction will be inaccurate. While correction models may be useful in experimental or engineering analysis with well-known actual optical films properties, they are too complex and too time consuming for production metrology in high volume manufacturing environments with varying actual films properties.

## SUMMARY

[0004] In a first novel aspect, a method of using removable opaque coating for accurate optical topography measurements on top surfaces of transparent films includes: depositing a highly reflective coating onto a top surface of a wafer, measuring topography on the highly reflective coating, and removing the highly reflective coating from the wafer.

[0005] In a second novel aspect, the highly reflective coating includes an organic material.

[0006] In a third novel aspect, the highly reflective coating comprises a refractive index value between one and two.

[0007] In a fourth novel aspect, the highly reflective coating comprises a complex wavelength greater than one at six-hundred and thirty-five nanometers.

[0008] In a fifth novel aspect, the highly reflective coating reflects at least twenty percent of incident light.

[0009] In a sixth novel aspect, the highly reflective coating when deposited maintains an underlayer pattern topography at a resolution of at least forty by forty micrometers.

[0010] In a seventh novel aspect, the highly reflective coating does not cause shape changing stress to the wafer.

[0011] Further details and embodiments and techniques are described in the detailed description below. This summary does not purport to define the invention. The invention is defined by the claims.

## BRIEF DESCRIPTION OF THE DRAWINGS

[0012] The accompanying drawings, where like numerals indicate like components, illustrate embodiments of the invention.

[0013] FIG. 1 is a diagram illustrating light reflections from multiple interfaces between transparent layers.

[0014] FIG. 2 is a diagram illustrating light reflection only from the top surface high reflectivity coating.

[0015] FIG. 3 is a diagram illustrating multiple steps of a wafer measuring process.

[0016] FIG. 4 is a diagram illustrating a wafer high reflectivity coating process.

[0017] FIG. 5 is a flowchart 100 describing high reflectivity wafer coating process.

#### DETAILED DESCRIPTION

[0018] Reference will now be made in detail to background examples and some embodiments of the invention, examples of which are illustrated in the accompanying drawings. In the description and claims below, relational terms such as "top", "down", "upper", "lower", "top", "bottom", "left" and "right" may be used to describe relative orientations between different parts of a structure being described, and it is to be understood that the overall structure being described can actually be oriented in any way in three-dimensional space.

[0019] Wafer shape and topography metrology is an important area and is gaining increasing importance in semiconductor industry. Many inline wafer processing steps comprise the use of one or more transparent layers. Wafers often are fabricated on top of a substrate that is also at least partially transparent. When using optical metrology to measure characteristics of an at least partially transparent layer, not all of the incident light reflects from the top surface. The light that does not reflect from the top surface of the at least partially transparent layer, travels through the at least partially transparent layer and then reflects from other subsequent surfaces. These reflections from subsequent surfaces cause inaccurate optical measurements of the top surface of the at least partially transparent layer. The reason for the inaccuracy is that the light reflecting from the top surface cannot be differentiated from the light reflecting from the bottom surface of the at least partially transparent layer. Therefore, what is a contour on the bottom surface of the at least partially transparent layer may appear as a contour on the top surface of the at least partially transparent layer.

[0020] FIG. 1 illustrates the multiple reflections of incident light upon the top surface of an at least partially transparent layer. The wafer illustrated in FIG. 1 comprises six layers. Layer six ( $n_6$ ) is a substrate that is partially transparent. Layers one through five are also at least partially transparent. During optical inspection an incident light is directed to the top surface of layer one. In this example, measurement of the characteristics of the top surface of layer one is desired. However, due to the at least partial transparency of the layer one the incident light is not totally reflected by

layer one. Rather, a portion of the incident light travels through layer one and reflects from the surface interface between layer one and layer two. A portion of the incident light also travels through two and reflects from the surface interface between layer two and layer three. A portion of the incident light also travels through layer three and reflects from the surface interface between layer three and layer four. A portion of the incident light also travels through layer four and reflects from the surface interface between layer four and layer five. A portion of the incident also travels through layer five and reflects from the surface interface between layer five and layer six. A portion of the incident light also travels through layer six and reflects from the bottom surface of layer six. Accordingly, FIG. 1 illustrates that multiple reflections of the incident light from the wafer are produced that are not reflections from the desired top surface of the top transparent layer. To improve measurement of characteristics of the top surface of the transparent layer a new method is needed.

[0021] A new method is illustrated in FIG. 2. FIG. 2 illustrates high reflectivity coating, also referred to as a “physical layer”, to improve optical measurements of the transparent layer top surface. A highly reflective coating is deposited on the top surface of the transparent layer top surface. The highly reflective coating is deposited such that the highly reflective coating has an even thickness across the top surface of the top transparent layer. The even thickness of the deposited highly reflective coating ensures that the top surface of the deposited highly reflective coating matches the top surface of the first transparent layer. In one example, the highly reflective coating material reflects at least twenty percent of the incident light. The increased reflectivity of the high reflectivity coating increases the ability to differentiate, based on light intensity, the light reflected from the highly reflective coating from light reflected from the subsequent layers.

[0022] In one example, the highly reflective coating is an organic material, such as a type of photoresist.

[0023] In another example, the highly reflective coating has a refractive index value between one and two.

[0024] In another example, the highly reflective coating comprises a complex wavelength greater than one at six-hundred and thirty-five nanometers.

[0025] In another example, the highly reflective coating reflects at least twenty percent of incident light.

[0026] In another example, the highly reflective coating when deposited maintains an underlayer pattern topography at a resolution of forty by forty micrometers.

[0027] In another example, the highly reflective coating does not cause destructive stress to the top transparent layer top surface.

[0028] In another example, the highly reflective coating does not cause destructive stress to the top transparent layer.

[0029] In another example, the highly reflective coating does not cause destructive or shape or topography changing stress to the wafer.

[0030] In another example, the highly reflective coating does not comprise metal.

[0031] In another example, the highly reflective coating is opaque.

[0032] FIG. 3 illustrates the steps for measuring the top surface characteristics of a transparent layer using a highly reflective coating. In step one, the top surface of the top transparent layer of a wafer is not coated. In step two, the top surface of the top transparent layer of the wafer is coated in a highly reflective coating. In one example, the coating is an organic, non-metallic, material. In another example, the coating is a metallic layer. The coating prevents reflections from the bottom of transparent films or internal interfaces of the film stacks. Therefore, only the light reflected from the top surface is used to measure wafer top surface topology. It is noted herein, use of organic material for coating is advantageous in that deposition of organic films does not increase films stress to levels where stress induces wafer bending.

[0033] In step three, topography of the top surface of the wafer coated in highly reflective coating is measured. Measuring top surface topography after coating the wafer provides a more accurate optical topography measurement in the presence of transparent films since transparent films effects such as distortion of the reflected phase or loss of variation of reflected light intensity are avoided.

[0034] In step four, the highly reflective coating is removed. The highly reflective coating can be removed using various methodologies. In one example, the highly reflective coating is removed using a solvent. Solvents include, but are not limited to, propylene glycol methyl ether, ethyl lactate, tetramethylammonium hydroxide.

[0035] FIG. 4 illustrates a method for depositing the highly reflective coating to the top surface of the wafer. The wafer is in contact with a vacuum chuck. A dispenser of the highly reflective material is located above the wafer. The wafer is then rotated while the dispenser dispenses the highly reflective material onto the top surface of the wafer. While the wafer is rotated by the vacuum chuck, the dispenser is moved so to deposit the highly reflective material at the center of the wafer or at the outer edge of the wafer and all positions in between.

[0036] FIG. 5 is a flowchart describing the steps of removable opaque coating for accurate optical topography measurements on top surfaces of transparent films. In step 101, a highly reflective coating is disposed onto the top surface of the wafer. In step 102, top surface topography measurements are made of the highly reflective coating. In one example, The surface topography measurement is performed using an optical interferometry based full wafer geometry metrology platform that can measure wafer shape, wafer flatness, dual-sided nanotopography and high-resolution edge roll-off.

[0037] In step 103, once the top surface topography measurements are completed, the highly reflective coating is removed from the wafer without damaging the wafer. In one example, the reflective coating is removed using solvents, such as propylene glycol methyl ether, ethyl lactate, tetramethylammonium hydroxide. In step 104, the wafer is further processed for final use.

[0038] This method of using removable opaque coating for accurate optical topography measurements on top surfaces of transparent films allows:

[0039] - Accurate measurement of top surface topography

[0040] - Prevent physical damage to top surface of wafer when topography is measured by way of physical contact with the wafer.

[0041] - Faster measurement time by using optical measurements.

[0042] - Fast removal of the high reflectivity coating by solvent wash.

[0043] Although certain specific embodiments are described above for instructional purposes, the teachings of this patent document have general applicability and are not limited to the specific embodiments described above. Accordingly, various modifications, adaptations, and combinations of various features of the described embodiments can be practiced without departing from the scope of the invention as set forth in the claims.

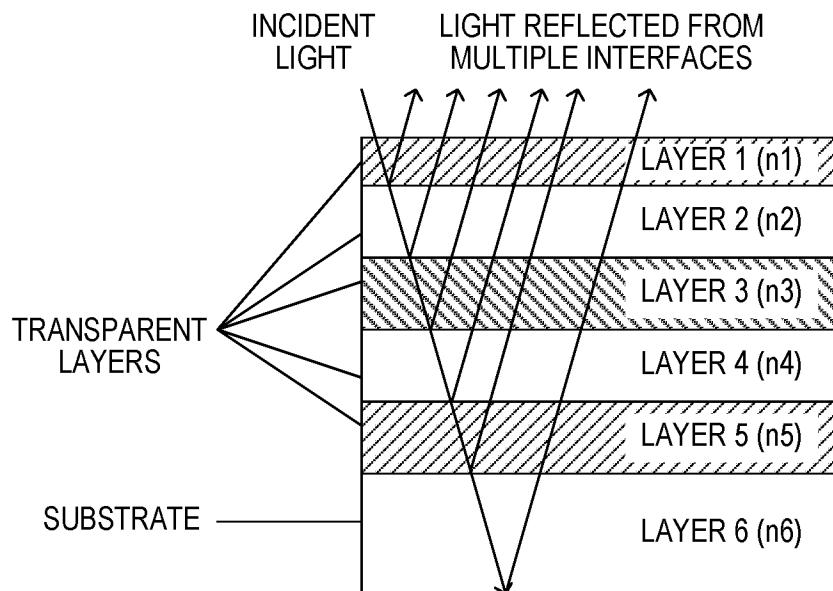
### **Listing of Claims**

What is claimed is:

1. A method, comprising:  
    depositing a highly reflective coating onto a wafer;  
    measuring topography on the highly reflective coating; and  
    removing the highly reflective coating from the wafer.
2. The method of Claim 1, wherein the highly reflective coating comprises organic material.
3. The method of Claim 1, wherein the highly reflective coating comprises a refractive index value between one and two.
4. The method of Claim 1, wherein the highly reflective coating comprises a complex wavelength greater than one at six-hundred and thirty-five nanometers.
5. The method of Claim 1, wherein the highly reflective coating reflects at least twenty percent of incident light.
6. The method of Claim 1, wherein the highly reflective coating when deposited maintains an underlayer pattern topography at a resolution of forty by forty micrometers.
7. The method of Claim 1, wherein the highly reflective coating does not cause destructive stress to the wafer.
8. The method of Claim 1, wherein the highly reflective coating does not comprise metal material.

9. The method of Claim 1, wherein the highly reflective coating is opaque.
10. The method of Claim 1, wherein the wafer comprises a layer of transparent material.
11. The method of Claim 1, wherein the depositing of the highly reflective coating is performed by a spin coater.
12. The method of Claim 1, wherein the measuring topography is performed using an optical interferometry.
13. The method of Claim 1, wherein the wafer shape is measured.
14. The method of Claim 1, wherein wafer flatness is measured.
15. The method of Claim 1, where dual-sided nanotopography is measured.
16. The method of Claim 1, wherein high-resolution edge roll-off is measured.
17. The method of Claim 1, wherein the removing of the highly reflective coating is performed using a solvent.
18. The method of Claim 17, wherein the solvent is propylene glycol methyl ether.
19. The method of Claim 17, wherein the solvent is ethyl lactate.
20. The method of Claim 17, wherein the solvent is tetramethylammonium hydroxide.

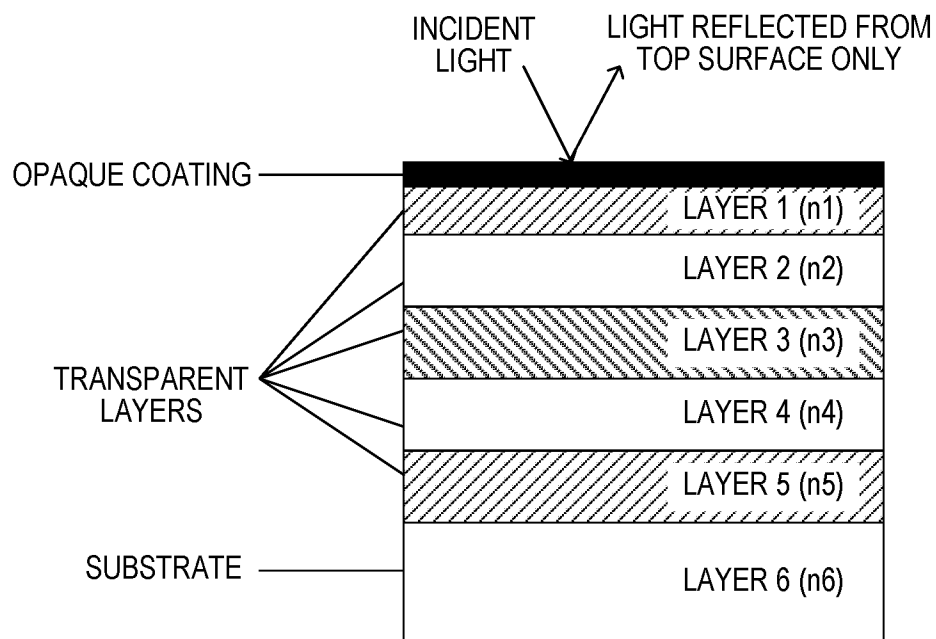
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LIGHT REFLECTIONS FROM MULTIPLE INTERFACES  
BETWEEN TRANSPARENT LAYERS

FIG. 1

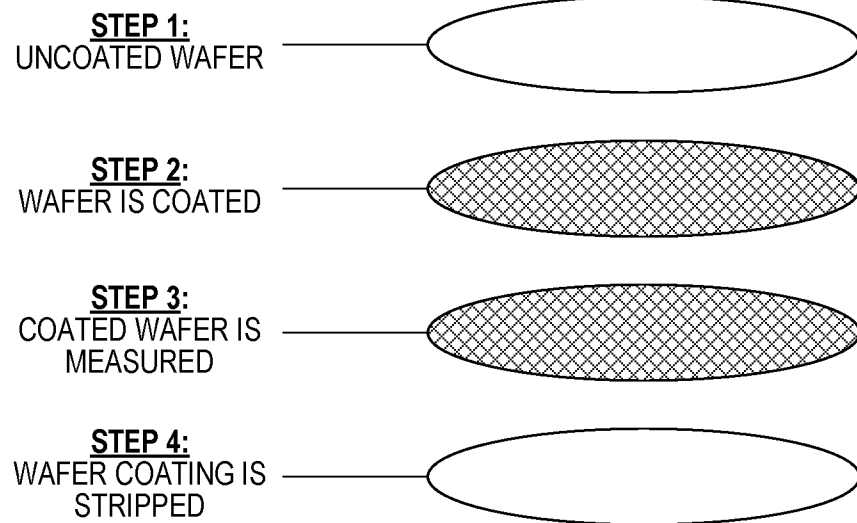
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LIGHT REFLECTION ONLY FROM THE TOP  
SURFACE COATED WITH OPAQUE COATING

FIG. 2

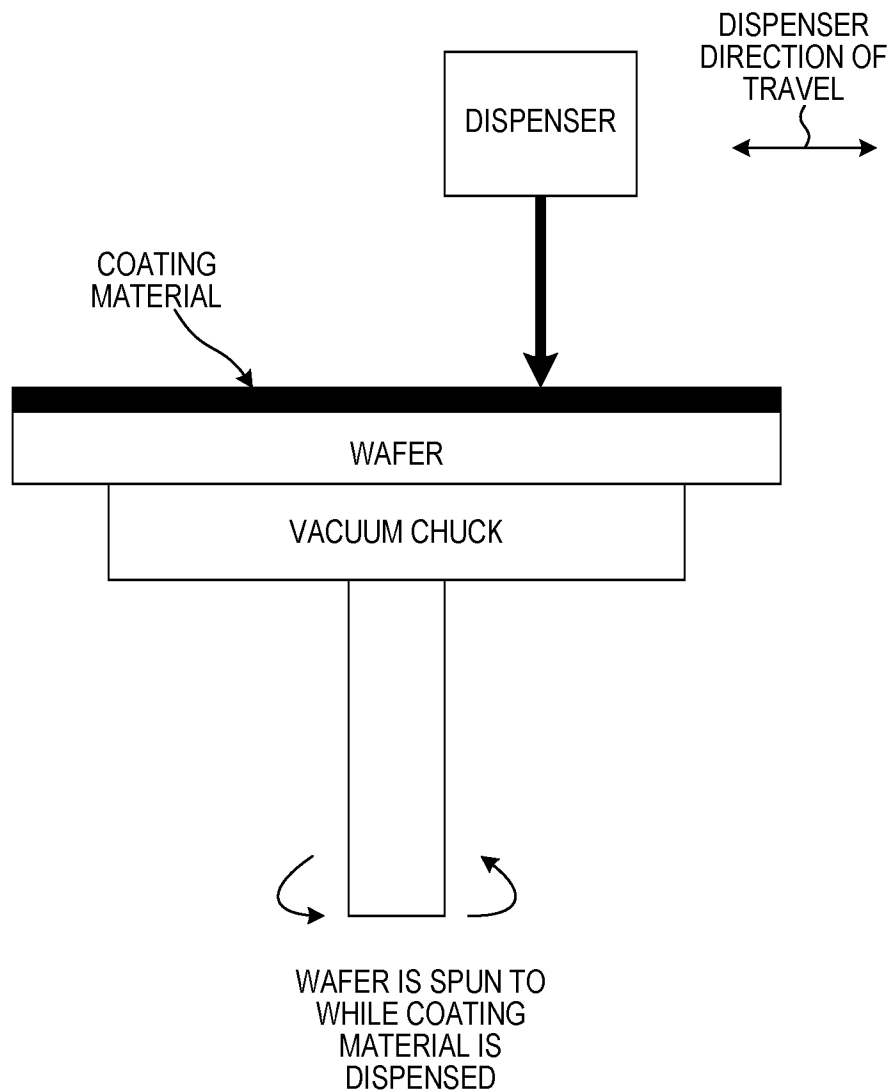
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WAFER MEASURING PROCESS

FIG. 3

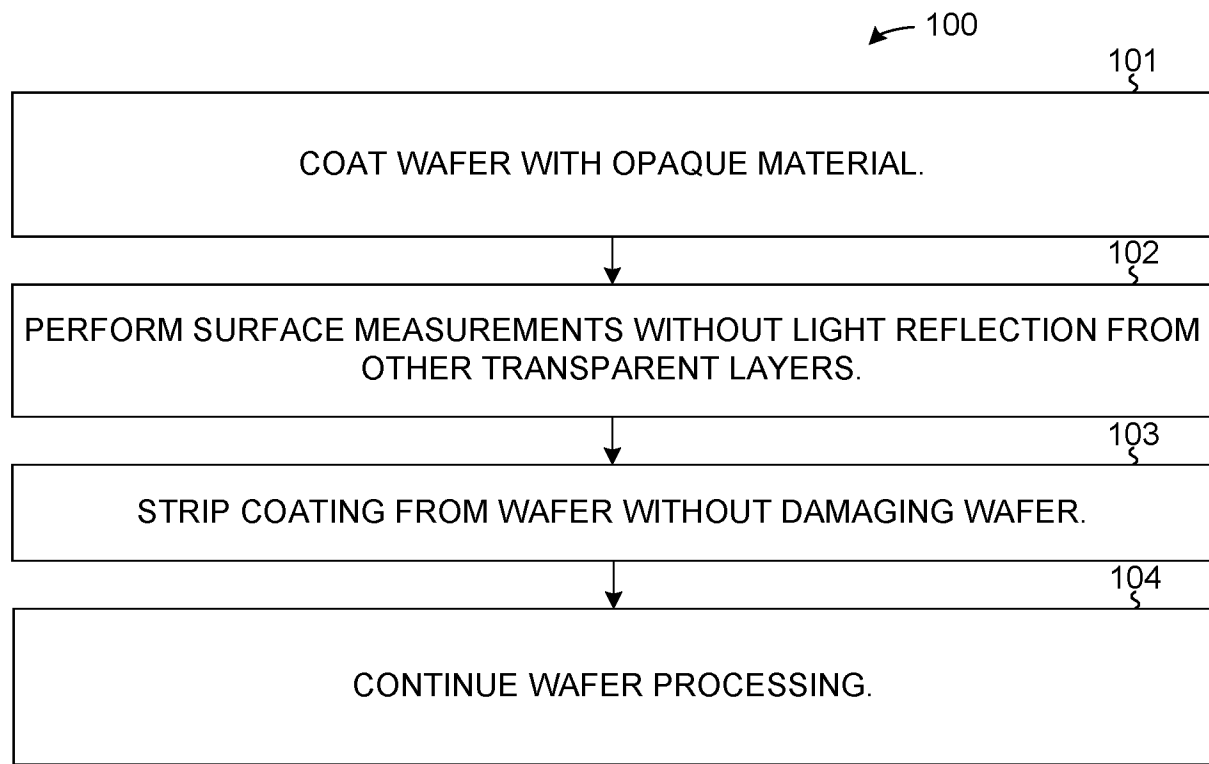
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COATING PROCESS

FIG. 4

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WAFER MANIPULATOR FLOWCHART

FIG. 5

**A. CLASSIFICATION OF SUBJECT MATTER****H01L 21/66(2006.01)i, H01L 21/02(2006.01)i, H01L 21/311(2006.01)i, H01L 21/78(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

H01L 21/66; G01B 11/06; G01B 11/24; G01N 21/88; H01L 21/02; H01L 21/027; H01L 21/266; H01L 21/311; H01L 21/78

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) &amp; keywords: topography, optical measurement, coating, opaque

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                                 | Relevant to claim No. |
|-----------|--------------------------------------------------------------------------------------------------------------------|-----------------------|
| X         | US 2010-0112730 A1 (COLIN BRODSKY et al.) 06 May 2010<br>See paragraphs [0032]-[0057], claims 1, 8 and figure 1.   | 1-20                  |
| A         | US 5004340 A (BARCLAY J. TULLIS et al.) 02 April 1991<br>See column 7, lines 30-58 and figure 4.                   | 1-20                  |
| A         | US 2018-0090307 A1 (INTERNATIONAL BUSINESS MACHINES CORPORATION)<br>29 March 2018<br>See claim 1 and figures 2-4E. | 1-20                  |
| A         | WO 2018-067677 A1 (TOKYO ELECTRON LIMITED et al.) 12 April 2018<br>See paragraphs [0088]-[0097] and figures 8-9.   | 1-20                  |
| A         | US 2018-0195855 A1 (KLA-TENCOR CORPORATION) 12 July 2018<br>See claim 1 and figures 1-10.                          | 1-20                  |



Further documents are listed in the continuation of Box C.



See patent family annex.

\* Special categories of cited documents:

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"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

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**25 March 2020 (25.03.2020)**

Name and mailing address of the ISA/KR

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**INTERNATIONAL SEARCH REPORT**

Information on patent family members

International application No.

**PCT/US2019/056839**

| Patent document<br>cited in search report | Publication<br>date | Patent family<br>member(s)                                                                                         | Publication<br>date                                                              |
|-------------------------------------------|---------------------|--------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------|
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| US 5004340 A                              | 02/04/1991          | JP 02-042742 A<br>US 5076691 A<br>US 5144524 A                                                                     | 13/02/1990<br>31/12/1991<br>01/09/1992                                           |
| US 2018-0090307 A1                        | 29/03/2018          | US 9997348 B2                                                                                                      | 12/06/2018                                                                       |
| WO 2018-067677 A1                         | 12/04/2018          | KR 10-2019-0052160 A<br>TW 201916144 A<br>US 2018-0096905 A1                                                       | 15/05/2019<br>16/04/2019<br>05/04/2018                                           |
| US 2018-0195855 A1                        | 12/07/2018          | CN 110419098 A<br>EP 3549159 A1<br>KR 10-2019-0097281 A<br>SG 11201906177 A<br>TW 201839876 A<br>WO 2018-129385 A1 | 05/11/2019<br>09/10/2019<br>20/08/2019<br>27/08/2019<br>01/11/2018<br>12/07/2018 |