



(51) International Patent Classification:

H02J 3/18 (2006.01) H02M 7/49 (2007.01)

(21) International Application Number:

PCT/EP2016/062354

(22) International Filing Date:

01 June 2016 (01.06.2016)

(25) Filing Language:

English

(26) Publication Language:

English

(71) Applicant: ABB SCHWEIZ AG [CH/CH]; Brown Boveri Strasse 6, CH-5400 Baden (CH).

(72) Inventors: BUCHMANN, Beat; Breitenackerstrasse 3, 5415 Nussbaumen (CH). HEROLD, Simon; Pfruendhofs-trasse 35, 8910 Affoltern am Albis (CH).

(74) Agent: SVELA, Reino; ABB AB, Intellectual Property, Ingenjör Bååths Gata 11, 721 83 Västerås (SE).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JP, KE, KG, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ,

TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

— of inventorship (Rule 4.17(iv))

Published:

— with international search report (Art. 21(3))

(54) Title: MODULAR MULTILEVEL CONVERTER CELL WITH INTEGRATED CURRENT SENSOR

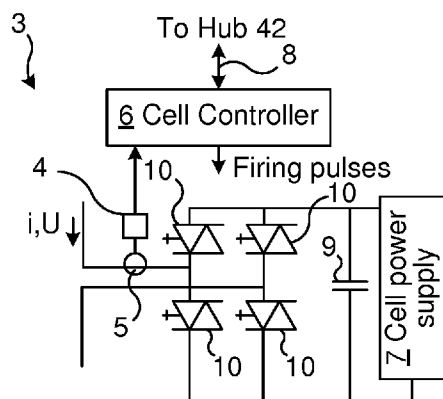


Fig. 3

(57) Abstract: The present disclosure relates to a modular cell (3) for a phase leg of an MMC power converter. The cell comprises a power storing device (9), a plurality of semiconductor switches (10), a cell controller (6), and a current sensor (4). The current sensor is connected between a current conducting line in the cell and the cell controller for measuring a current through the cell and signalling information about said measured current to the cell controller. The cell controller comprises an optical communication interface (8) and is configured for forwarding the information about the measured current to a higher level controller over said optical communication interface.

MODULAR MULTILEVEL CONVERTER CELL WITH INTEGRATED CURRENT SENSOR

TECHNICAL FIELD

The present disclosure relates to devices and methods for current
5 measurements in a modular multilevel converter.

BACKGROUND

A Modular Multilevel power Converter (MMC), also known as Chain-Link
Converter (CLC), comprises a plurality of converter cells, or converter sub-
modules, serially connected in converter branches, or phase legs, that in turn
10 may be arranged in a star, delta, direct or indirect converter topology. Each
converter cell comprises, in the form of a half-bridge or full-bridge circuit, a
capacitor for storing energy and power semiconductor switches such as
insulated gate bipolar transistor (IGBT) devices, gate-turn-off thyristor
(GTO) devices, integrated gate commutated thyristor (IGCT) devices, or
15 Metal–Oxide–Semiconductor Field-Effect Transistor (MOSFET) devices for
connecting the capacitor to the converter branch with one or two polarities.
MMCs may be used in electric power transmission systems such as Static
Synchronous Compensator (STATCOM), Frequency Converters in direct or
indirect topology and High-Voltage Direct Current (HVDC) transmission.

20 For control and protection reasons, phase leg current measurements are
mandatory in a converter. Figure 1 illustrates a typical MMC in delta topology
in which the phase leg between points 1 and 2 has a current i_{12} which is
measured by a sensor (transducer) connected between the phase leg and a
grounded input/output (I/O) board of a central control unit of the converter.
25 If the converter is a high-voltage converter, a lot of insulation is needed for
the sensor since the voltage difference between the phase and ground is high.
The high-voltage converter may be located in a dedicated room or building,
and the sensor and its insulation may be located in a wall bushing into said
room or building.

30 Control functions can include:

- Current control
- Cell sorting and/or selection algorithms

Protection functions can include:

- Overcurrent protection
- 5 - Semiconductor thermal model

Since MMC converters can be designed for high voltages (several 10 kV:s, e.g. $U_{12} = 36 \text{ kV}_{\text{rms}}$), current measurements can be expensive, or there may be no sensors available on the market due to the high insulation level. Insulation of the current transducer has to be designed for the voltage U_0 of figure 1, which is in normal operation approximately $U_{12}/\sqrt{3}$ or in failure cases (earth fault in the 3-phase system) even approximately U_{12} .

An alternative, to reduce the insulation level, is to place a current sensor in each individual cell as an integrated part of the cell circuitry. The sensor may then be connected between a conducting line in each cell of the phase and the cell controller of the cell, each at high voltage potential, reducing the voltage difference which has to be handled by the insulation. However, since each cell of the phase leg has to know the present current, each cell has to be provided with its own current measurement sensor, which increases the cost of the converter.

US 2014/0210431 discloses a multi-cell power supply for receiving power from a source and delivering power at an output terminal to a load. The multi-cell power supply includes a first power cell coupled to the source, and a first current sensor circuit. The first power cell provides a first output current, and includes a first output terminal coupled to a reference node of the multi-cell power supply, and a second output terminal coupled to the output terminal. The first current sensor circuit includes a first current sensor and a power supply. The first current sensor is coupled to the first output terminal of the first power cell, and measures the first output current. The power supply is coupled to either the reference node or a floating ground node of the first power cell, and provides power to the first current sensor.

Thus, the current sensor circuit is outside the power cell but connected to the power cell to measure its output current. The current sensor circuit, as well as the power cell, may each have a fibre optic interface for communication with a central controller, whereby information about the measured current may be forwarded from the current sensor circuit to the power cell via the central controller.

SUMMARY

According to the present invention, problems with the prior art are solved by using a current sensor which is integrated in a cell of each phase of an MMC, the current sensor circuit thus being part of the cell module. However, in order to reduce cost and complexity of the converter, not all cells are provided with an integrated current sensor. Instead, information about measurements of the current performed by the sensor is signalled to a higher level controller over an optical interface between the central controller and a local cell controller of the cell module comprising the current sensor. This information may then be forwarded, possibly after processing by the central controller, to the other cell modules of the phase, thus ensuring that all cells in the phase has up-to-date information about the phase current. By means of the optical interface, the current sensor is insulated from the grounded higher level controller.

According to an aspect of the present invention, there is provided a modular cell for a phase leg of an MMC. The cell comprises a power storing device, a plurality of semiconductor switches, a cell controller, and a current sensor. The current sensor is connected between a current conducting line in the cell and the cell controller for measuring a current through the cell and signalling information about said measured current to the cell controller. The cell controller comprises an optical communication interface and is configured for forwarding the information about the measured current to a higher level controller over said optical communication interface.

According to another aspect of the present invention, there is provided an MMC, comprising at least one phase leg, each phase leg comprising a

plurality of series connected cells, at least one, or a plurality, of said cells being in accordance with the modular cell aspect of the present disclosure.

According to another aspect of the present invention, there is provided a method performed by a cell controller comprised in a cell in a phase leg of an
5 MMC. The method comprises receiving information from a current sensor, connected between a current conducting line in the cell and the cell controller. The information is about a measured current of the current conducting line. The method also comprises forwarding the received information to a higher level controller over an optical communication
10 interface.

According to another aspect of the present invention, there is provided a computer program product comprising computer-executable components for causing a cell controller to perform the method of the present disclosure when the computer-executable components are run on processor circuitry
15 comprised in the cell controller.

It is to be noted that any feature of any of the aspects may be applied to any other aspect, wherever appropriate. Likewise, any advantage of any of the aspects may apply to any of the other aspects. Other objectives, features and advantages of the enclosed embodiments will be apparent from the following
20 detailed disclosure, from the attached dependent claims as well as from the drawings.

Generally, all terms used in the claims are to be interpreted according to their ordinary meaning in the technical field, unless explicitly defined otherwise herein. All references to "a/an/the element, apparatus, component, means,
25 step, etc." are to be interpreted openly as referring to at least one instance of the element, apparatus, component, means, step, etc., unless explicitly stated otherwise. The steps of any method disclosed herein do not have to be performed in the exact order disclosed, unless explicitly stated. The use of "first", "second" etc. for different features/components of the present
30 disclosure are only intended to distinguish the features/components from

other similar features/components and not to impart any order or hierarchy to the features/components.

BRIEF DESCRIPTION OF THE DRAWINGS

Embodiments will be described, by way of example, with reference to the
5 accompanying drawings, in which:

Fig 1 is a schematic illustration of an embodiment of an MMC with current sensor, in accordance with prior art.

Fig 2 is a schematic illustration of an embodiment of an MMC, in accordance with the present invention.

10 Fig 3 is a schematic circuit diagram of an embodiment of a cell module of an MMC, with integrated sensor circuitry, in accordance with the present invention.

Fig 4 is a schematic block diagram of an embodiment of a control topology of an MMC with current sensors, in accordance with the present invention.

15 DETAILED DESCRIPTION

Embodiments will now be described more fully hereinafter with reference to the accompanying drawings, in which certain embodiments are shown.

However, other embodiments in many different forms are possible within the scope of the present disclosure. Rather, the following embodiments are
20 provided by way of example so that this disclosure will be thorough and complete, and will fully convey the scope of the disclosure to those skilled in the art. Like numbers refer to like elements throughout the description.

When electric potential is mentioned herein, it is interchangeable with the voltage measured to ground.

25 Figure 2 is a schematic circuit diagram of an embodiment of an MMC 1, e.g. a high-voltage MMC. The MMC 1 may be used in electric power transmission systems such as STATCOM, Frequency Converters in direct or indirect topology or HVDC transmission. One or more (high-voltage) phases, u, v and

w having respective currents i_1 , i_2 and i_3 are input to the converter 1 via input lines, e.g. via bushings through a wall of the room or building in which the converter 1 is located. In this example the MMC is in delta configuration with three phase legs 2, but any other configuration and number of phase legs is
5 also possible with embodiments of the present invention. Each phase leg 2 comprises a plurality of cascaded (series connected) cells (also called cell modules) 3. The currents in the converter 1 are referred to as “ i ”, while the voltages are referred to as “ U ” in the figure, in combination with arrows indicating current directions.

- 10 The cells 3 may be of any suitable type, e.g. half-bridge or full-bridge, comprising a power storing device and a plurality of semiconductor switches.

Figure 3 illustrates an example embodiment of a cell 3 according to the invention. The cell comprises integrated current sensor circuitry 4 with insulation 5 at an input or output terminal of the cell, for measuring the
15 current i through the cell, and thus through each cell of the phase leg. The sensor 4 is connected to/between a current conducting line (could also be called conductor or wire), via the insulation 5, and the cell controller 6, also comprised in the cell 3 and at a high electrical potential thus reducing the voltage/potential difference over the sensor, as discussed herein. The cell
20 controller controls the semiconductor switches 10, e.g. diodes, IGBT, GTO, IGCT, and/or MOSFET devices, e.g. in combination with an anti-parallel diode, by means of firing pulses. In accordance with the present invention, the cell controller 6 of the cell 3 is in optical communication with a higher level controller, e.g. a phase controller or hub 42 (see also figure 4), over an
25 optical communication interface (e.g. comprising an optical cable/fibre and/or laser) 8. The cell 3 further comprises a power storing device, e.g. one or more capacitor(s) 9 or inductors, and a cell power supply 7 (which may also power the sensor 4) deriving power from the power storing device 9, and any other commonly used components of a regular MMC cell.

- 30 In accordance with the present invention, not all cells 3 of a phase leg 2 comprises current sensor circuitry 4 and 5. However, in other respects cells 3

without such sensor may be as described with reference to figure 3.

Particularly, each and all of the cells 3 of the phase leg 2 may comprise an optical interface between its cell controller 8 and a higher level controller, e.g. hub 42. Thus, information about a current measured by a sensor 4 in a cell 3
5 comprising such a sensor may be signalled optically to each of the other cells 3 in the phase via the higher level controller 42, or at least to some or all of the cells 3 in the phase leg 2 which do not comprise its own sensor circuitry or who's sensor circuitry is not operational.

Figure 4 is a block diagram of an embodiment of a control topology for a
10 converter 1. As discussed above, a phase leg comprises a plurality of cells 3, each of which comprises a cell controller 6, and one or some of the cells each comprises a current sensor 4. The phase leg may comprise any number of cells 3, here denoted 3a, 3b, 3c, 3d...3n. Cell 3a comprises a cell controller 6a and no current sensor, cell 3b comprises a cell controller 6b and a current
15 sensor 4b, cell 3c comprises a cell controller 6c and no current sensor, and cell 3d comprises a cell controller 6d and a current sensor 4d, and further to cell 3n (each cell with or without a current sensor). Each cell controller 6 has an optical interface 8 for optical communication with higher level controller(s) 42 and or 41. In the example of the figure, each phase leg 2 has a
20 control hub 42 which is configured for optical communication with each of the cell controllers 6 of the phase leg. The respective hub 42a, 42b and 42c (and hub(s) 42 for any further phase legs 2) of each phase leg may also be configured for optical communication with an even higher level controller, here called a central controller 41, which may be the main controller of the
25 converter 1. To simplify the figure, only one phase leg 2 is shown, but the discussion is relevant to any further phase leg(s), whose cell controllers 3 are configured for optical communication with their higher level controller, e.g. hub 42. In an alternative embodiment, e.g. if the converter comprises only one phase leg but may also be relevant for converters comprising a plurality
30 of phase legs, there are no hubs 42 but the cell controllers 3 may be configured for optical communication directly with the central controller 41.

In accordance with the present invention, at least one, but not all cells 3 of a phase leg 2 comprises an integrated current sensor 4. The most cost effective alternative would be to only have a current sensor in one of the cells 3 of each phase leg, but for the sake of redundancy it may in other embodiments be
5 convenient that a plurality of cells 3 of each phase leg 2 comprises an integrated current sensor 4.

The solution of the present disclosure to reduce the insulation level of the current sensor 4 is to place the current sensor integrated in the cell module as in figure 3. The measurement signals are sent via fast fibre optic link 8 to a
10 central control board 41 or intermediate control hub 42 of the phase 2 that has an electrical potential equal to, or close to, ground, which means that insulation is done by means of the fibre optic cable 8 which may be easier to handle. The power supply 7 of the current transducer is derived locally from the cell 3 (Cell Power Supply 7) and the sensor signal is captured by a cell
15 internal control board (Cell Controller 6) also comprised in the cell module 3. This reduces the insulation level (potential/voltage difference) from several tens of kilovolts to some kilovolts (e.g. about 4 kV). For this reduced voltage level it is more easy to find current sensors on the market and at a lower price. Another advantage of this approach with integrated current sensor 4 is
20 the modularity. Current measurement will no longer depend on the different converter voltage levels and the number of cells 3 connected in series. As mentioned above, a disadvantage of including a current sensor 4 in each cell 3 is that a high number of current sensors are needed which increases the cost of the converter 1 (especially for converters with a high number of cells).

25 In order to reduce costs, only one or some of the cells 3 of a phase leg 2 is with a current sensor 4. In order to avoid impact on the cell redundancy (allowing cell modules 3 to be by-passed, removed or exchanged), it may be convenient to equip more than one cell 3 of each phase leg 2 with a current sensor 4. For example, if three redundant cells 3 per phase leg 2 are installed,
30 the number of cells 3 per leg 2 with current sensor should be four.

Cells 3 without a current sensor 4 gets the current information via optical link 8 from cells with current sensors via a hub 42 or central controller 41 to which all cells of a phase leg are connected (via optical communication link 8).

- 5 In some embodiments of the present invention, the cell controller 6 receives, or is configured for receiving, information about current measurements in other cell(s) of the phase leg 2 over the optical communication interface 8. Thus, the cell controller may compare the measurements in other cells in the phase leg to determine whether they sufficiently correspond to the
- 10 measurements performed by the current sensor in its own cell 3. Thus, a failure of a current sensor may be determined, or the cell controller 6 may decide to use a mean value of the current measurements of its own and other cells for controlling the semiconductor switches 10.

In some embodiments of the present invention, the current conducting line

15 has a voltage measured to ground of at least 10 kV, e.g. between 10 and 50 kV, such as between 20 and 40 kV, or higher e.g. up to 100 kV, 200 kV or 300 kV or more.

In some embodiments of the present invention, the cell controller (6) has a voltage measured to ground of at least 10 kV, e.g. between 10 and 50 kV, such

20 as between 20 and 40 kV, or higher e.g. up to 100 kV, 200 kV or 300 kV or more.

In some embodiments of the present invention, the difference in electrical potential between the current conducting line and the cell controller is relatively small since none are at ground potential, e.g. some kilovolts such as

25 about or less than 10 kV or 5 kV, e.g. between 1 and 5 kV, such as between 2 and 4 kV.

In some embodiments of the present invention, the cell controller 6 is configured to, over the optical interface 8 to the higher level controller 41 or 42, send an indication that the current sensor 4 has measured a current

30 which is above a predetermined overcurrent threshold. Thus, a warning

message, e.g. in the form of a bit information “overcurrent_pulseblock” can be sent to the other cells 3 in the phase leg 2, especially cells without current sensor, e.g. allowing the other cells to block pulse firing to the semiconductor switches in view of the overcurrent.

- 5 In some embodiments of the present invention, the cell controller 6 is configured to, in response to the measured current above the overcurrent threshold, block firing pulses to the semiconductor switches 10.

The cell controller 6 may be configured for performing the method of the present disclosure by means of a computer program product comprising
10 computer-executable components in the form of software for causing the cell controller to perform the method.

Embodiments of the present invention may be conveniently implemented in a cell controller 6 using one or more conventional general purpose or specialized digital computer, computing device, machine, or microprocessor,
15 including one or more processors or field-programmable gate arrays (FPGA), memory and/or computer readable storage media forming a computer program product of the present invention when programmed according to the teachings of the present disclosure. Appropriate software coding can readily be prepared by skilled programmers based on the teachings of the
20 present disclosure, as will be apparent to those skilled in the software art.

In some embodiments, the present invention relates to a computer program product which is a non-transitory storage medium or computer readable medium (media) having instructions stored thereon/in in the form of computer-executable components which can be used to program a cell
25 controller 6 to perform the method of the present invention. Examples of the storage medium can include, but is not limited to, any type of disk including floppy disks, optical discs, DVD, CD-ROMs, microdrive, and magneto-optical disks, ROMs, RAMs, EPROMs, EEPROMs, DRAMs, VRAMs, flash memory devices, magnetic or optical cards, nanosystems (including molecular

memory ICs), or any type of media or device suitable for storing instructions and/or data.

The present disclosure has mainly been described above with reference to a few embodiments. However, as is readily appreciated by a person skilled in
5 the art, other embodiments than the ones disclosed above are equally possible within the scope of the present disclosure, as defined by the appended claims.

CLAIMS

1. A modular cell (3) for a phase leg (2) of a Modular Multilevel power Converter, MMC, (1), the cell comprising:
 - a power storing device (9);
 - 5 a plurality of semiconductor switches (10);
 - a cell controller (6); and
 - a current sensor (4);

wherein the current sensor (4) is connected between a current conducting line in the cell (3) and the cell controller (6) for measuring a current through

 - 10 the cell and signalling information about said measured current to the cell controller; and

wherein the cell controller (6) comprises an optical communication interface (8) and is configured for forwarding the information about the measured current to a higher level controller (41; 42) over said optical communication

 - 15 interface.
2. The cell of claim 1, wherein the cell controller (6) is also configured for receiving information about current measurements in other cell(s) of the phase leg (2) over the optical communication interface.
 3. The cell of claim 1 or 2, wherein the current conducting line has a
 - 20 voltage measured to ground of at least 10 kV.
 4. The cell of any preceding claim, wherein the cell controller (6) has a voltage measured to ground of at least 10 kV.
 5. The cell of any preceding claim, wherein the difference in electrical potential between the current conducting line and the cell controller is less
 - 25 than 10 kV.

6. The cell of any preceding claim, wherein the cell controller (6) is configured to, over the optical interface (8) to the higher level controller (41; 42), send an indication that the current sensor (4) has measured a current which is above a predetermined overcurrent threshold.

5 7. The cell of claim 6, wherein the cell controller (6) is configured to, in response to the measured current above the overcurrent threshold, block firing pulses to the semiconductor switches (10).

8. A Modular Multilevel power Converter, MMC, (1), comprising at least one phase leg (2), each phase leg comprising a plurality of series connected
10 cells (3), at least one, or a plurality, of said cells (3) being in accordance with any preceding claim.

9. A method performed by a cell controller (6) comprised in a cell (3) in a phase leg (2) of an MMC (1), the method comprising:

15 receiving information from a current sensor (4), connected between a current conducting line in the cell (3) and the cell controller (6), about a measured current of the current conducting line;

forwarding the received information to a higher level controller (41; 42) over an optical communication interface (8).

10. The method of claim 9, further comprising:

20 receiving information about current measurements in other cell(s) of the phase leg (2) over the optical communication interface.

11. A computer program product comprising computer-executable components for causing a cell controller (6) to perform the method of claim 9 or 10 when the computer-executable components are run on processor
25 circuitry comprised in the cell controller.

1/2

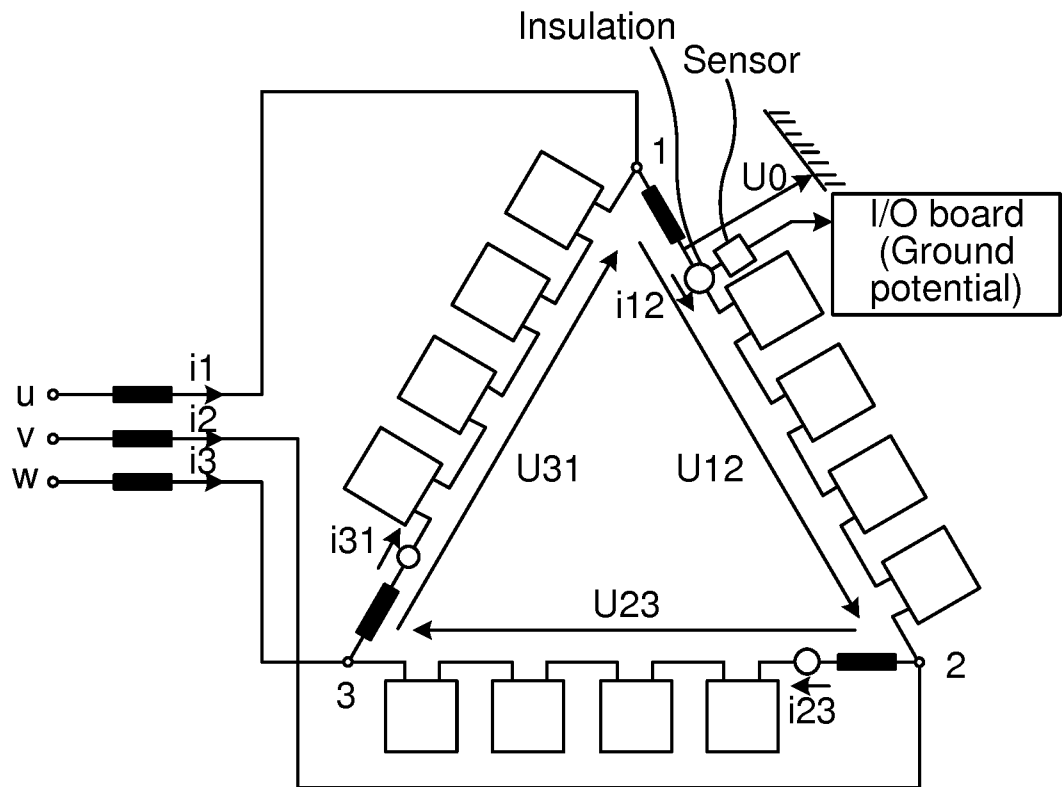


Fig. 1

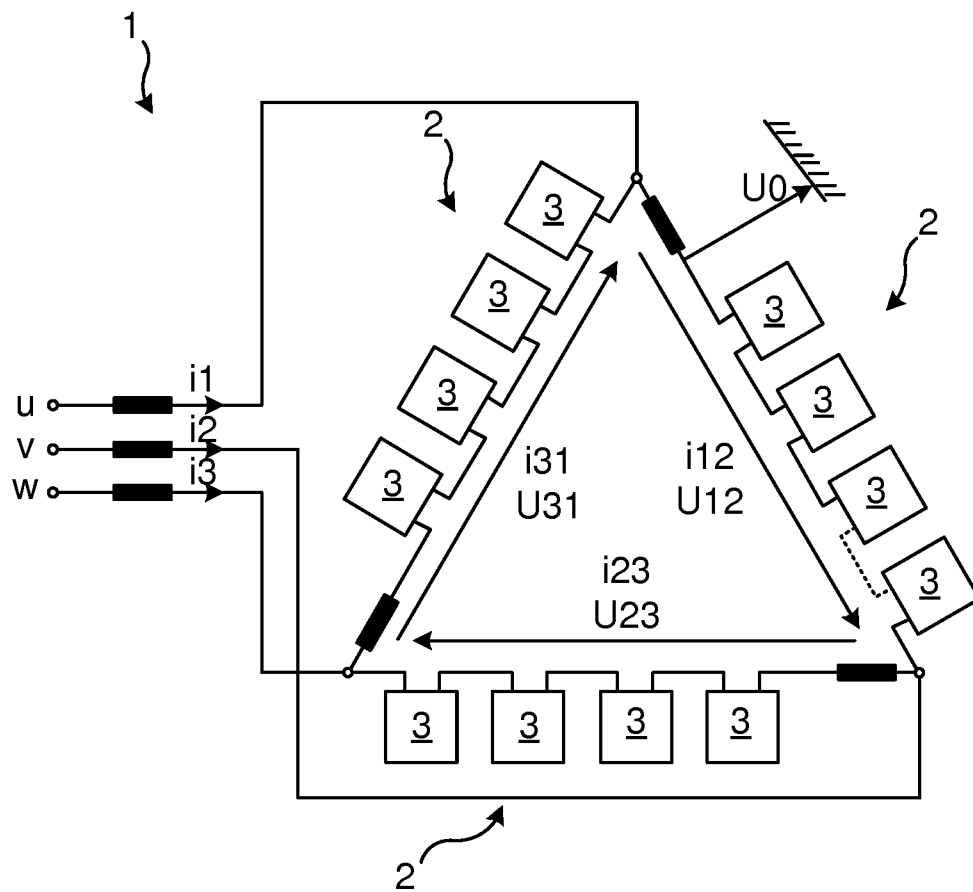


Fig. 2

2/2

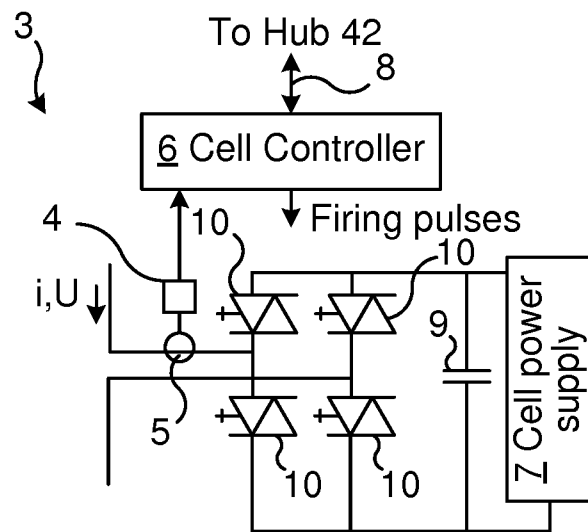


Fig. 3

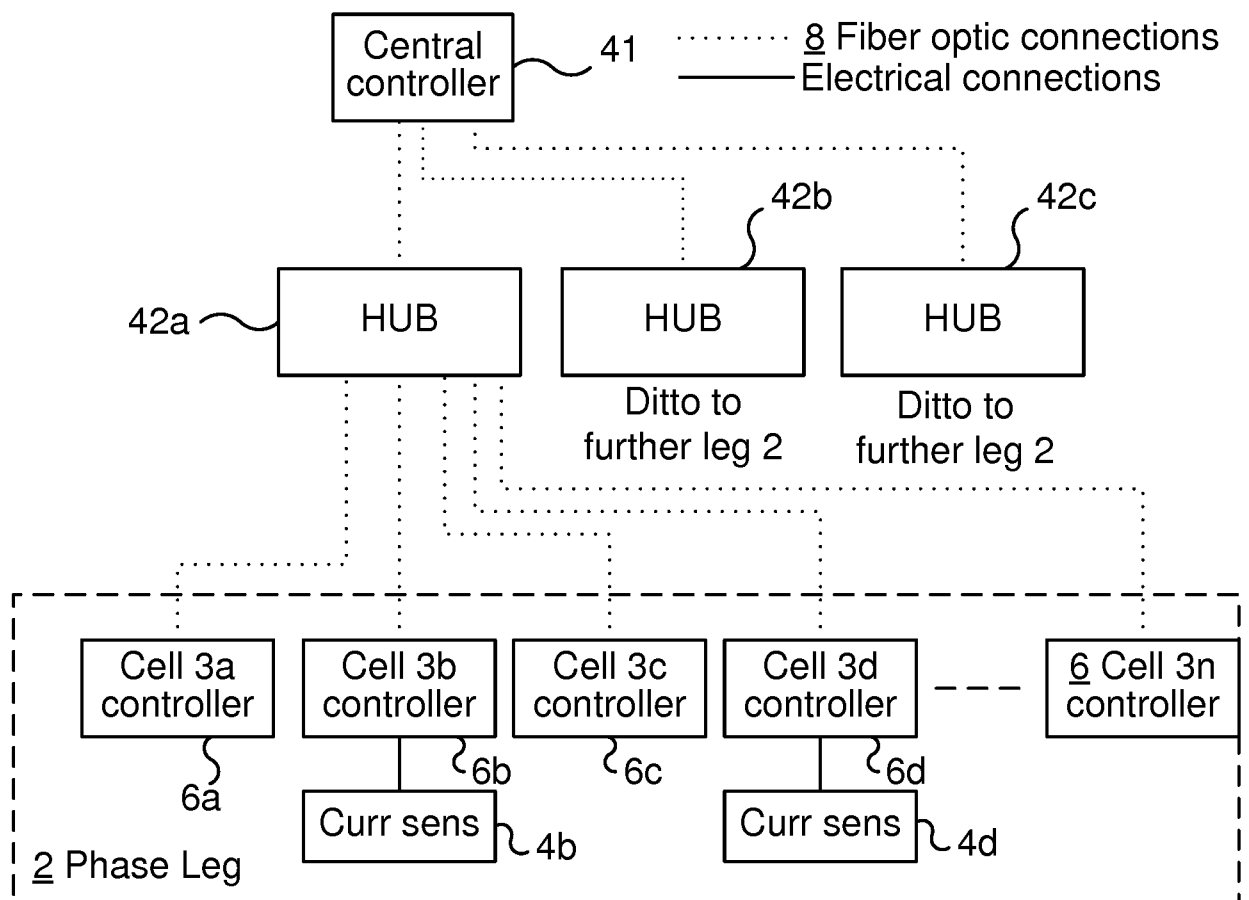


Fig. 4

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2016/062354

A. CLASSIFICATION OF SUBJECT MATTER

INV. H02J3/18 H02M7/49
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H02J H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2014/210431 A1 (RASTOGI MUKUL [US] ET AL) 31 July 2014 (2014-07-31) abstract; figures 2-7 paragraphs [0031] - [0036] -----	1-11
A	WO 2011/107151 A1 (ABB RESEARCH LTD [CH]; NORRGA STAFFAN [SE]; JONSSON TOMAS [SE]; SHUKLA) 9 September 2011 (2011-09-09) abstract; figures 1-8 -----	1-11
A	US 6 014 323 A (AIELLO MARC F [US] ET AL) 11 January 2000 (2000-01-11) abstract; figures 1-14 -----	1-11
A	EP 2 595 304 A2 (EATON CORP [US]) 22 May 2013 (2013-05-22) abstract -----	1-11
	-/--	



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

7 February 2017

Date of mailing of the international search report

15/02/2017

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Garcia, Jesus

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2016/062354

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 2 830 207 A2 (LSIS CO LTD [KR]) 28 January 2015 (2015-01-28) paragraph [0025]; claims 1-3; figures 1-3 -----	1-11

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2016/062354

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2014210431 A1	31-07-2014	CA 2896654 A1	07-08-2014
		CN 105009437 A	28-10-2015
		EP 2951914 A2	09-12-2015
		US 2014210431 A1	31-07-2014
		WO 2014120850 A2	07-08-2014

WO 2011107151 A1	09-09-2011	EP 2543124 A1	09-01-2013
		US 2013016541 A1	17-01-2013
		WO 2011107151 A1	09-09-2011

US 6014323 A	11-01-2000	NONE	

EP 2595304 A2	22-05-2013	AU 2012244320 A1	06-06-2013
		CA 2794873 A1	18-05-2013
		CN 103124160 A	29-05-2013
		EP 2595304 A2	22-05-2013
		US 2013128628 A1	23-05-2013
		US 2015036402 A1	05-02-2015

EP 2830207 A2	28-01-2015	CN 104333254 A	04-02-2015
		EP 2830207 A2	28-01-2015
		JP 6027060 B2	16-11-2016
		JP 2015023799 A	02-02-2015
		KR 20150011064 A	30-01-2015
		US 2015022135 A1	22-01-2015
