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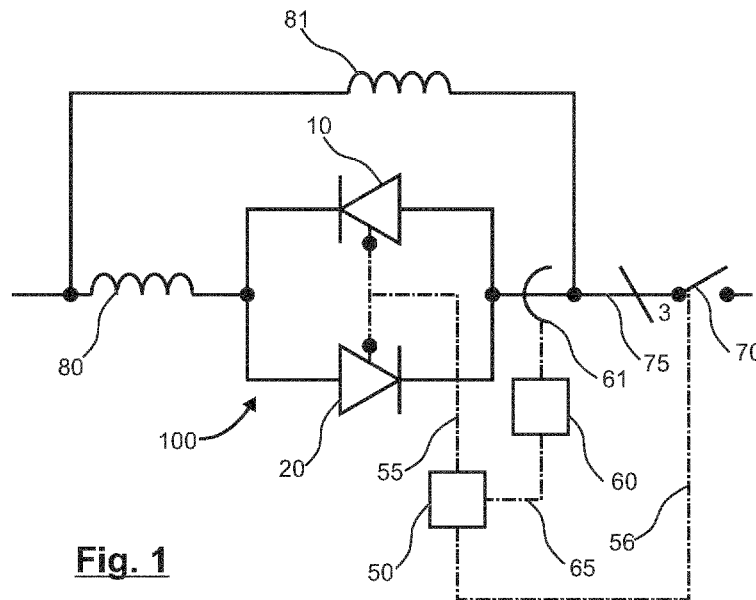


Fig. 1

(57) Abstract: A thyristor circuit (100) comprises at least one branch comprising at least one thyristor (10, 20), a thyristor control circuit (50) and a current detector (60, 61). The current detector (60, 61) is configured to detect a current value representative of an electrical current flowing through the thyristor and to input the detected current value into the thyristor control circuit. The thyristor control circuit is configured to determine a fault condition in which the detected current value exceeds a predetermined current threshold based on a deteriorated blocking ability of the thyristor. Dependent on the determination result, the thyristor control circuit triggers the thyristor into a conductive state.



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THYRISTOR CIRCUIT AND THYRISTOR PROTECTION METHOD

TECHNICAL FIELD

5 The present disclosure relates to a thyristor circuit having at least one branch that comprises at least thyristor, and to a thyristor protection method for protecting a thyristor in the at least one branch.

BACKGROUND

0 In high voltage, high current applications, electronic devices often employ thyristors as power electronic components. Upon reception of a trigger current at its gate, a thyristor starts to conduct an electrical current in a path from its anode terminal to its cathode terminal through its stack of semiconductor junctions. In the literature, triggering is also referred to as firing or gating. Unless triggered, the thyristor is in a non-conducting state or blocking state.

5 Thyristors are thus used for current control in various applications, e. g. as crow bars, high power rectifiers etc. When thyristors or a stack of multiple series-connected thyristors are connected in an antiparallel manner, they can be used to control currents in both directions, e. g. in AC applications such as AC furnaces.

20 In typical applications, a fault current condition may occur. In this case the thyristors involved in conducting the fault current are conventionally blocked. As a temperature of the junctions, or junction temperature, rises to a high value, the blocking ability of a thyristor starts to deteriorate, leading to a high resistance electrical path through the junctions. Thus, in the blocking state and in the case of a fault current condition, the rise of the junction temperature may further be promoted, which may lead to a failure or thermal destruction of the thyristor.

There is a desire to protect the thyristor or thyristors in a thyristor circuit from being thermally destroyed in a fault current condition.

25 Document US 3,611,043 A describes a protective circuit for a power system including a thyristor switch and a backup breaker provided in series with the thyristor switch. The protective circuit causes the backup circuit breaker to interrupt in the abnormal event that the thyristor switch becomes disabled.

SUMMARY OF THE INVENTION

An embodiment of the present disclosure provides a thyristor circuit having an improved fault current behavior. According to an aspect relating to a thyristor circuit, the circuit comprises:

at least one branch comprising at least one thyristor;

a thyristor control circuit for selectively triggering the thyristor;

a current detector configured to detect a current value representative of an electrical current flowing through the thyristor and to input the detected current value into the thyristor control circuit;

wherein the thyristor control circuit is configured to determine a fault condition in which the detected current value exceeds a predetermined current threshold based on a deteriorated blocking ability of the thyristor, and, dependent on the determination result, to trigger the thyristor into a conductive state;

wherein triggering the thyristor into the conductive state comprises continuously triggering the thyristor into the conductive state.

Another aspect relates to a thyristor protection method for protecting a thyristor in at least one branch of a thyristor circuit, the method comprising:

detecting a current value representative of an electrical current flowing through the thyristor;

determining whether the current value exceeds a predetermined current threshold based on a blocking ability dependent on a junction temperature of the thyristor;

dependent on the determination result, triggering the thyristor into a conductive state,

wherein triggering the thyristor into the conductive state comprises continuously triggering the thyristor into the conductive state.

Also, a thyristor circuit according to an aspect comprises at least one branch, the branch comprising at least one thyristor. The thyristor circuit further comprises a thyristor control circuit and a current detector. The thyristor control circuit is for selectively triggering the thyristor. The current detector is configured to detect a current value representative of an

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5 electrical current that flows through the thyristor. The current detector is further configured to input the detected current value into the thyristor control circuit. The thyristor control circuit is configured to determine a fault condition in which the detected current value exceeds a predetermined current threshold based on a deteriorated blocking ability of the thyristor. The thyristor control circuit is further configured to trigger the thyristor into a conductive state dependent on the result of the determination.

0 Fault condition, as used herein, typically refers to an event or time period during which certain electrical values of the thyristor or the branch in which the thyristor is located exceed a limit value. The limit value may be representative of certain current values in the thyristor or the branch in which the thyristor is located.

In some aspects, the fault condition may be an excessive current, or overcurrent, occurring in one or more branches. For example, the fault condition may be a branch fault, e.g. an excessively high current in one branch. In other examples, the fault condition may be a multi-branch fault, e.g. an excessively high current in multiple branches.

5 Excessively high current, as used herein, typically refers to a magnitude or amplitude of a current that will lead to a destruction of the thyristor within a short time period, such as a time period shorter than 500ms or shorter than 100 ms or shorter than 10ms.

0 According to an aspect relating to a thyristor protection method, a thyristor protection method for protecting a thyristor in at least one branch of a thyristor circuit comprises detecting a current value representative of an electrical current flowing through the thyristor, determining whether the current value exceeds a predetermined current threshold based on a deteriorated blocking ability of the thyristor, and dependent on the determination result, triggering the thyristor into a conductive state.

25 In the above aspects relating to the thyristor circuit or to the thyristor protection method, when the thyristor control circuit determines that a fault condition has occurred, it issues a trigger current to the gate of the at least one thyristor such that the thyristor is brought into the conductive state. A fault condition is detected as an overcurrent condition, not as an overvoltage condition. The fault condition comprises a high current condition, or overcurrent condition, typically based on a fault external to the device that includes the thyristor circuit,
30 e. g. a converter external fault.

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When the thyristor control circuit determines that a fault condition has not occurred, it may perform a normal operation of the thyristor, i. e. perform a selective trigger operation of the thyristor according to a normal operation application scheme. Such a normal operation application scheme may as well be performed by another device different from the thyristor control circuit. As non-limiting and illustrative examples, the respective device may e. g. trigger the thyristor according to crow bar scheme in a crow bar application, e. g. trigger the thyristor according to a rectification scheme in a rectification application, or e. g. trigger the thyristor according to a current and/or power control scheme of an AC arc furnace.

The current detector may be any suitable type of current detection device, and as a non-limiting example may be an inductively coupled current detector. The current threshold may be predetermined and set in advance, i. e. prior to operation. The current threshold may, for example, be set taking into account a nominal operation range or an extended operation range of the thyristor. A nominal operation range, as used herein, comprises at least a current that the thyristor is able to bear permanently, e. g. by design, without being deteriorated or destroyed beyond the design limits. An extended operation range, as used herein, comprises at least a current that the thyristor is able to bear for a limited period of time. For example, the current threshold may be set to just above the maximum nominal operation current of the thyristor, or it may be set to e. g. 105%, 110% or 115% of the nominal operation current. In a further example, the current threshold may be set to just above the extended operation range.

The conductive state that is to be achieved by triggering the thyristor, as used herein, is typically a state in which a conducting current valve behavior is intended. In the conductive state, an electrical resistance across the junctions of the thyristor is typically significantly lower than during the flow of an unintended current that flows in decreased blocking conditions, e. g. in a high-temperature state of the junctions.

By intentionally triggering the at least one thyristor into the conductive state when a fault condition is detected, a deterioration of the blocking capabilities can be suppressed, and thus a harmful increase of the junction temperature due to a fault current through the at least one thyristor can be prevented.

According to an embodiment, triggering the thyristor into the conductive state comprises continuously triggering the thyristor into the conductive state. The configuration may thus be such as to continuously trigger the thyristor into the conductive state dependent on the determination result. Continuously triggering, as used herein, means that the conductive state

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of the thyristor is maintained by the triggering operation without allowing the thyristor to revert to the blocked state, at least as long as the fault condition is determined to prevail.

In embodiments, continuously triggering the thyristor into the conductive state may include continuously triggering the thyristor for at least 50 ms, preferably at least 100 ms, more preferably at least 130 ms, even more preferably at least 160 ms.

In further embodiments, the thyristor circuit further comprises a circuit breaker, e.g. for the branch current. Here, when continuously triggering the thyristor, the control of the circuit breaker is carried out such as to interrupt the current, such as the branch current. As used herein, the term branch current may refer to all branch currents in the case that multiple branches are provided.

A circuit breaker operation involves a certain delay, or time-lag, that mainly results from actors of the breaker that perform mechanical actions to interrupt a line carrying the branch current. In an embodiment involving the circuit breaker, the herein-described control by the thyristor control circuit may efficiently bridge the time gap from a fault condition occurrence until a secure interruption by the circuit breaker.

In embodiments, continuously triggering the thyristor into the conductive state may include continuously triggering the thyristor for at least as long as it takes for the circuit breaker to interrupt the line carrying the branch current.

In further embodiments, the thyristor circuit comprises a plurality of branches, for example but not limited to three branches in a three-phase system. Each branch comprises at least one thyristor. According to embodiments, the thyristors in each branch are controlled in a like manner, for example by the thyristor control circuit. For example, the thyristors in each of the branches are triggered into the conductive state dependent on the determination result, as described herein.

In embodiments employing the plurality of branches, a circuit breaker, as described herein, may be configured to interrupt the currents in each of the branches, e. g. a common circuit breaker for all branches. For example, the circuit breaker is controlled such as to interrupt the branch currents in all branches when continuously triggering the thyristor or thyristors.

According to further embodiments, the fault condition is determined when at least in one of the multiple branches the detected current value exceeds the predetermined current threshold. For example, and not by way of limitation, when only in one or two of three branches of a

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three-phase system the respective detected current value exceeds the predetermined current threshold, the fault condition is determined, and the thyristor or the thyristors, preferably the thyristors of all branches, is/are triggered into the conductive state. According to yet further embodiments, the thyristor control circuit is configured to trigger the at least one thyristor of each branch into the conductive state dependent on the fault condition.

According to further embodiments, the at least one branch comprises a stack of thyristors. A stack of thyristors comprises two or more thyristors. In an aspect, the number of thyristors in the stack is selected depending on a voltage level to which the stack is connected or which the stack is exposed to. The voltage level is, for example, a rated voltage level or a maximally expected voltage level.

In an exemplary configuration, the stack of thyristors comprises 10 thyristors or more, preferably 20 thyristors or more, even more preferably 24 thyristors. The stack is typically configured of a configuration a first series connection of thyristors and a second series connection of thyristors. The first and second series connections are disposed in an antiparallel connection, i. e. for carrying currents in one branch direction and for carrying currents in a branch direction opposite to the one branch direction, respectively.

In embodiments employing a stack of thyristors, the thyristor control circuit is configured to trigger all thyristors of the stack in a respective branch into the conductive state dependent on the fault condition.

In further embodiments employing a stack of thyristors, where a stack of thyristors is disposed in multiple branches, the thyristor control circuit is configured to trigger all thyristors of each stack in all branches into the conductive state dependent on the fault condition.

According to an aspect, the thyristor circuit may further comprise a network interface for connecting at least one of the thyristor control circuit and the current detector to a data network, in particular a global data network. The data network may be a TCP/IP network such as Internet. The thyristor control circuit and/or the current detector is operatively connected to the network interface for carrying out commands received from the data network. The commands may include a control command for controlling the thyristor control circuit, e. g. a setting command for setting the current threshold, or a circuit breaker operation command. In this case, the thyristor control circuit is adapted for carrying out the task in response to the control command. The commands may include a status request. In response to the status

request, or without prior status request, the thyristor control circuit and/or the current detector may be adapted for sending a status information to the network interface, and the network interface is then adapted for sending the status information over the network. The commands may include an update command including update data. In this case, the thyristor control circuit and/or the current sensor is adapted for initiating an update in response to the update command and using the update data. According to an embodiment, the network interface is configured to transceive a digital signal between the thyristor control circuit and/or the current detector on the one hand, and the data network on the other hand. The digital signal includes an operational command, typically a setting value for the current threshold, and/or information about the thyristor control circuit and/or the current detector or the network.

BRIEF DESCRIPTION OF THE DRAWINGS

The subject-matter of the present disclosure will be explained in more detail with reference to exemplary embodiments which are illustrated in the drawings. In the drawings:

Fig. 1 schematically shows a thyristor circuit according to an embodiment;

Fig. 2 schematically shows a thyristor circuit according to a further embodiment; and

Fig. 3 shows a flow chart of a thyristor protection method according to embodiments described herein.

Throughout the drawings, identical or like parts are provided with the same reference symbols, and the description thereof will not be repeated.

DETAILED DESCRIPTION OF EMBODIMENTS

Fig. 1 shows a thyristor circuit 100 having a branch comprising a series circuit, the series circuit configured of a snubber reactor 80 and a pair of antiparallel-connected thyristors 10, 20. A parallel reactor 81 is connected in an electrically parallel manner to the series circuit. In Fig. 1, upstream of a feed line 75, the digit "3" indicates a three-phase system in which the thyristor circuit 100 is provided in each of the three branches. However, the present disclosure is not limited to a three-phase system, and 1, 2, or 4 or more branches may as well be provided. A three-phase circuit breaker 70 is provided to electrically interrupt the feed line 75 upon reception of a circuit breaker interruption signal.

A thyristor control circuit 50 is configured such that it may selectively trigger the thyristors 10, 20. Selectively triggering, as used herein, may include the thyristor control circuit 50

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controlling each thyristor 10, 20 independently from each other. Selectively triggering, as used herein, may also include the thyristor control circuit 50 controlling multiple thyristors 10, 20, e. g. a common group of thyristors 10, 20 or a stack of thyristors 10, 20 (to be described later) together. The thyristor control circuit 50 may be connected with a gate of the respective thyristor 10, 20 via a gate trigger line 55. In the configuration shown in Fig. 1, the thyristor control circuit 50 is also connected, via a circuit breaker trigger line 56, with the three-phase circuit breaker 70 to control the circuit breaker to interrupt the phases of feed line 75.

A current sensor assembly, comprising an inductive current sensor 61 and a current value output circuit 60 coupled thereto, is connected via a current signal line 65 with the thyristor control circuit 50. The current sensor 61 detects a current flowing in the branch that contains the thyristors 10, 20. The current value output circuit 60 calculates a current value from the output of the current sensor 61 and inputs the current value, as a current signal, into the thyristor control circuit 50.

In the thyristor control circuit 50, a value of a current threshold is set in advance. The thyristor control circuit 50 determines whether the current in the branch, as input via the current signal line 65 as the current signal, exceeds the current threshold. Exceeding, as used herein, includes exceeding an absolute value of the current, i. e. exceeding is fulfilled when a positive-sign current becomes greater than a maximum value or when a negative-sign current becomes less than a minimum value. In typical embodiments, the maximum value is the reverse-sign representation of the minimum value, i. e. the absolute value of the current is taken into account.

The current threshold is typically chosen such as to represent a branch fault, such as a low resistance current or short-circuit current; in other words: The current threshold typically represents an overcurrent condition which may lead to a destruction, such as a thermal destruction, of the thyristor.

The current threshold in the present embodiment is predetermined and set in advance, i. e. prior to operation. Here, the current threshold is set taking into account the nominal operation range the thyristor, i. e. in the present case a maximum current. The maximum current may be determined depending on a blocking ability that is shifted, i.e. deteriorated, when the junction temperature rises due to the current flowing through the thyristor. The maximum current may

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also be determined to be a current that the thyristor is able to bear permanently without being destroyed.

When the thyristor control circuit 50 determines that the fault condition is present, it issues a gate trigger signal to the gates of the thyristors 10, 20 via the gate trigger line 55. A gate trigger signal, as used herein, is a signal that ensures an ignited state or pass-through state of the thyristor from its anode to its cathode, e. g. via a sufficient gate current flowing into the gate. The gate trigger signal is typically a pulsed signal, or pulse train, having a pulse width of at least 50 ms, preferably of at least 100 ms or at least 130 ms or at least 160 ms.

Together with issuing the gate trigger signal, e. g. at the same time as issuing the gate trigger signal, the thyristor control circuit 50 issues a circuit breaker trigger signal via the circuit breaker trigger line 56. The circuit breaker trigger signal instructs the circuit breaker to be operated into its interrupted position, i. e. line-open position.

In a conventional application, when a high current that fulfils the condition for a fault flows through the branch that includes the thyristors 10, 20, any thyristor 10, 20 that is still operated to be in the blocked state (i. e., the non-ignited state) may deteriorate in blocking ability due to a rise of temperature that is caused by the high current. As a result, the temperature of the pn junctions in the thyristor 10, 20 rises further. Even if a circuit breaker, such as circuit breaker 70, is operated upon detection of a fault condition, it takes several tens to several hundreds of milliseconds for the circuit breaker to operate. Blocking in case of a high junction temperature in the thyristor during this time period, or time gap, may be sufficient to permanently deteriorate or destroy the thyristor.

In contrast, according to the present disclosure, the thyristor 10, 20 is operated to be in the conductive state when a fault condition is detected. While the fault current, i.e. excessively high current, flows through the thyristor 10, 20, the pn junctions do not block, thus suppressing the temperature rise during the above-stated time period to a lower value.

In other words: The current through the thyristor valve 10, 20 is metered. If a fault occurs and the current exceeds a certain level, the triggering of the thyristors is immediately switched over from the controlled and phase sequenced triggering to continuous triggering of all phases. This allows the fault current to flow without any reduction, but the continuous firing protects the thyristor 10, 20 as it does not have to block, i. e. the thyristor 10, 20 does not have to cope with any overvoltage.

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In the fault condition, or overcurrent condition, it is not mandatory that also an overvoltage condition be present. Due to the increased junction temperature, the thyristor 10, 20 might not be able to block a certain voltage in this situation that could have been blocked without the increase of temperature.

5 In a conventional setting, the thyristor 10, 20 would be controlled such as to be in the blocking state, and thus overheat. For overheated thyristors the blocking voltage capability is highly reduced because of the increased junction temperature. In a conventional setting that does not employ the present technique, the thyristor 10, 20 is controlled to further block, which would lead to failure and/or destruction.

0 While the fault current flowing through the thyristor 10, 20 heats up the junction and reduces the blocking voltage capability of the semiconductor, by not having to block the voltage any more as the circuit breaker 70 disconnects the feed line 75 several tens to several hundreds of milliseconds later, the thyristor 10, 20 is protected. The fault, i. e. the overcurrent, is not prevented, but the blocking of the thyristor 10, 20 in the case of a fault condition is
5 prevented, which would destroy the semiconductor when having to carry this fault current.

It is noted that according to the present technique, it is not a voltage but a high current that is monitored, and which is used by the thyristor control circuit 50 to determine the condition to issue the trigger instructions. The high current, or overcurrent, stems from a converter external fault.

0 By employing the present technique, for example, a series reactor for limiting a fault current can be dispensed with. Any such reactor would add cost and due to the electrical losses increase the operating cost. It would add a component which can fail. This component is not needed any more by employing the present technique.

Fig. 2 schematically shows a thyristor circuit according to a further embodiment. In the
25 embodiment of Fig. 2, a stack 30 of serially connected thyristors and an antiparallel-connected stack 40 of serially connected thyristors is provided in place of the single thyristors 10, 20. Each stack 30, 40 comprises two or more thyristors that are connected in series, i. e. stacked. In a typical application, each stack 30, 40 comprises at least 10 or at least 20 thyristors; for example, each stack 30, 40 comprises 24 thyristors, but not limited to these
30 numbers. As in the configuration shown in Fig. 1, in the embodiment of Fig. 2 three branches are configured as shown, and a three-phase circuit breaker 70 is provided to interrupt the feed line 75 upon receiving a circuit breaker signal. Again, a parallel reactor 81 is provided per

phase. Note that three phases are merely an example, and the present disclosure is not limited to a three-phase application, but is applicable to single-phase applications or multi-phase applications having a number of phases that is different from three.

The main way of operation of the other components including the thyristor control circuit 50 is mainly as described above in the embodiment of Fig. 1.

In the embodiment of Fig. 2 employing the stacks 30, 40, it is preferred that when the fault condition is determined to be present in one or more branches, all thyristors of a stack 30, 40 or all thyristors of each stack 30, 40 in the branch(es) in which the fault condition occurred are triggered, preferably continuously triggered, into the conductive state.

In the embodiment of Fig. 2, configuration may be such that when the fault condition is determined to be present in one or more branches, all thyristors of each stack 30, 40 in all branches are triggered, preferably continuously triggered, into the conductive state.

Fig. 3 shows a flow chart of a thyristor protection method according to embodiments described herein. The method is applicable, for example, to the exemplary configuration shown in Fig. 1 or that shown in Fig. 2, but it can be applied to each suitable configuration having at least some of the constituent elements described above. The method is a thyristor protection method for protecting a thyristor 10, 20, possibly in a thyristor stack 30, 40, in at least one branch of a thyristor circuit 100.

In Fig. 3, the method is started in 1000. The processing proceeds to 1001 in which a current value representative of an electrical current flowing through the thyristor 10, 20 is detected. In 1002, it is determined whether the current value exceeds a predetermined current threshold. The current threshold is set in advance, and the current threshold is set taking into account the nominal operation range the thyristor, i. e. in the present case a maximum current that the thyristor is able to bear permanently without being destroyed.

If, in 1002, it is determined that the current value does not exceed the predetermined current threshold, the processing returns to 1001. If, in 1002, it is determined that the current value exceeds the predetermined current threshold, the processing continues in 1003. In 1003, the thyristor 10, 20 is triggered into a conductive state, preferably continuously triggered into the conductive state. The processing proceeds to 1004, where the method is ended.

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In Fig. 3, in 1003, as an example, triggering the thyristor into the conductive state typically comprises continuously triggering the thyristor for at least 50 ms or at least 100 ms or at least 130 ms or at least 160 ms.

Along with triggering in 1003, the method typically provides issuing a triggering operation to open a circuit breaker, such as the three-phase circuit breaker 70 of Figs. 1 and 2.

While the present disclosure illustrates specific embodiments and aspects in detail with reference to the drawings and the foregoing description, any such illustration and description are to be considered illustrative or exemplary and not restrictive. The invention is not limited to the disclosed embodiments.

Other variations to the disclosed embodiments will be apparent to those skilled in the art. In the claims, the term “comprise” does not exclude other elements or steps, and the indefinite article “a”/“an” does not exclude a plurality. A single processor or controller or other unit may fulfil the function of several items of the description or the claims. For example, the current controller 60, 61 may be a single unit. For example, the current controller 60, 61, or parts thereof, may be integrated with the thyristor control circuit 50. The mere fact that certain measures are recited in mutually different dependent claims does not indicate that a combination of these measures cannot be used to advantage. Any reference signs in the claims should not be construed as limiting the scope.

It is to be understood that, if any prior art publication is referred to herein, such reference does not constitute an admission that the publication forms a part of the common general knowledge in the art, in Australia or any other country. In the claims which follow and in the preceding description of the invention, except where the context requires otherwise due to express language or necessary implication, the word “comprise” or variations such as “comprises” or “comprising” is used in an inclusive sense, i.e. to specify the presence of the stated features but not to preclude the presence or addition of further features in various embodiments of the invention.

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CLAIMS

1. A thyristor circuit, comprising:

at least one branch comprising at least one thyristor;

a thyristor control circuit for selectively triggering the thyristor;

5 a current detector configured to detect a current value representative of an electrical current flowing through the thyristor and to input the detected current value into the thyristor control circuit;

wherein the thyristor control circuit is configured to determine a fault condition in which the detected current value exceeds a predetermined current threshold based on a deteriorated blocking ability of the thyristor, and, dependent on the determination
0 result, to trigger the thyristor into a conductive state;

wherein triggering the thyristor into the conductive state comprises continuously triggering the thyristor into the conductive state.

2. The thyristor circuit according to claim 1, wherein the deteriorated blocking ability of
5 the thyristor is dependent on a junction temperature of the thyristor.

3. The thyristor circuit according to any one of the preceding claims, wherein the current threshold is set based on a junction temperature characteristic of the thyristor.

4. The thyristor circuit according to any one of the preceding claims, further comprising
20 a circuit breaker, wherein the thyristor control circuit is further configured, when it continuously triggers the thyristor, to control the circuit breaker such as to interrupt the current.

5. The thyristor circuit according to any one of the preceding claims, wherein triggering
the thyristor into the conductive state includes continuously triggering the thyristor for at least 50 ms.

- 25 6. The thyristor circuit according to any one of the preceding claims, wherein the thyristor circuit comprises a plurality of branches each comprising at least one thyristor, typically three phase branches each comprising at least one thyristor.

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7. The thyristor circuit according to claim 6, wherein the thyristor control circuit is configured to determine the fault condition when at least in one of the multiple branches the detected current value exceeds the predetermined current threshold.
8. The thyristor circuit according to claim 6 or 7, wherein the thyristor control circuit is configured to trigger the at least one thyristor of each branch into the conductive state dependent on the fault condition.
9. The thyristor circuit according to any one of the preceding claims, wherein the at least one branch comprises a stack of thyristors.
10. The thyristor circuit according to claim 9, wherein the thyristor control circuit is configured to trigger all thyristors of the stack in a respective branch into the conductive state dependent on the fault condition.
11. The thyristor circuit according to claim 9, wherein the branches each comprise a stack of thyristors, wherein the thyristor control circuit is configured to trigger all thyristors of each stack in all branches into the conductive state dependent on the fault condition.
- 5 12. The thyristor circuit according to any one of the preceding claims, further comprising a network interface for connecting at least one of the thyristor control circuit and the current detector to a network, wherein the network interface is configured to transceive a digital signal between the thyristor control circuit and/or the current detector and the data network, wherein the digital signal includes an operational command, typically a setting value for the current threshold, and/or information about the thyristor control circuit and the current detector or the network.
- 20 13. A thyristor protection method for protecting a thyristor in at least one branch of a thyristor circuit, the method comprising:

25 detecting a current value representative of an electrical current flowing through the thyristor;

determining whether the current value exceeds a predetermined current threshold based on a blocking ability dependent on a junction temperature of the thyristor;

dependent on the determination result, triggering the thyristor into a conductive state,

wherein triggering the thyristor into the conductive state comprises continuously triggering the thyristor into the conductive state.

14. The thyristor circuit according to any one of claims 1 to 4, wherein triggering the thyristor into the conductive state includes continuously triggering the thyristor for at least 100 ms.
15. The thyristor circuit according to any one of claims 1 to 4, wherein triggering the thyristor into the conductive state includes continuously triggering the thyristor for at least 130 ms.
16. The thyristor circuit according to any one of claims 1 to 4, wherein triggering the thyristor into the conductive state includes continuously triggering the thyristor for at least 160 ms.

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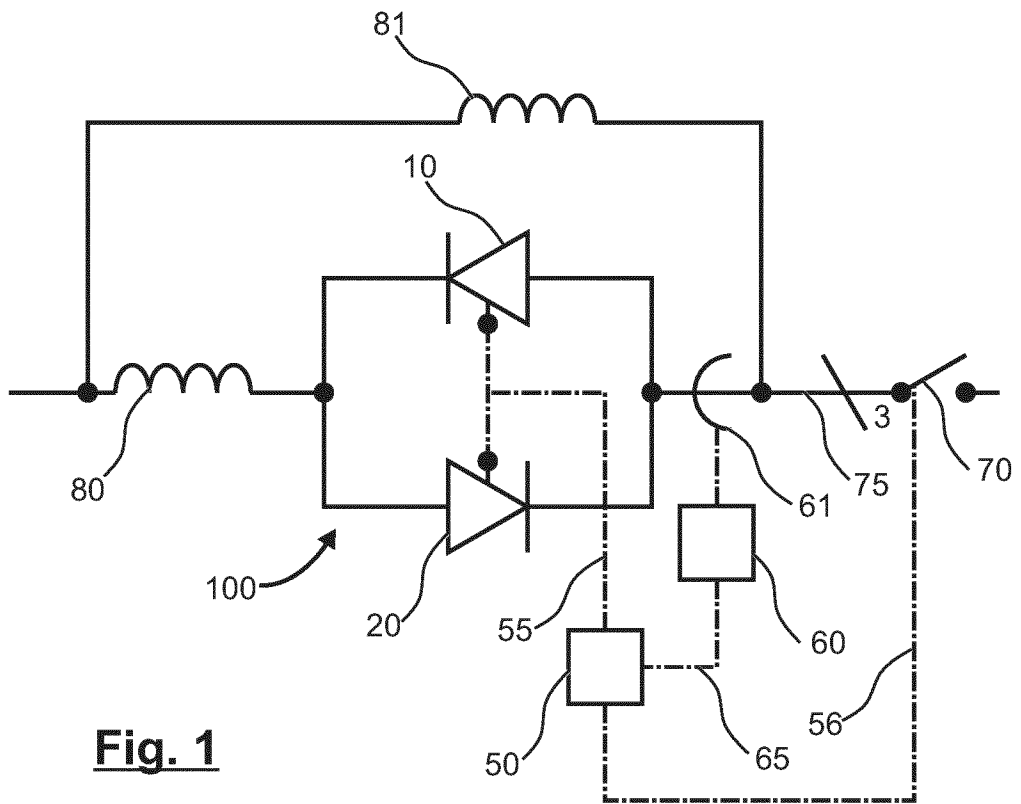


Fig. 1

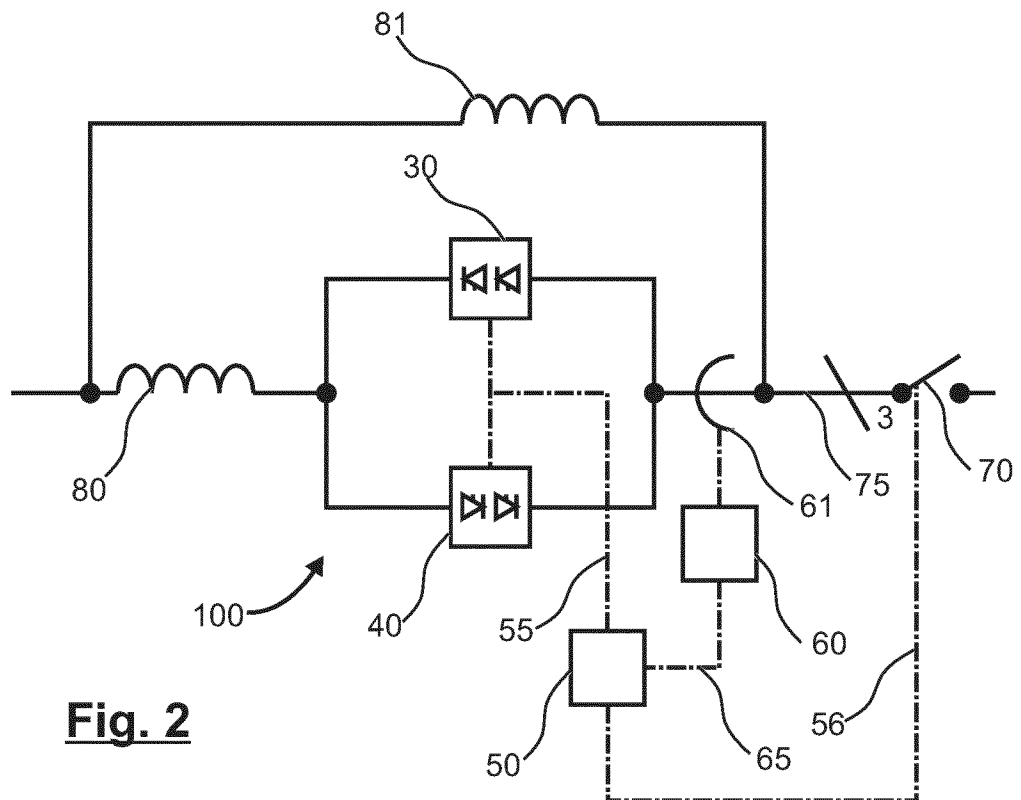


Fig. 2

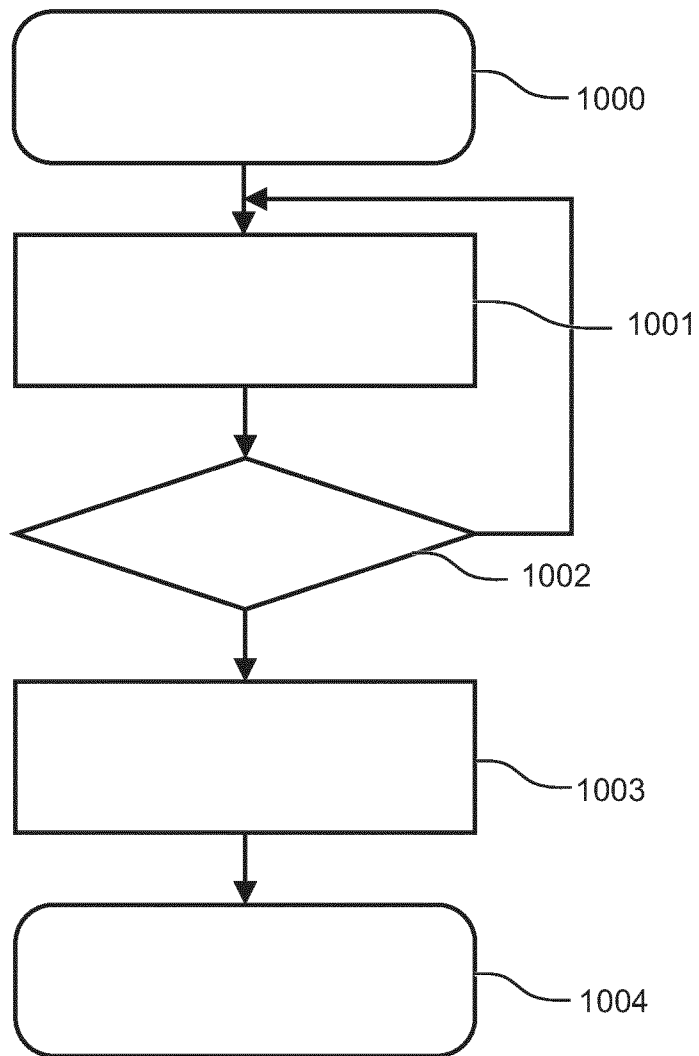


Fig. 3