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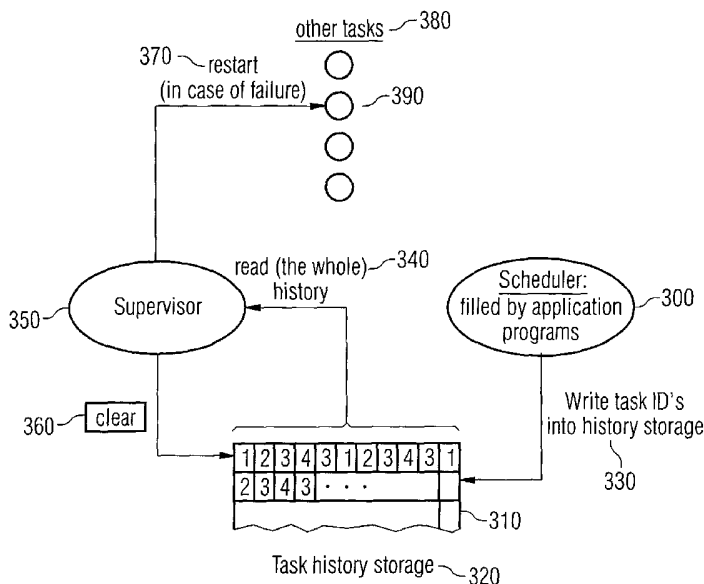
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[Continued on next page]

(54) Title: TASK SUPERVISION



(57) Abstract: The invention relates to a method for supervising a task-based data processing, wherein for a plurality of tasks the following steps are performed for each task: scheduling the task for processing, and logging the scheduling of the task by storing a task identifier in a log memory, said task identifier identifying the scheduled task and being assigned to the scheduled task. The task identifiers stored in the log memory form a task history pattern of scheduled tasks. By means of the task history pattern is detected, whether a failure appears in the task-based data processing. At least one safety measure is taken when a failure is detected. The invention relates further to a corresponding network element, communications system and computer program.

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

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 IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category ° | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
|------------|---|-----------------------|
| X | <p>TAKAHASHI S ET AL: "Trace visualization and analysis, tool for supervisory control systems" SYSTEMS, MAN, AND CYBERNETICS, 2000 IEEE INTERNATIONAL CONFERENCE ON NASHVILLE, TN, USA 8-11 OCT. 2000, PISCATAWAY, NJ, USA, IEEE, US, 8 October 2000 (2000-10-08), pages 1198-1203, XP010524818 ISBN: 0-7803-6583-6 page 1198, column 1, line 1 -column 2, line 19 page 1199, column 1, line 2 - line 10 page 1199, column 2, line 34 -page 1200, column 2, line 14</p> <p style="text-align: center;">--- -/--</p> | 1-16 |

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

| Category ° | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
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| A | US 5 664 088 A (ROMANOVSKY ALEXANDER BORISOVIT ET AL) 2 September 1997 (1997-09-02) abstract column 2, line 33 -column 3, line 5 --- | |
| A | CHENG J ET AL: "A tasking deadlock detector for Ada programs" COMPUTER SOFTWARE AND APPLICATIONS CONFERENCE, 1991. COMPSAC '91., PROCEEDINGS OF THE FIFTEENTH ANNUAL INTERNATIONAL TOKYO, JAPAN 11-13 SEPT. 1991, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 11 September 1991 (1991-09-11), pages 56-63, XP010023461 ISBN: 0-8186-2152-4 page 56, column 1, line 14 -column 2, line 19 page 62, column 2, line 42 - line 52 ----- | |

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

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| Patent document cited in search report | Publication date | Patent family member(s) | Publication date |
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