



(51) International Patent Classification:
H04L 1/18 (2006.01)

(21) International Application Number:
PCT/CN2018/071948

(22) International Filing Date:
09 January 2018 (09.01.2018)

(25) Filing Language: English

(26) Publication Language: English

(71) Applicant: NEC CORPORATION [JP/JP]; 7-1, Shiba 5-Chome, Minato-ku, Tokyo 108-8001 (JP).

(72) Inventor; and

(71) Applicant (for SC only): LIANG, Lin [CN/CN]; 6F, Building D2, Liangmaqiao Diplomatic Office Building, No. 19 Dongfangdonglu, Chaoyang District, Beijing 100600 (CN).

(72) Inventor: WANG, Gang; 6F, Building D2, Liangmaqiao Diplomatic Office Building, No. 19 Dongfangdonglu, Chaoyang District, Beijing 100600 (CN).

(74) Agent: KING & WOOD MALLESONS; 20th Floor, East Tower, World Financial Centre, No.1 Dongsanhuan Zhonglu, Chaoyang District, Beijing 100020 (CN).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IR, IS, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME,

MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: METHOD AND DEVICE FOR HARQ FEEDBACK

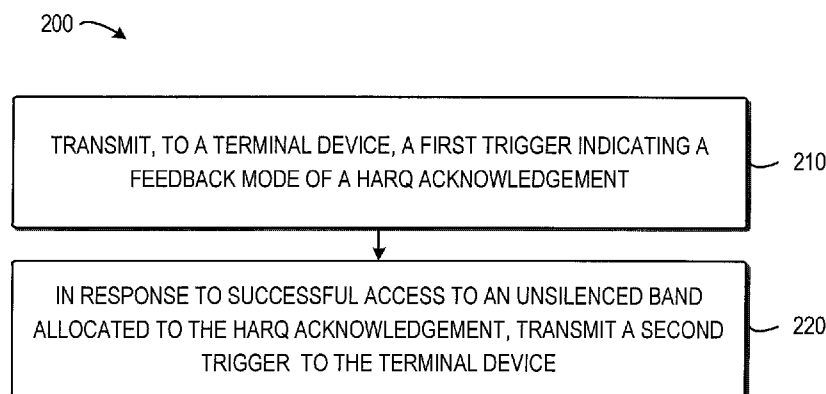


FIG. 2

(57) Abstract: Embodiments of the disclosure generally relate to feedback of a HARQ acknowledgement. A network device transmits, to a terminal device, a first trigger indicating a feedback mode of a HARQ acknowledgement. The feedback mode is selected from at least one of: a trigger mode, a timing mode, and a mixed mode. In response to successful access to an unsilenced band allocated to the HARQ acknowledgement, the network device transmits, to the terminal device, a second trigger indicating at least one target terminal device that is to feedback the HARQ acknowledgement based on the feedback mode indicated by the first trigger.

METHOD AND DEVICE FOR HARQ FEEDBACK

TECHNICAL FIELD

5 [0001] Embodiments of the present disclosure generally relate to the field of communications, and more particularly, to a method and device for feedback of a Hybrid Automatic Repeat Request (HARQ) acknowledgement on unlicensed band.

BACKGROUND

10 [0002] Unlicensed band operation has been studied and applied in 3GPP, for example, Licensed Assist Access (LAA). According to conventional standards, due to the absence of Physical Uplink Control Channel (PUCCH) in an unlicensed band and mandatory presence of a licensed primary cell, HARQ acknowledgement (also referred to as HARQ-ACK) is delivered on the licensed cell.

15 [0003] Newly-developed mobile standards, for example, a New Radio (NR) system, support unlicensed band operation, especially, feedback of HARQ-ACK on the unlicensed band. In an example scenario in NR, the feedback of HARQ-ACK is standalone deployment, which means that no licensed band is involved in the feedback. Thus, there is a need for a scheme to feedback the HARQ-ACK on unlicensed band.

20 SUMMARY

[0004] In general, embodiments of the present disclosure provide a solution for solving the issue as discussed above.

25 [0005] In a first aspect of embodiments of the present disclosure, embodiments of the present disclosure provide a method performed by a network device. The network device transmits, to a terminal device, a first trigger indicating a feedback mode of a HARQ acknowledgement. The feedback mode being selected from at least one of: a trigger mode, a timing mode, and a mixed mode. In response to successful access to an unlicensed band allocated to the HARQ acknowledgement, the network device transmits,

to the terminal device, a second trigger indicating at least one target terminal device that is to feedback the HARQ acknowledgement based on the feedback mode indicated by the first trigger.

5 [0006] In a second aspect of embodiments of the present disclosure, embodiments of the disclosure provide a network device. The network device comprises: a processor and a memory. The memory contains the memory containing program including instructions executable by the processor, the processor being configured to cause the network device to perform the method according to the first aspect of the present disclosure.

10 [0007] In a third aspect of embodiments of the present disclosure, embodiments of the present disclosure provide a computer-readable storage medium. The computer-readable storage medium having instructions stored thereon, the instructions, when being executed by a network device, causing a network device to perform the method according to the first aspect of the present disclosure.

15 [0008] In a fourth aspect of embodiments of the present disclosure, embodiments of the present disclosure provide a method performed by a terminal device. The terminal device receives, from a network device, a first trigger indicating a feedback mode of a HARQ acknowledgement, the feedback mode being selected from at least one of: a trigger mode, a timing mode, and a mixed mode. In response to the feedback mode being the trigger mode or the mixed mode, the terminal device detects, from the network device, a
20 second trigger indicating at least one target terminal device that is to feedback the HARQ acknowledgement.

25 [0009] In a fifth aspect of embodiments of the present disclosure, embodiments of the disclosure provide a terminal device. The terminal device comprises: a processor and a memory. The memory contains the memory containing program including instructions executable by the processor, the processor being configured to cause the terminal device to perform the method according to the fourth aspect of the present disclosure.

[0010] In a sixth aspect of embodiments of the present disclosure, embodiments of the present disclosure provide a computer-readable storage medium. The computer-readable storage medium having instructions stored thereon, the instructions, when being executed

by a terminal device, causing a terminal device to perform the method according to the fourth aspect of the present disclosure.

[0011] Other features and advantages of the embodiments of the present disclosure will also be apparent from the following description of specific embodiments when read in conjunction with the accompanying drawings, which illustrate, by way of example, the principles of embodiments of the disclosure.

BRIEF DESCRIPTION OF THE DRAWINGS

[0012] The above and other aspects, features, and benefits of various embodiments of the disclosure will become more fully apparent, by way of example, from the following detailed description with reference to the accompanying drawings, in which like reference numerals or letters are used to designate like or equivalent elements. The drawings are illustrated for facilitating better understanding of the embodiments of the disclosure and not necessarily drawn to scale, in which:

[0013] FIG. 1 shows a schematic diagram 100 of a conventional scheme of feedback of a HARQ acknowledgement;

[0014] FIG. 2 shows a flowchart of a method 200 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0015] FIG. 3 shows a schematic diagram 300 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0016] FIG. 4 shows a schematic diagram 400 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0017] FIG. 5 shows a schematic diagram 500 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0018] FIG. 6 shows a schematic diagram 600 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0019] FIG. 7 shows a schematic diagram 700 of feedback of a HARQ

acknowledgement in accordance with embodiments of the present disclosure;

[0020] FIG. 8 shows a schematic diagram 800 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

5 [0021] FIG. 9 shows a schematic diagram 900 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0022] FIG. 10 shows a flowchart of a method 1000 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure;

[0023] FIG. 11 shows a block diagram of an apparatus 1100 implemented at a network device in accordance with embodiments of the present disclosure;

10 [0024] FIG. 12 shows a block diagram of an apparatus 1200 implemented at a terminal device in accordance with embodiments of the present disclosure; and

[0025] FIG. 13 shows a simplified block diagram of a device 1300 that is suitable for implementing embodiments of the present disclosure.

15 **DETAILED DESCRIPTION**

[0026] The subject matter described herein will now be discussed with reference to several example embodiments. It should be understood these embodiments are discussed only for the purpose of enabling those skilled persons in the art to better understand and thus implement the subject matter described herein, rather than suggesting any limitations
20 on the scope of the subject matter.

[0027] The terminology used herein is for the purpose of describing particular embodiments only and is not intended to be limiting of example embodiments. As used herein, the singular forms “a,” “an” and “the” are intended to include the plural forms as well, unless the context clearly indicates otherwise. It will be further understood that the
25 terms “comprises,” “comprising,” “includes” and/or “including,” when used herein, specify the presence of stated features, integers, steps, operations, elements and/or components, but do not preclude the presence or addition of one or more other features,

integers, steps, operations, elements, components and/or groups thereof.

[0028] It should also be noted that in some alternative implementations, the functions/acts noted may occur out of the order noted in the figures. For example, two functions or acts shown in succession may in fact be executed concurrently or may sometimes be executed in the reverse order, depending upon the functionality/acts involved.

[0029] As used herein, the term “communication network” refers to a network following any suitable communication standards, such as New Radio Access (NR), Long Term Evolution (LTE), LTE-Advanced (LTE-A), Wideband Code Division Multiple Access (WCDMA), High-Speed Packet Access (HSPA), and so on. Furthermore, the communications between a terminal device and a network device in the communication network may be performed according to any suitable generation communication protocols, including, but not limited to, the first generation (1G), the second generation (2G), 2.5G, 2.75G, the third generation (3G), the fourth generation (4G), 4.5G, the future fifth generation (5G) communication protocols, and/or any other protocols either currently known or to be developed in the future.

[0030] Embodiments of the present disclosure may be applied in various communication systems. Given the rapid development in communications, there will of course also be future type communication technologies and systems with which the present disclosure may be embodied. It should not be seen as limiting the scope of the present disclosure to only the aforementioned system.

[0031] The term “network device” includes, but not limited to, a base station (BS), a gateway, a management entity, and other suitable device in a communication system. The term “base station” or “BS” represents a node B (NodeB or NB), an evolved NodeB (eNodeB or eNB), a NodeB in NR (gNB), a Remote Radio Unit (RRU), a radio header (RH), a remote radio head (RRH), a relay, a low power node such as a femto, a pico, and so forth.

[0032] The term “terminal device” includes, but not limited to, “user equipment (UE)” and other suitable end device capable of communicating with the network device. By

way of example, the “terminal device” may refer to a terminal, a Mobile Terminal (MT), a Subscriber Station (SS), a Portable Subscriber Station, a Mobile Station (MS), or an Access Terminal (AT).

[0033] Now some exemplary embodiments of the present disclosure will be described below with reference to the figures. Reference is first made to FIG. 1, which illustrates schematic diagram of shows a schematic diagram 100 of a conventional scheme of feedback of a HARQ acknowledgement.

[0034] The embodiments of FIG. 1 illustrate feedback of HARQ-ACK in LAA. In the embodiments, downlink data are transmitted in Physical Downlink Shared Channel (PDSCH) via an unlicensed band, and the HARQ-ACK is transmitted via PUCCH on a licensed band. Timing on unlicensed PDSCH (frame structure 3) is treated as Frequency Division Duplexing (FDD) (frame structure 1) timing. That means, if primary cell (Pcell) is FDD, PDSCH on subframe n will feedback on licensed PUCCH on subframe n+4.

[0035] It is thus can be seen that the traditional HARQ-ACK feedback is timing-based, which means that the timing between HARQ-ACK UL slot and scheduled PDSCH slot is determined. For example, it is 4 subframes for LTE-FDD, and k subframes for TD-LTE where k can be determined by TDD configuration.

[0036] For the NR system, the timing k may be indicated by Downlink Control Information (DCI) or static configured by Radio Resource Control (RRC). All these licensed band mobile communication systems use predefined and determined timing based HARQ-ACK feedback. For unlicensed band LAA, as HARQ-ACK feedback PUCCH is delivered on licensed band, it also uses the conventional timing based method.

[0037] However, if NR is deployed standalone on unlicensed band, timing based feedback may be blocked due to the failure of Listen Before Talk (LBT) when timing interval between DL and feedback UL is longer than the threshold.

[0038] In order to solve the above and other potential problems, embodiments of the present disclosure provide solutions for feedback of the HARQ-ACK on the unlicensed band. In the proposed solution, PDSCH can continuously transmit within Maximum

Channel Occupied Time (MCOT), and feedback once it is triggered. In particular, a network device transmits a first trigger indicating a feedback mode of a HARQ-ACK. In response to successful access to an unlicensed band allocated to the HARQ acknowledgement, the network device further transmits a second trigger indicating at least one target terminal device that is to feedback the HARQ-ACK based on the feedback mode indicated by the first trigger. With the two triggers, the block possibility due to LBT can be significantly reduced.

[0039] More details of embodiments of the present disclosure will be discussed with reference to figures below. FIG. 2 shows a flowchart of a method 200 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. With the method 200, the above and other potential deficiencies in the conventional approaches can be overcome. It would be appreciated by those skilled in the art that the method 200 may be implemented by a network device, for example, a gNB.

[0040] The method 200 is entered at 210, where the network device transmits, to a terminal device, a first trigger indicating a feedback mode of a HARQ acknowledgement. The feedback mode is selected from at least one of: a trigger mode, a timing mode, and a mixed mode.

[0041] According to embodiments of the present disclosure, the trigger mode represents a mode in which the HARQ-ACK is fed back according to triggers, namely, the first and second triggers as discussed. The timing mode represents a mode in which the HARQ-ACK is fed back based on timing. The mixed mode is a mode in which the timing mode is tried first, and if failed, the terminal device waits for the triggers.

[0042] In some embodiments, the first trigger may further indicate a HARQ acknowledgement group including slots and/or subframes to be fed back by the HARQ acknowledgement, a resource allocated for transmission of the HARQ acknowledgement (also referred to as “allocated resource” or “PUCCH resource” hereafter), feedback timing, and/or the like.

[0043] In some embodiments, in the timing mode, one or more bit fields may be used to indicate feedback timing. In the trigger mode or the mixed mode, the same bit field

may be reinterpreted as HARQ-ACK group and the allocated resource.

[0044] In some embodiments, the HARQ acknowledgement group may comprise all schedulable subframes after the first trigger is transmitted. In other words, the HARQ acknowledgement of all schedulable subframes may be fed back in scheduled order since last trigger.

[0045] Alternatively, in some embodiments, the HARQ acknowledgement group may just comprise a portion of the schedulable subframes. For example, a trigger window may be set according to system rudiments, specifications and so on. Assuming no repeat new data HARQ process within a trigger window, all HARQ process may be divided into several groups, and one or more groups may be fed back. In another example, all schedulable subframes in the trigger window may be indicated by the HARQ acknowledgement group, where the window size may be indicate in DCI associated with the second trigger.

[0046] At 220, in response to successful access to an unsilenced band allocated to the HARQ acknowledgement, the network device transmits a second trigger to the terminal device. The second trigger indicates one or more terminal devices (also referred to as “target terminal device” hereafter) that are to feedback the HARQ acknowledgement based on the feedback mode indicated by the first trigger. For example, the second trigger may include identifier(s) of the target terminal device(s).

[0047] In some embodiments, the second trigger is a common trigger indicating that all terminal devices are to feedback the HARQ acknowledgement. In other words, all the terminal devices are target terminal devices in this case.

[0048] Alternatively, the second trigger may be a dedicated trigger indicating that one or more dedicated terminal devices are to feedback the HARQ acknowledgement. In such a case, the target terminal devices merely include the dedicated terminal device(s).

[0049] In addition or alternatively, the second trigger may further indicate that the feedback is to be performed on the allocated resource.

[0050] According to embodiments of the present disclosure, method 200 may further

include a step of receiving the HARQ acknowledgement from one or more target terminal devices after a time interval from the transmission associated with the second trigger. In some embodiments, the time interval may be set as $25 \mu s$.

5 [0051] In view of the forgoing discussion of the embodiments, the blocking possibility of the feedback of the HARQ-ACK due to failure of LBT can be significantly reduced, and transmission efficiency can be effectively improved.

[0052] More details will be discussed with respect to FIG. 3, which shows a schematic diagram 300 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure.

10 [0053] In trigger-based HARQ-ACK feedback, the first trigger may be transmitted in DCI in each scheduled PDSCH transmission. The first trigger may carry information including 1 or 2 bits indicating HARQ-ACK feedback mode in one of trigger mode, timing mode or mix mode. The trigger mode means that transmission of HARQ-ACK feedback associating with PDSCH indicated by DCI is on hold and waits for the second
15 trigger to activate. The timing mode means that transmission of HARQ-ACK feedback associating with PDSCH indicated by DCI is transmitting based on timing information indicated by DCI. The mix mode means that transmission of HARQ-ACK feedback associating with PDSCH indicated by DCI is transmitting based on timing information indicated by DCI firstly. If timing transmission fails due to LBT, transmission of
20 HARQ-ACK feedback associating with PDSCH indicated by DCI is on hold and waits for the second trigger to activate.

25 [0054] The second trigger may be used for triggering uplink HARQ-ACK feedback transmitting. When a network device, for example, gNB, acquires the channel access, it transmits the second trigger information indicated within DCI. The second trigger may be a common trigger in DCI scrambled by common RNTI or dedicated trigger scrambled by dedicated RNTI. The second trigger may carry information about PUCCH resource and time information indicating the end of downlink transmission. The terminal device, for example, a UE, may transmit HARQ-ACK after a time interval at the ending of downlink transmission. The time interval may be $25 \mu s$ for better coexistence. In this

way, HARQ-ACK feedback may be performed under better control.

[0055] Before a terminal device transmits the HARQ-ACK, it first determines its HARQ-ACK group and then feeds back corresponding group HARQ-ACK information. HARQ-ACK group includes slots and/or subframes to be fed back by the HARQ acknowledgement.

[0056] One example of HARQ-ACK group for a UE is that HARQ-ACK group slots are all scheduled PDSCH slots since last HARQ-ACK feedback transmission and no earlier than UE's configuration of minimal feedback slots. Once a second trigger, which is a common trigger, is received by the UE, the UE will know that all UEs will feedback HARQ-ACK corresponding to their HARQ-ACK groups on their predefined resources. As shown in FIG. 3, a UE receives PDSCH indicated by DCI trigger mode in slots 1,2,3,4 since last feedback. In this case, the HARQ-ACK group is {1,2,3,4}. Once UE successfully decodes second trigger DCI scrambled by common RNTI in slot 9, it will feedback HARQ-ACK of its HARQ-ACK group {1,2,3,4} to gNB on its allocated resource.

[0057] Another example of HARQ-ACK group is that divided slots into group and each group corresponding to a HARQ-ACK group periodically. For example, slots number in each group is 4 and group size is 5. That means, absolute slot 0,1,2,3 is group 0, absolute slot 4,5,6,7 is group 1, ..., absolute slot 16,17,18,19 is group 4 and absolute slot 20,21,22,23 is group 0 periodically. As shown in FIG. 3, the UE successfully decodes the DCI which includes the second trigger and is scrambled by dedicated RNTI in slot 9. The UE thus can know that the second trigger indicates a feedback HARQ-ACK group index 0. Then the nearest group with indicated group index will be transmitted in this occasion. For example, slots 0,1,2,3 are feedback, even slot 0 has no PDSCH and slot 4 has PDSCH, slot 0 should feedback Discontinuous Transmission (DTX) to indicate that PDCCH is not received in this feedback and slot 4 should wait for the occasion indicating group index 1. Group index indication may be a bitmap, which means that group 0 and group 1 can be indicated at the same time in one second trigger dedicated DCI.

[0058] In some embodiments, the feedback of the HARQ-ACK may be encoded in a

variety of ways. For example, 3 HARQ-bits for 2 subframes feedback per codeword may be implemented as follows. In this case, 2 subframes have 9 states, and 3 bits of one codeword have 8 states. So, there is at least one mapping of 2 to 1, and the rests are 1 to 1 mapping. As “NACK NACK” and “DTX DTX” means that the channel condition is bad, they can be encoded together into one codeword. The rests are one to one mapping into each codeword. In this way, the mismatch between gNB and UE due to failed detection of PDCCH can be solved by nearly one to one mapping. Table 1 shows an example of encoding of the HARQ-bits.

Table 1

Encoding	Subframe 2*i	Subframe 2*i+1
0	ACK	ACK
1	ACK	NACK
2	ACK	DTX
3	NACK	ACK
4	NACK	DTX
5	DTX	ACK
6	DTX	NACK
7	NACK/DTX	NACK/DTX

[0059] It is to be understood that the above example of the encoding of the HARQ-ACK is discussed for illustrations, rather than limitation. Those skilled in the art would appreciate that the encoding can be implemented other suitable ways within the scope of the present disclosure.

[0060] More embodiments of the present disclosure are now discussed below.

[0061] Generally, before transmission on an unlicensed band, the network node needs to sense the state of a channel, that is, whether the channel is busy or idle. After sensing the channel is idle for a while, the network node can access the channel. This process is referred to as Clear Channel Access (CCA) or LBT.

[0062] In LAA, it is specified 4 categories of LBT, namely, Cat. 1 to 4. Two most frequently-used types are:

[0063] Cat.2: LBT without back off, e.g. $25\ \mu s$ LBT; and

[0064] Cat.4: LBT with random back off, e.g. window size is 7, first generate a random number $[0, 7]$, take 3 for example. And LBT time is $34 + 3 \times 9 = 61\ \mu s$.

[0065] Maximum Channel Occupied Time (MCOT) is the maximum occupied time of a single access. Within MCOT, a cat.2 LBT can be used. While outside MCOT, Cat.4 LBT is needed for fair coexistence.

[0066] To achieving better performance and simpler implementation for standalone NR unlicensed band HARQ-ACK feedback, according to embodiments of the present disclosure, HARQ timing is designed for self-contained feedback to satisfy LBT.

[0067] In some embodiments, a specific TA offset and fractional and/or complete Orthogonal Frequency Division Multiplexing (OFDM) symbols for LBT are employed for HARQ timing.

[0068] After successful of CCA, the network device, for examples a gNB, starts to transmit downlink data to a terminal device, for example, a UE. When the gNB finishes transmission, the UE may feedback HARQ-ACK after sensing the channel is idle for a time interval (also referred to as a “gap” which is denoted as “ t_{ack} ”). In some embodiments, the length of the gap may be set as $25\ \mu s$. That is, $t_{ack} = 25\ \mu s$.

[0069] FIG. 4 shows a schematic diagram 400 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. As shown in FIG. 4, gNB can continue transmitting downlink data after sensing channel is idle for a gap t_{ack} following uplink HARQ-ACK feedback signals. The gap is indicated by 410 in FIG. 4.

[0070] For an unlicensed band, reference TA offset is at least t_{ack} , i.e. $768T_s$ ($768 \times 64T_c$, $25\ \mu s$). As for LTE-FDD, Reference TA offset is $0T_s$, and as for LTE-TDD, Reference TA offset is $624T_s$ ($20.3125\ \mu s$).

[0071] Timing between PDSCH data and PUCCH HARQ-ACK may be indicated through DCI. FIG. 5 shows a schematic diagram 500 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. Specifically, FIG. 5 shows an example of timing delay of 2 subframes. Within MCOT, Cat.2 LBT is used for transmission.

[0072] From point of coverage, PUCCH feedback may repeat based on PUCCH format symbol length N_PUCCH and PUCCH resource symbol length N_ACK for HARQ-ACK feedback. N_PUCCH is format indicated by high layer or DCI, and N_ACK may be indicated by DCI or RRC or calculated by Slot Format Indication (SFI) configuration.

[0073] For example, in PUCCH format 1, $N_PUCCH = 2$. SFI indicates 9 OFDM symbols for DL, so there are 3 complete symbols and 1 fractional symbol ($66.7+4.7-25-25=21.4 \mu s$) left for 15KHz Subcarrier Spacing (SCS).

[0074] According to embodiments of the present disclosure, there may be several ways to calculate fractional symbol start position and complete symbol repetition.

[0075] In some embodiments, the time-continuous signal $S_I^{(p,\mu)}(t)$ on antenna port p and subcarrier spacing configuration μ for OFDM symbol I in a subframe for any physical channel or signal is transmitted within $0 \leq t < (N_u^\mu + N_{CP,I}^\mu)T_c$. In the embodiments, the size of various fields in the time domain is expressed as a number of time units $T_c = 1/(\Delta f_{\max} \cdot N_f)$, where $\Delta f_{\max} = 480 \cdot 10^3$ Hz and $N_f = 4096$.

[0076] The starting position of OFDM symbol I for subcarrier spacing configuration μ in a subframe is given by

$$t_{\text{start},I}^\mu = \begin{cases} 0 & I = 0 \\ t_{\text{start},I-1}^\mu + (N_u^\mu + N_{CP,I-1}^\mu) \cdot T_c & \text{otherwise} \end{cases} \quad (1)$$

where

$$N_u^\mu = 2048\kappa \cdot 2^{-\mu}$$

$$N_{CP,I}^\mu = \begin{cases} 512\kappa \cdot 2^{-\mu} & \text{extended cyclic prefix} \\ 144\kappa \cdot 2^{-\mu} + 16\kappa & \text{normal cyclic prefix, } I = 0 \text{ or } I = 7 \cdot 2^\mu \\ 144\kappa \cdot 2^{-\mu} & \text{normal cyclic prefix, } I \neq 0 \text{ and } I \neq 7 \cdot 2^\mu \end{cases}$$

[0077] Let $I_{PUCCH,start}^\mu$ indicates the start OFDM symbol of fractional symbol and $I_{PDSCH,end}^\mu$ indicates the end of PDSCH symbol. $T_{TA} = (N_{TA} + N_{TA,offset})T_c + T_{gap}$ is a time interval where $N_{TA} + N_{TA,offset}$ is timing advance (TA) signaled by gNB and T_{gap} is interval of downlink and uplink switch time. In some embodiments, T_{gap} is set as 25us on unlicensed band.

[0078] N_{PUCCH}^μ is the length of PUCCH OFDM symbol for uplink HARQ-ACK transmission, and N_{start}^μ is the start sample within fractional OFDM symbol corresponding to sample rate $1/T_c$.

[0079] With above definition, UE can calculate the start OFDM symbol of fractional symbol $I_{PUCCH,start}^\mu$ and the start sample within fractional OFDM symbol N_{start}^μ as,

$$\begin{aligned} \min_{I_{PUCCH,start}^\mu} T_{TA} &\leq (t_{start, I_{PUCCH,start}^\mu+1}^\mu - t_{start, I_{PDSCH,end}^\mu+1}^\mu) \\ N_{start}^\mu &= T_{TA} / T_c - (t_{start, I_{PUCCH,start}^\mu}^\mu - t_{start, I_{PDSCH,end}^\mu+1}^\mu) / T_c \end{aligned} \quad (2)$$

[0080] The time-continuous signal $S_I^{(p,\mu)}(t)$ transmitted by the UE is defined below.

[0081] If I is $I_{PUCCH,start}^\mu$, that is, in the case of a fractional symbol,

$$S_I^{(p,\mu)}(t) = \begin{cases} 0 & 0 \leq t < N_{start}^\mu T_c \\ S_{I+1}^{(p,\mu)}(t - N_{CP,I}^\mu T_c) & N_{start}^\mu T_c \leq t < (N_u^\mu + N_{CP,I}^\mu) T_c \end{cases} \quad (3)$$

Else, in the case of a complete symbol,

$$S_I^{(p,\mu)}(t - N_{CP,I}^\mu T_c) = S_{I+N_{PUCCH}^\mu}^{(p,\mu)}(t - N_{CP,I+N_{PUCCH}^\mu}^\mu T_c). \quad (4)$$

[0082] In this way, UE may start to transmit uplink signal, for example, the HARQ-ACK, after the interval of T_{gap} under any subcarrier spacing configuration μ .

The proper repetition of the fractional symbol and the complete symbol may be afforded for receiver diversity.

[0083] FIG. 6 shows a schematic diagram 600 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. In particular, FIG. 6 shows an example of 2 complete symbols PUCCH and 1 fractional symbol PUCCH for SCS 15KHz, where fractional symbol is generated on symbol 11 and complete symbols are generated on symbols 12 and 13, respectively.

[0084] FIG. 7 shows a schematic diagram 700 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. In particular, FIG. 7 shows an example of SCS 60KHz within one subframe. As shown in FIG. 7, there are 1 fractional symbol and 4 complete symbols. The complete symbols include 2 PUCCH symbols and 2 repetition symbols. The duration of the gap lasts more than 1 symbol.

[0085] For the case the whole slot is for PUCCH, it starts transmission on fractional symbol 2 based on equations (2)-(4). For the case fractional slot is for PUCCH, PDSCH multiple slot scheduling should indicate the DL OFDM end symbol in PUCCH feedback slot. Transport block is rate matched based on the sum of DL OFDM symbols.

[0086] Still more embodiments of the present disclosure are discussed below.

[0087] For feedback of the HARQ-ACK, there is a need for a mechanism of resource allocation, especially for occasional or window-based feedback. To solve the problem and potential related issues, embodiments of the present disclosure provide solutions of resource allocation and/or encoding of HARQ-ACK for HARQ-ACK window.

[0088] FIG. 8 shows a schematic diagram 800 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. In the example shown in FIG. 8, when PDSCH cannot feedback within a time window (for example, Maximum Channel Occupied Time (MCOT)), a timer can be started. In this example, the time window is referred to as "MCOT1", which includes subframes 1 to 3. Once another MCOT (referred to as "MCOT2") is detected before expiration of the timer, the HARQ-ACK can be fed back in MCOT2. Otherwise, the HARQ-ACK can be fed back on another timing or be abandoned.

[0089] In some embodiments, if a UE cannot feedback due to failure of LBT, it may wait for next feedback occasion within a time window, also referred to as a "HARQ-ACK window". If a UE can access the channel in a feedback occasion, it may feedback information of current HARQ-ACK, as well as information of previous HARQ-ACK which is blocked due to failure of LBT.

[0090] FIG. 9 shows a schematic diagram 900 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. In the example shown in FIG. 9, the HARQ-ACK window is 4, which means that the HARQ-ACK window includes 4 subframes. In subframes 1-3, the UE cannot access the channel and cannot feedback the HARQ-ACK at positions 910, 920 and 930. In the example, the UE successfully accesses the channel in subframe 4 and thus feeds back the HARQ-ACK at position 940.

[0091] In some embodiments, there are several encoding schemes for HARQ-ACK feedback, for example, schemes based on multiple codebook, bundling, and partial bundling.

[0092] In embodiments using the scheme of multiple codebook, assuming n_1 is the resource in the 1st occasion, in i -th occasion, the resource n_i may be denoted as the following:

$$n_i = (n_1 + (i - 1)N_{\text{PUCCH}} / N_{\text{window}}) \bmod N_{\text{PUCCH}} \quad (5)$$

where N_{PUCCH} represents the number of PUCCH resources, and N_{window} represents the HARQ-ACK window size.

[0093] In embodiments using the scheme of bundling, all HARQ-ACKs that need to be fed back in the HARQ-ACK window are bundled into one HARQ-ACK, for example, 1 bit. Still referring to the example shown in FIG. 9, if downlink (DL) data on the subframes 1-4 are correctly received by the UE, the UE will transmit a positive HARQ-ACK, for example, "1" or "ACK", to the gNB. If any of the DL data on the subframes 1-4 is not correctly received by the UE, the UE will transmit a negative HARQ-ACK, for example, "0" or "NACK", to the gNB.

[0094] In embodiments using the scheme of partial bundling, resource n is used by current timing HARQ-ACK, all previous HARQ-ACKs in the window are bundled into one HARQ-ACK, for example, 1 bit, using another predefined resource e.g. $n+1$. Still referring to the example shown in FIG. 9, if the partial bundling is employed, the HARQ-ACK of DL data transmitted on subframe 4 is transmitted on resource n , while the HARQ-ACKs of DL data transmitted on subframes 1-3 are bundled into 1 bit which is

transmitted on resource $n+1$.

[0095] FIG. 10 shows a flowchart of a method 1000 of feedback of a HARQ acknowledgement in accordance with embodiments of the present disclosure. The method 1000 may be implemented by a terminal device, for example, a UE.

5 [0096] The method 1000 is entered at 1010, where the terminal device receives, from a network device, a first trigger indicating a feedback mode of a HARQ acknowledgement. The feedback mode is selected from at least one of: a trigger mode, a timing mode, and a mixed mode.

10 [0097] According to embodiments of the present disclosure, the trigger mode represents a mode in which the HARQ-ACK is fed back according to triggers, namely, the first and second triggers as discussed. The timing mode represents a mode in which the HARQ-ACK is fed back based on timing. The mixed mode is a mode in which the timing mode is tried first, and if failed, the terminal device waits for the triggers.

15 [0098] In some embodiments, the first trigger may further indicate a HARQ acknowledgement group including slots and/or subframes to be fed back by the HARQ acknowledgement, a resource allocated for transmission of the HARQ acknowledgement, feedback timing, and/or the like.

20 [0099] In some embodiments, the HARQ acknowledgement group may comprise all schedulable subframes after the first trigger is transmitted. In other words, the HARQ acknowledgement of all schedulable subframes may be fed back in scheduled order since last trigger.

25 [00100] At 1020, in response to the feedback mode being the trigger mode or the mixed mode, the terminal device detects a second trigger from the network device. The second trigger indicates one or more terminal devices (also referred to as “target terminal device” hereafter) that are to feedback the HARQ acknowledgement based on the feedback mode indicated by the first trigger. For example, the second trigger may include identifier(s) of the target terminal device(s).

[00101] In some embodiments, the second trigger is a common trigger indicating that all

terminal devices are to feedback the HARQ acknowledgement. In other words, all the terminal devices are target terminal devices in this case.

[00102] Alternatively, the second trigger may be a dedicated trigger indicating that one or more dedicated terminal devices are to feedback the HARQ acknowledgement. In
5 such a case, the target terminal devices merely include the dedicated terminal device(s).

[00103] In addition or alternatively, the second trigger may further indicate that the feedback is to be performed on the allocated resource.

[00104] According to embodiments of the present disclosure, the terminal device may further determine whether the terminal device is to feedback the HARQ acknowledgement
10 based on the second trigger. For example, the terminal device may determine whether it per se is a target terminal device based on identification(s) of the target terminal device(s) indicated in the second trigger. If the terminal device determines that it per se is to feedback the HARQ acknowledgement, it transmits the HARQ acknowledgement to the network device after a time interval from the reception associated with the second trigger.

[00105] Now reference is made to FIG. 11, which shows a block diagram of an apparatus 1100 implemented at a network device in accordance with embodiments of the present disclosure. It would be appreciated that the apparatus 1100 may be implemented
15 at a network device, for example, a gNB.

[00106] As shown, the apparatus 1100 includes a first transmitting unit 1110 and a
20 second transmitting unit 1120. The first transmitting unit 1110 is configured to transmit, to a terminal device, a first trigger indicating a feedback mode of a HARQ acknowledgement, the feedback mode being selected from at least one of: a trigger mode, a timing mode, and a mixed mode. The second transmitting unit 1120 is configured to, in response to successful access to an unsilenced band allocated to the HARQ
25 acknowledgement, transmit, to the terminal device, a second trigger indicating at least one target terminal device that is to feedback the HARQ acknowledgement based on the feedback mode indicated by the first trigger.

[00107] In some embodiments, the first trigger further indicates at least one of: a HARQ

acknowledgement group including slots and/or subframes to be fed back by the HARQ acknowledgement; a resource allocated for transmission of the HARQ acknowledgement; and feedback timing.

5 [00108] In some embodiments, the HARQ acknowledgement group comprises all or a portion of schedulable subframes after the first trigger is transmitted.

[00109] In some embodiments, the second trigger is a common trigger indicating that all terminal devices are to feedback the HARQ acknowledgement, or wherein the second trigger is a dedicated trigger indicating that a dedicated terminal device is to feedback the HARQ acknowledgement.

10 [00110] In some embodiments, the apparatus 1100 may further include a receiving unit configured to receive the HARQ acknowledgement from a target terminal device after a time interval from the transmission associated with the second trigger.

15 [00111] FIG. 12 shows a block diagram of an apparatus 1200 implemented at a terminal device in accordance with embodiments of the present disclosure. It would be appreciated that the apparatus 1200 may be implemented at a terminal device, for example, a UE.

20 [00112] As shown, the apparatus 1200 includes a first receiving unit 1210 and a second receiving unit 1220. The first receiving unit 1210 is configured to receive, from a network device, a first trigger indicating a feedback mode of a HARQ acknowledgement, the feedback mode being selected from at least one of: a trigger mode, a timing mode, and a mixed mode. The second receiving unit 1220 is configured to, in response to the feedback mode being the trigger mode or the mixed mode, detect, from the network device, a second trigger indicating at least one target terminal device that is to feedback the HARQ acknowledgement.

25 [00113] In some embodiments, the first trigger further indicates at least one of: a HARQ acknowledgement group including slots and/or subframes to be fed back by the HARQ acknowledgement; a resource allocated for transmission of the HARQ acknowledgement; and feedback timing.

[00114] In some embodiments, the HARQ acknowledgement group comprises all or a portion of schedulable subframes after the first trigger is transmitted.

[00115] In some embodiments, the second trigger is a common trigger indicating that all terminal devices are to feedback the HARQ acknowledgement, or wherein the second trigger is a dedicated trigger indicating that a dedicated terminal device is to feedback the HARQ acknowledgement.

[00116] In some embodiments, the apparatus 1100 may further include a determining unit configured to determine whether the terminal device is to feedback the HARQ acknowledgement based on the second trigger; and a transmitting unit configured to, in response to determining that the terminal device is to feedback the HARQ acknowledgement, transmit the HARQ acknowledgement to the network device after a time interval from the reception associated with the second trigger.

[00117] It is also to be noted that the apparatus 1100 or 1200 may be respectively implemented by any suitable technique either known at present or developed in the future. Further, a single device shown in FIG. 2 or 10 may be alternatively implemented in multiple devices separately, and multiple separated devices may be implemented in a single device. The scope of the present disclosure is not limited in these regards.

[00118] It is noted that the apparatus 1100 or 1200 may be configured to implement functionalities as described with reference to FIG. 2 or 10. Therefore, the features discussed with respect to the method 200 may apply to the corresponding components of the apparatus 1100, and the features discussed with respect to the method 1000 may apply to the corresponding components of the apparatus 1200. It is further noted that the components of the apparatus 1100 or 1200 may be embodied in hardware, software, firmware, and/or any combination thereof. For example, the components of the apparatus 1100 or 1200 may be respectively implemented by a circuit, a processor or any other appropriate device. Those skilled in the art will appreciate that the aforesaid examples are only for illustration not limitation.

[00119] In some embodiment of the present disclosure, the apparatus 1100 or 1200 may comprise at least one processor. The at least one processor suitable for use with

embodiments of the present disclosure may include, by way of example, both general and special purpose processors already known or developed in the future. The apparatus 1100 or 1200 may further comprise at least one memory. The at least one memory may include, for example, semiconductor memory devices, e.g., RAM, ROM, EPROM, EEPROM, and flash memory devices. The at least one memory may be used to store program of computer executable instructions. The program can be written in any high-level and/or low-level compliable or interpretable programming languages. In accordance with embodiments, the computer executable instructions may be configured, with the at least one processor, to cause the apparatus 1100 to at least perform according to the method 200 as discussed above and to cause the apparatus 1200 to at least perform according to the method 1000 as discussed above.

[00120] Based on the above description, the skilled in the art would appreciate that the present disclosure may be embodied in an apparatus, a method, or a computer program product. In general, the various exemplary embodiments may be implemented in hardware or special purpose circuits, software, logic or any combination thereof. For example, some aspects may be implemented in hardware, while other aspects may be implemented in firmware or software which may be executed by a controller, microprocessor or other computing device, although the disclosure is not limited thereto. While various aspects of the exemplary embodiments of this disclosure may be illustrated and described as block diagrams, flowcharts, or using some other pictorial representation, it is well understood that these blocks, apparatus, systems, techniques or methods described herein may be implemented in, as non-limiting examples, hardware, software, firmware, special purpose circuits or logic, general purpose hardware or controller or other computing devices, or some combination thereof.

[00121] The various blocks shown in FIG. 11 or 12 may be viewed as method steps, and/or as operations that result from operation of computer program code, and/or as a plurality of coupled logic circuit elements constructed to carry out the associated function(s). At least some aspects of the exemplary embodiments of the disclosures may be practiced in various components such as integrated circuit chips and modules, and that the exemplary embodiments of this disclosure may be realized in an apparatus that is

embodied as an integrated circuit, FPGA or ASIC that is configurable to operate in accordance with the exemplary embodiments of the present disclosure.

[00122] Fig. 13 is a simplified block diagram of a device 1300 that is suitable for implementing embodiments of the present disclosure. As shown, the device 1300 includes one or more processors 1310, one or more memories 1320 coupled to the processor(s) 1310, one or more transmitters and/or receivers (TX/RX) 1340 coupled to the processor 1310.

[00123] The processor 1310 may be of any type suitable to the local technical network, and may include one or more of general purpose computers, special purpose computers, microprocessors, digital signal processors (DSPs) and processors based on multicore processor architecture, as non-limiting examples. The device 1300 may have multiple processors, such as an application specific integrated circuit chip that is slaved in time to a clock which synchronizes the main processor.

[00124] The memory 1320 may be of any type suitable to the local technical network and may be implemented using any suitable data storage technology, such as a non-transitory computer readable storage medium, semiconductor based memory devices, magnetic memory devices and systems, optical memory devices and systems, fixed memory and removable memory, as non-limiting examples.

[00125] The memory 1320 stores at least a part of a program 1330. The TX/RX 1340 is for bidirectional communications. The TX/RX 1340 has at least one antenna to facilitate communication, though in practice a terminal device or a network device mentioned in this disclosure may have several ones. The communication interface may represent any interface that is necessary for communication with other network elements.

[00126] The program 1330 is assumed to include program instructions that, when executed by the associated processor 1310, enable the device 1300 to operate in accordance with the embodiments of the present disclosure, as discussed herein with reference to Figs. 2 and 10. That is, embodiments of the present disclosure can be implemented by computer software executable by the processor 1310 of the device 1300, or by hardware, or by a combination of software and hardware.

[00127] While this specification contains many specific implementation details, these should not be construed as limitations on the scope of any disclosure or of what may be claimed, but rather as descriptions of features that may be specific to particular embodiments of particular disclosures. Certain features that are described in this specification in the context of separate embodiments can also be implemented in combination in a single embodiment. Conversely, various features that are described in the context of a single embodiment can also be implemented in multiple embodiments separately or in any suitable sub-combination. Moreover, although features may be described above as acting in certain combinations and even initially claimed as such, one or more features from a claimed combination can in some cases be excised from the combination, and the claimed combination may be directed to a sub-combination or variation of a sub-combination.

[00128] Similarly, while operations are depicted in the drawings in a particular order, this should not be understood as requiring that such operations be performed in the particular order shown or in sequential order, or that all illustrated operations be performed, to achieve desirable results. In certain circumstances, multitasking and parallel processing may be advantageous. Moreover, the separation of various system components in the embodiments described above should not be understood as requiring such separation in all embodiments, and it should be understood that the described program components and systems can generally be integrated together in a single software product or packaged into multiple software products.

[00129] Various modifications, adaptations to the foregoing exemplary embodiments of this disclosure may become apparent to those skilled in the relevant arts in view of the foregoing description, when read in conjunction with the accompanying drawings. Any and all modifications will still fall within the scope of the non-limiting and exemplary embodiments of this disclosure. Furthermore, other embodiments of the disclosures set forth herein will come to mind to one skilled in the art to which these embodiments of the disclosure pertain having the benefit of the teachings presented in the foregoing descriptions and the associated drawings.

[00130] Therefore, it is to be understood that the embodiments of the disclosure are not to be limited to the specific embodiments disclosed and that modifications and other embodiments are intended to be included within the scope of the appended claims. Although specific terms are used herein, they are used in a generic and descriptive sense

5 only and not for purpose of limitation.

WHAT IS CLAIMED IS:

1. A method implemented at a network device, comprising:
transmitting, to a terminal device, a first trigger indicating a feedback mode of a
5 Hybrid Automatic Repeat Request (HARQ) acknowledgement, the feedback mode being
selected from at least one of: a trigger mode, a timing mode, and a mixed mode; and
in response to successful access to an unsilenced band allocated to the HARQ
acknowledgement, transmitting, to the terminal device, a second trigger indicating at least
one target terminal device that is to feedback the HARQ acknowledgement based on the
10 feedback mode indicated by the first trigger.
2. The method according to Claim 1, wherein the first trigger further indicates
at least one of:
a HARQ acknowledgement group including slots and/or subframes to be fed back
15 by the HARQ acknowledgement;
a resource allocated for transmission of the HARQ acknowledgement; and
feedback timing.
3. The method according to Claim 2, wherein the HARQ acknowledgement
20 group comprises all or a portion of schedulable subframes after the first trigger is
transmitted.
4. The method according to Claim 1, wherein the second trigger is a common
trigger indicating that all terminal devices are to feedback the HARQ acknowledgement,
25 or
wherein the second trigger is a dedicated trigger indicating that a dedicated
terminal device is to feedback the HARQ acknowledgement.
5. The method according to Claim 1, further comprising:
30 receiving the HARQ acknowledgement from a target terminal device after a time
interval from the transmission associated with the second trigger.

6. A method implemented at a terminal device, comprising:
receiving, from a network device, a first trigger indicating a feedback mode of a Hybrid Automatic Repeat Request (HARQ) acknowledgement, the feedback mode being selected from at least one of: a trigger mode, a timing mode, and a mixed mode; and
5 in response to the feedback mode being the trigger mode or the mixed mode, detecting, from the network device, a second trigger indicating at least one target terminal device that is to feedback the HARQ acknowledgement.

7. The method according to Claim 6, wherein the first trigger further indicates
10 at least one of:
a HARQ acknowledgement group including slots and/or subframes to be fed back by the HARQ acknowledgement;
a resource allocated for transmission of the HARQ acknowledgement; and
feedback timing.

8. The method according to Claim 7, wherein the HARQ acknowledgement group comprises all or a portion of schedulable subframes after the first trigger is transmitted.

9. The method according to Claim 6, wherein the second trigger is a common trigger indicating that all terminal devices are to feedback the HARQ acknowledgement,
20 or
wherein the second trigger is a dedicated trigger indicating that a dedicated terminal device is to feedback the HARQ acknowledgement.

10. The method according to Claim 6, further comprising:
determining whether the terminal device is to feedback the HARQ acknowledgement based on the second trigger; and
in response to determining that the terminal device is to feedback the HARQ
30 acknowledgement, transmitting the HARQ acknowledgement to the network device after a time interval from the reception associated with the second trigger.

11. A network device, comprising:

a processor; and

a memory, the memory containing program including instructions executable by the processor, the processor being configured to cause the network device to perform the method according to any of Claims 1-5.

12. A terminal device, comprising:

a processor; and

a memory, the memory containing program including instructions executable by the processor, the processor being configured to cause the terminal device to perform the method according to any of Claims 6-10.

13. A computer-readable storage medium having instructions stored thereon, the instructions, when being executed by a network device, causing the network device to perform the method according to any of Claims 1-5.

14. A computer-readable storage medium having instructions stored thereon, the instructions, when being executed by a terminal device, causing the terminal device to perform the method according to any of Claims 6-10.

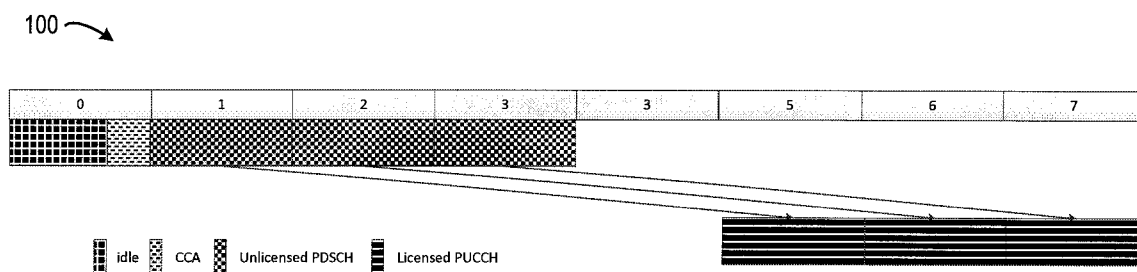


FIG. 1

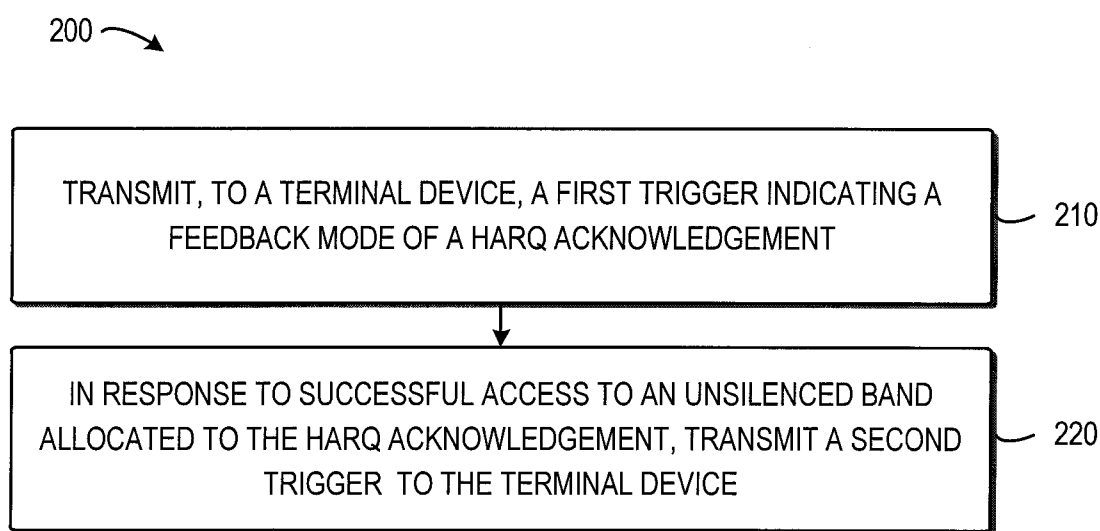


FIG. 2

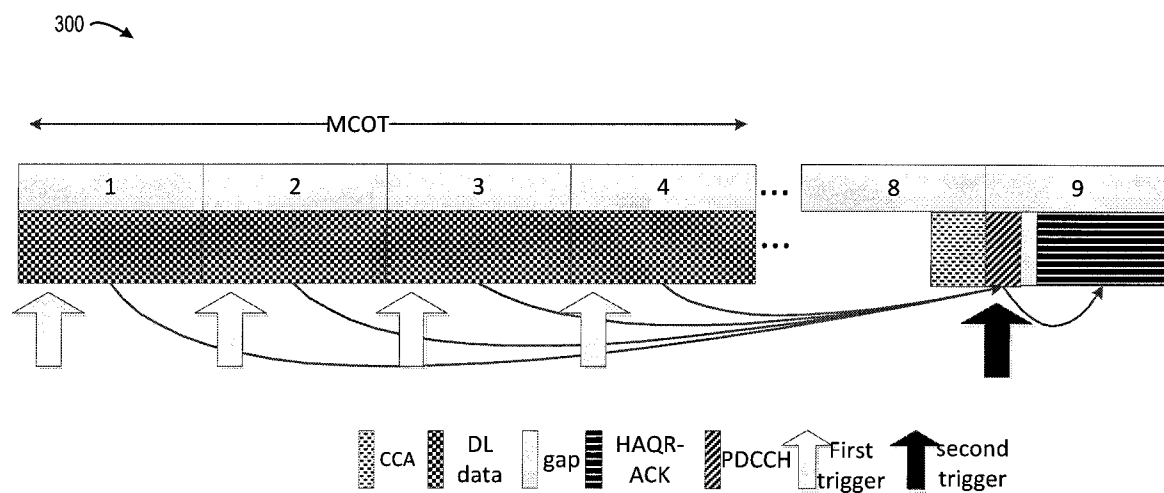


FIG. 3

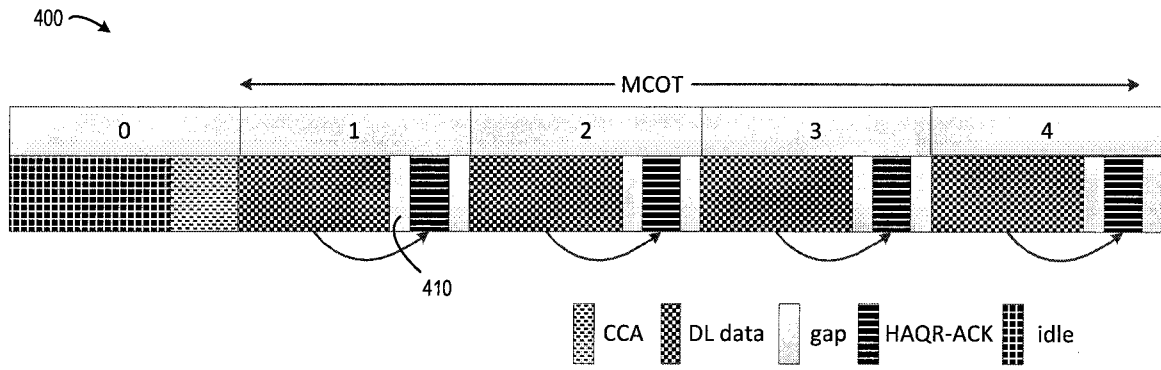


FIG. 4

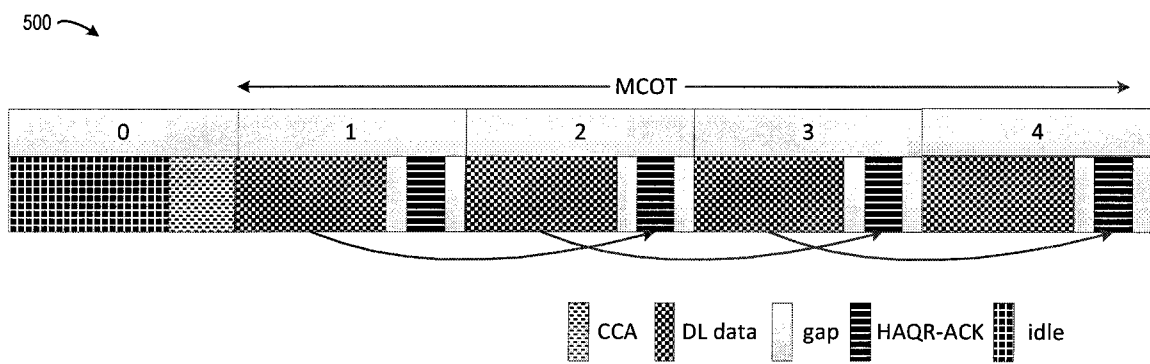


FIG. 5

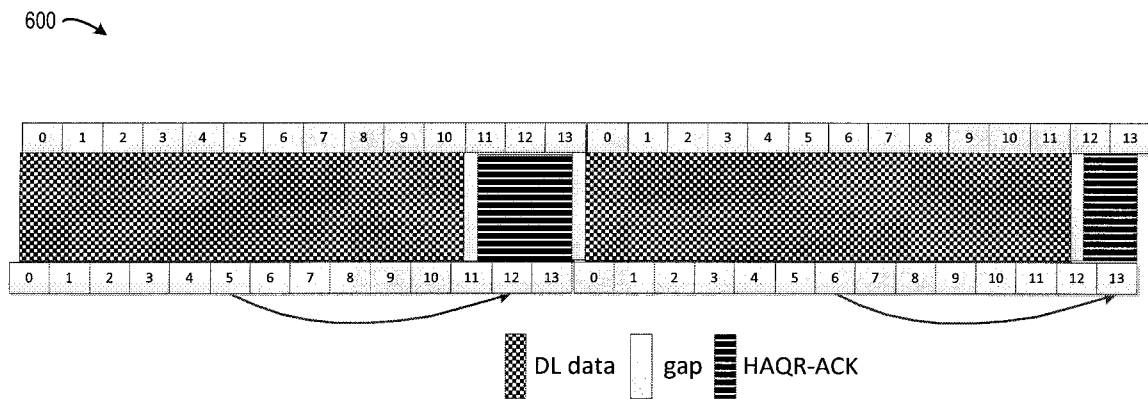


FIG. 6

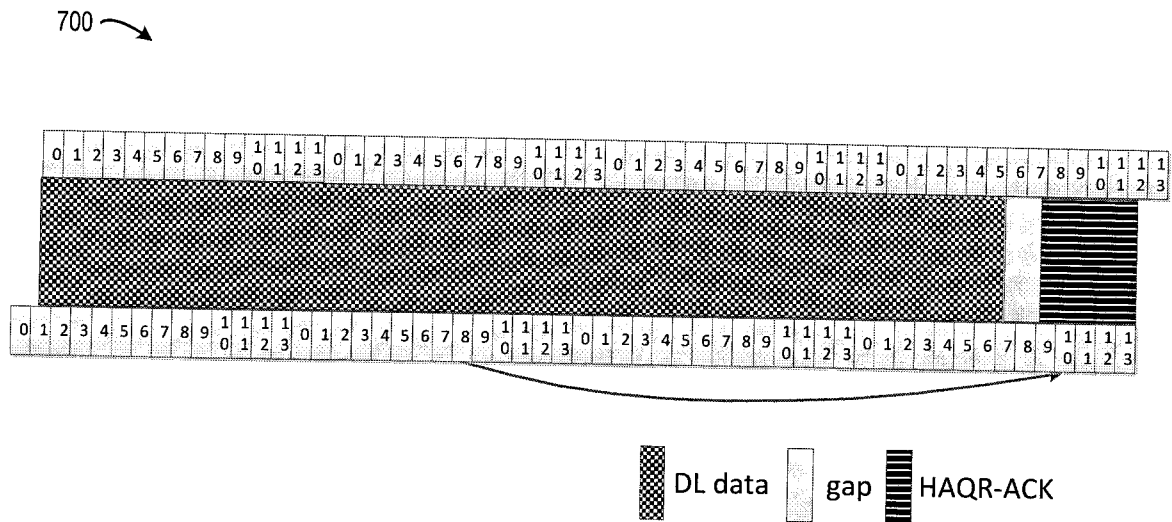


FIG. 7

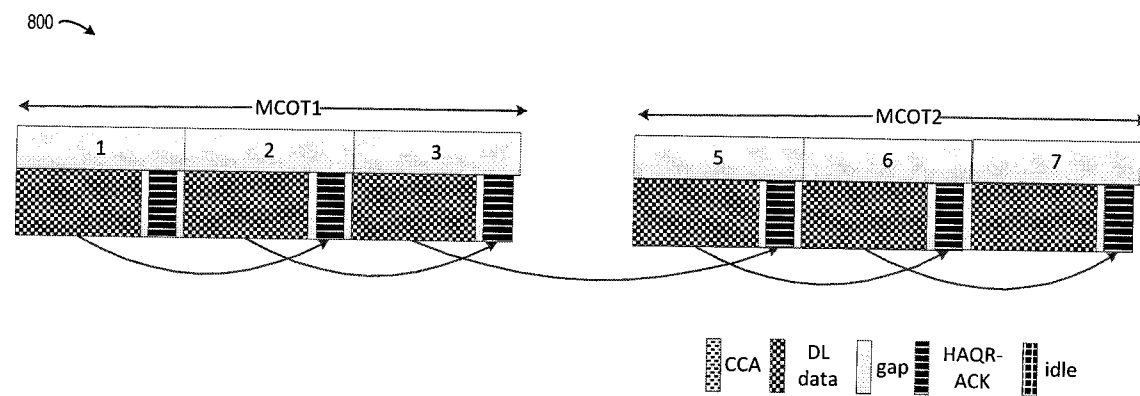


FIG. 8

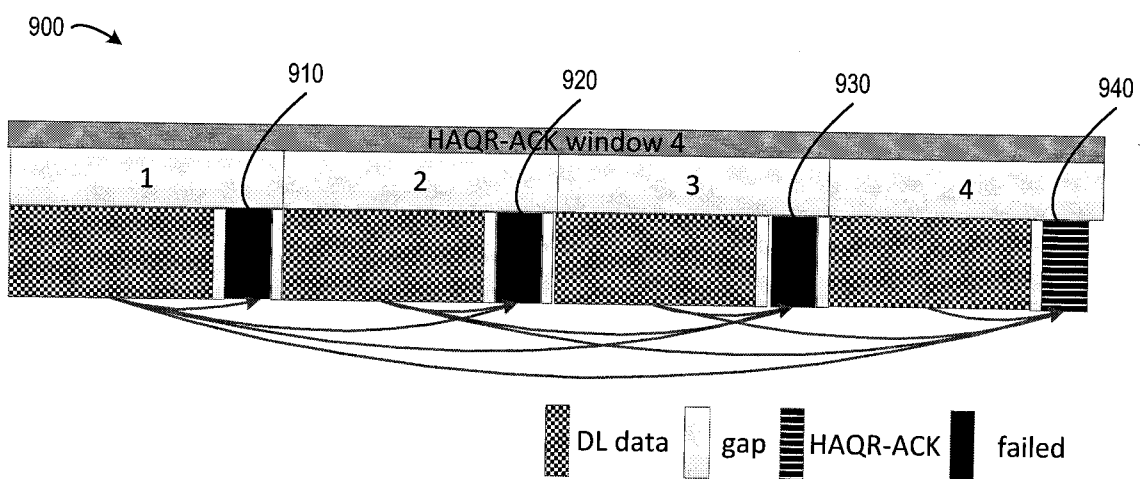
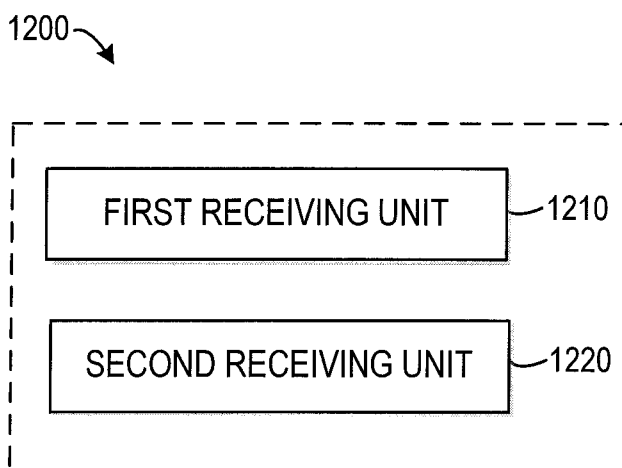
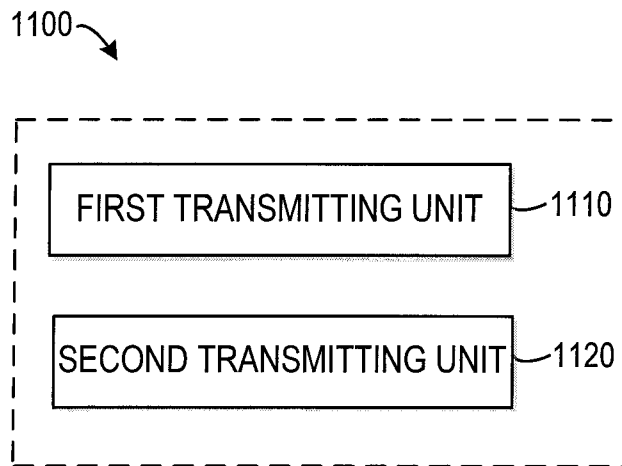
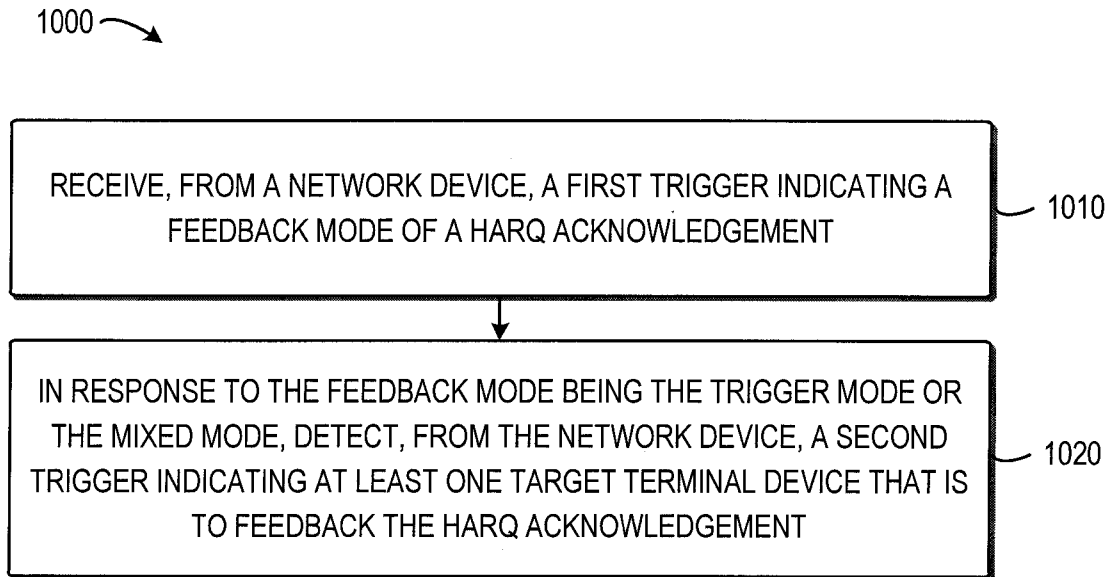


FIG. 9



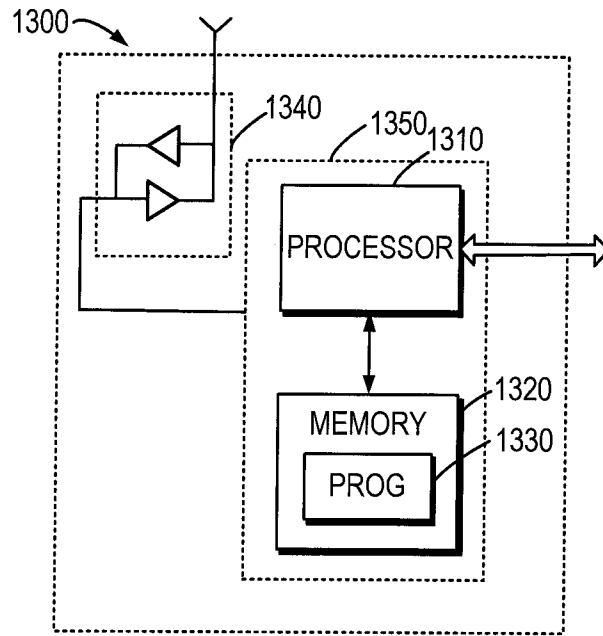


FIG. 13

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2018/071948

A. CLASSIFICATION OF SUBJECT MATTER

H04L 1/18(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04L; H04Q; H04W

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNPAT,CNKI,WPI,EPDOC,3GPP,GOOGLE: HARQ, feedback+, feed+, ACK, NACK, unsilenced, unlicens+, mode, state, method, scheme, trigger, activat+

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	LG ELECTRONICS. "Consideration on HARQ-ACK feedback method for NR" 3GPP TSG RAN WG1 Meeting #89 R1-1707656, 19 May 2017 (2017-05-19), section 2	1-14
A	SAMSUNG. "Triggered HARQ-ACK Transmissions" 3GPP TSG RAN WG1 #89 R1-1708028, 19 May 2017 (2017-05-19), the whole document	1-14
A	CN 101741528 A (HUAWEI TECHNOLOGIES CO., LTD.) 16 June 2010 (2010-06-16) the whole document	1-14
A	WO 2017205669 A1 (QUALCOMM INCORPORATED) 30 November 2017 (2017-11-30) the whole document	1-14

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

14 September 2018

Date of mailing of the international search report

25 September 2018

Name and mailing address of the ISA/CN

STATE INTELLECTUAL PROPERTY OFFICE OF THE
P.R.CHINA
6, Xitucheng Rd., Jimen Bridge, Haidian District, Beijing
100088
China

Authorized officer

ZHANG,Feng

Facsimile No. (86-10)62019451

Telephone No. 86-(10)-53961628

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.

PCT/CN2018/071948

Patent document cited in search report			Publication date (day/month/year)	Patent family member(s)			Publication date (day/month/year)
CN	101741528	A	16 June 2010	None			
WO	2017205669	A1	30 November 2017	TW	201743581	A	16 December 2017
				US	2017346605	A1	30 November 2017