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- (71) Applicant (for all designated States except US): **MI-CRON TECHNOLOGY, INC.** [US/US]; 8000 South Federal Way, Boise, ID 83716-9632 (US).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): **GRUNZKE, Terry, M.** [US/US]; 6722 S. Lunar Ave, Boise, ID 83709 (US).

(74) Agent: **KERN, Jacob, T.;** Brooks, Cameron & Huebsch, PLLC., 1221 Nicollet Ave., Suite 500, Minneapolis, MN 55403 (US).

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[Continued on next page]

(54) Title: SYSTEMS, DEVICES, MEMORY CONTROLLERS, AND METHODS FOR MEMORY INITIALIZATION

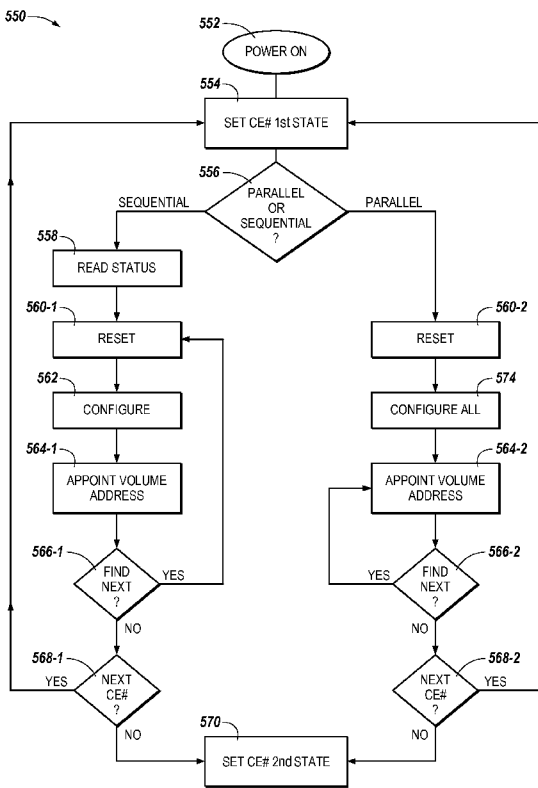


Fig. 5

(57) Abstract: Systems, devices, memory controllers, and methods for initializing memory are described. Initializing memory can include configuring memory devices in parallel. The memory devices can receive a shared enable signal. A unique volume address can be assigned to each of the memory devices.

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LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK,
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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G06F 12/00; G11C 7/10; G06F 13/10; G11C 8/04; G06F 13/14; G11C 8/12; G11C 16/00; G06F 13/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: memory initialization, parallel, shared enable signal, volume address, linear daisy chain

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	KR 10-2008-0056276 A (MOSAID TECHNOLOGIES, INC.) 20 June 2008 See the abstract, claims 17-23, and figures 1-6.	1-34
A	US 2002-0188781 A1 (DANIEL SCHOCH et al.) 12 December 2002 See the abstract, claims 1,21, and figures 2-6.	1-34
A	KR 10-2009-0080568 A (INTEL CORP.) 24 July 2009 See the abstract, claims 1,7,8, and figures 1-4.	1-34
A	KR 10-2008-0068835 A (MOSAID TECHNOLOGIES, INC.) 24 July 2008 See the abstract, claims 1,5, and figures 1-6.	1-34

 Further documents are listed in the continuation of Box C. See patent family annex.

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