${\bf (19) \ World \ Intellectual \ Property \ Organization}$

International Bureau



(43) International Publication Date 17 April 2008 (17.04.2008)

PCT

(10) International Publication Number WO 2008/045705 A1

(51) International Patent Classification: *H04N 7/24* (2006.01) *H04N 7/26* (2006.01)

(21) International Application Number:

PCT/US2007/080012

(22) International Filing Date:

28 September 2007 (28.09.2007)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

11/543,576

5 October 2006 (05.10.2006) US

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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

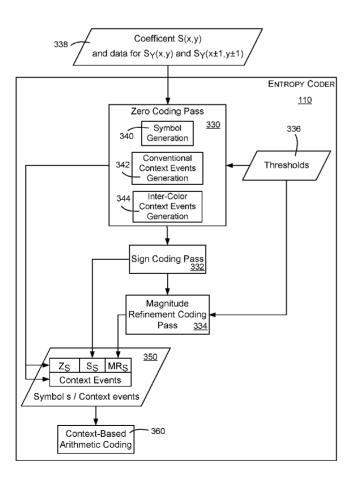
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

 as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))

[Continued on next page]

(54) Title: COLOR IMAGE CODING USING INTER-COLOR CORRELATION



(57) Abstract: Described is an image compression technology by which a coefficient representative of a chrominance component corresponding to an (x, y) coordinate pair is coded into a symbol, along with plurality of context events associated with that symbol, to facilitate more efficient context-based arithmetic coding. For example, four context events may be generated, including one context event based on the corresponding (x, y) coefficient value in the luminance component, and three context events based on other luminance coefficients that have adjacent horizontal, vertical and diagonal relationships with the corresponding luminance component's coefficient, respectively. example implementation, coding the chrominance coefficient and computing the plurality of context events occurs in a zero coding pass of an entropy A sign coding pass and a magnitude refinement pass may be performed to obtain further symbols for the context-based arithmetic coding.



WO 2008/045705 A1



as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))

before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

Published:

with international search report

COLOR IMAGE CODING USING INTER-COLOR CORRELATION

BACKGROUND

[0001] The quality of digital images continues to improve as advances in digital imaging technology provide increasingly higher resolution and color variation. In general, however, as image quality increases, the resultant image data files increase geometrically in size.

[0002] To reduce the storage required to store image data and the bandwidth needed to transmit image data, image compression technologies are employed, such as those based on JPEG standards. In general, a JPEG encoder is designed to allow its operator to trade off the resultant data file size of an image against that image's quality when decoded. As can be readily appreciated, any significant improvement in compression that does not have a corresponding reduction in image quality is desirable.

SUMMARY

[0003] This Summary is provided to introduce a selection of representative concepts in a simplified form that are further described below in the Detailed Description. This

Summary is not intended to identify key features or essential features of the claimed subject matter, nor is it intended to be used in any way that would limit the scope of the claimed subject matter.

[0004] Briefly, various aspects of the subject matter described herein are directed towards an image compression technology by which a coefficient representative of a chrominance component corresponding to an (x, y) coordinate pair is coded into a symbol. For that symbol, a plurality of context events are coded, each of which are based upon a corresponding coefficient of a luminance component at the (x, y) coordinate pair and at least one other coefficient of another luminance component having a coordinate relationship with the (x, y) coordinate pair. The symbol is associated with the context events for more efficient context-based arithmetic coding.

[0005] In one example implementation, coding the chrominance coefficient and computing the plurality of context events occurs in a zero coding pass of entropy coding. In this example implementation, coding the chrominance coefficient into the symbol and computing the plurality of context events comprises comparing the coefficients against a decreasing (e.g., halved) threshold

value until the coefficient representative of the chrominance component meets the threshold. When the threshold is met, a sign coding pass and a magnitude refinement pass may be performed to obtain further symbols for the context-based arithmetic coding.

[0006] Computing the context events may comprise generating four context events, including one context event based on the corresponding coefficient in the luminance component, and three context events based on other luminance coefficients that have adjacent horizontal, vertical and diagonal relationships with the corresponding luminance component's coefficient. For example, four such context events, C_1 - C_4 , may be obtained from the following computations, where each $|S_Y(x-coordinate\ identifier,\ y-coordinate\ identifier)|$ refers to an absolute value of a coefficient of a luminance component at coordinates relative to an $(x,\ y)$ coordinate pair of the chrominance component, and T refers to the threshold value:

 $C_1 = 1$, if $|S_Y(x,y)| >= T$; otherwise, $C_1 = 0$;

 $C_2 = 1$, if $|S_Y(x,y-1)| >= T$ OR $|S_Y(x,y+1)| >= T$; otherwise, $C_2 = 0$;

 $C_3 = 1$, if $|S_Y(x-1,y)| >= T$ OR $|S_Y(x+1,y)| >= T$; otherwise, $C_3 = 0$;

 $C_4 = 1$, if $|S_Y(x-1,y-1)| >= T$ OR $|S_Y(x+1,y-1)| >= T$ OR $|S_Y(x-1,y+1)| >= T$ OR $|S_Y(x+1,y+1)| >= T$; otherwise, $C_4 = 0$.

In one example, a symbol is generated by [00071 comparing a coefficient representing a component of an image against a threshold value, and if the threshold value is met, setting a symbol to a first value, e.g., binary one. A first context event set is generated for associating with the symbol if the image component is a luminance component, or a second context event set is generated for associating with the symbol if the image component is a chrominance component. Generating the second context event set includes using at least two coefficients representing luminance components that each has a relationship with the chrominance component. For example, one context event may be generated by comparing a luminance coefficient that has an identical coordinate relationship with the chrominance component's (x, y) coordinates against the threshold value. A second, third and fourth context event may be made by threshold comparisons against luminance coefficients having adjacent horizontal, adjacent vertical and adjacent diagonal coordinate relationships, respectively.

[0008] A system in an image processing environment may include an encoder that inputs source data and outputs compressed image data based on the source image data. To

this end, the encoder includes a zero coding pass component, a sign coding pass component and a magnitude refinement coding pass component that provide symbols and associated context events to a context-based arithmetic coding component. The zero coding pass component includes a symbol generator that generates at least one zero pass symbol representative of a selected chrominance component, and an inter-color context events generator that generates the context events for association with each zero pass symbol, in which the context events generator generates the context events based on a set of at least two luminance components that each have a relationship with the selected chrominance component.

[0009] Other advantages may become apparent from the following detailed description when taken in conjunction with the drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] The present invention is illustrated by way of example and not limited in the accompanying figures in which like reference numerals indicate similar elements and in which:

[0011] FIGURE 1 shows an illustrative example block diagram of a general-purpose encoder and decoder into which various aspects of the present invention may be incorporated.

- [0012] FIG. 2 is a representation of data to be coded for a chrominance component in an image, and corresponding luminance component data used for generating inter-color context events related to the chrominance component and used in coding the chrominance component.
- [0013] FIG. 3 is a block diagram representing various example components within the entropy coder of an encoder for including a component for generating inter-color context events.
- [0014] FIG. 4 is a flow diagram representing various example steps taken to generate a symbol and related intercolor context events for a U or V component being coded.
- [0015] FIG. 5 shows an illustrative example of a general-purpose computing environment including a computer into which various aspects of the present invention may be incorporated.

DETAILED DESCRIPTION

[0016] Various aspects of the technology described herein are generally directed towards encoding and decoding images such that between encoded and decoded states, the data representing the image is compressed to facilitate better storage and/or transmission. One way in which the technology may be used is to improve the compression rates in JPEG image compression, whether based on conventional JPEG standards or JPEG 2000 standards; (JPEG 2000 is described in the reference by Majid Rabbani and Rajan Joshi, "An overview of the JPEG2000 still image compression standard, "Signal Processing: Image Communication 17 (2002) 3-48). As will be understood, however, the technology described herein may provide benefits in other types of image compression, including image compression in other formats, and/or compression of static images and/or moving images. Moreover, the technology may provide benefits in either lossless or lossy compression.

[0017] As will be understood, in one example implementation, while encoding an image arranged with chrominance (U and V) components, related luminance (Y) components are used to generate additional information, referred to as context events, that better improve compression. In this example implementation, four inter-

color context events are generated based upon the Y coefficient corresponding to a U or V coefficient being encoded and the Y coefficient's surrounding coefficients, e.g., including context events based on the corresponding coefficient in the Y component and Y coefficients with adjacent horizontal, vertical and diagonal relationships therewith. Notwithstanding, it can be readily appreciated that more inter-color context events may be selected to provide even better performance. However, the further improvement is relatively limited compared to the increase in complexity, and, based upon analysis and tests, the four context events already cover most inter-color correlation. Also, note that when the U or V component is coded, the above four inter-color context events can be combined with conventional spatial context events that are obtained from the same component (e.g., the neighboring coefficients of the same component).

[0018] As such, the present invention is not limited to any particular embodiments, aspects, concepts, structures, functionalities or examples described herein. Rather, any of the embodiments, aspects, concepts, structures, functionalities or examples described herein are non-limiting, and the present invention may be used various

ways that provide benefits and advantages in computing and networking in general.

[0019] FIG. 1 shows a functional block diagram of a representative image encoder 100 used to compress source data 102, paired with a decoder 140 that decompresses data into output data 150. By way of example, the source data 102 may include a grayscale image and a color image.

[0020] When the encoder 100 receives the source data 102 in one example embodiment, the encoder 100 first presents the source data 102 to a preprocessor 104. Typically, in color image coding, color signals such as RGB (red, green, and blue) or the like (e.g., CMYK or HSV) are separated by the preprocessor 104 into luminance (Y) components corresponding to a grayscale image, and chrominance (U and V) components that provide the color information.

[0021] The output of the preprocessor 104 is received at a transformer 106 that performs frequency transformation on the output of preprocessor 104. The transformer 106 may perform discrete wavelet transformation (DWT), discrete cosine transformation (DCT), or any other similar frequency domain transformation on the preprocessed data.

[0022] Note that individual data values vary less from neighboring values in the transformed frequency domain data, as compared to the spatial domain data. Taking advantage of the less variant data values in the frequency domain data, the quantizer 108 identifies and aggregates data values that have identical values, and replaces a repeating series of identical data values with one instance of the data value combined with an indication of how many times that identical data value repeats. Similarly, the quantizer 108 may combine a series of similar but not identical values with a single identical value when the data values fall within a particular tolerance. Aggregating similar but not identical data values is used in lossy compression, in which some degradation of the original image is acceptable.

[0023] The output of the quantizer 108 is presented to an entropy coder 110 that generates the compressed image data 120. In general, entropy coding compresses data by identifying or predicting the frequency with which data values occur in a data file. Then, instead of representing each data value with a fixed, equal-length value, entropy coding represents more frequently appearing data values with shorter binary representations. By replacing frequently appearing data values with shorter

representations instead of fixed, equal-length representations, the entropy coder 110 reduces the size of the compressed data 120a.

data 120a generated by the entropy coder 110 is presented to a channel 130. The channel 130 may include any type of data storage and/or data transmission media. The output of the channel 130 is represented as compressed data 120b, which may be identical to the compressed data 120a, particularly if transmitting directly, but is labeled separately to indicate it may be a copy, or may be available at a later time. Further, the compressed data 120b may be obtained after some additional processing of the compressed data 120a, e.g., it may be a lower-quality and/or reduced-size image such as if being sent to a small screen or as a miniaturized representation thereof, and/or on a low bandwidth connection.

[0025] A decoder 140 receives or retrieves the compressed data 120b from the channel 130. The decoder 140 basically decompresses the compressed data 120b through a mirror image of the process applied by the encoder 100. More particularly, and as is generally known, the compressed data 120b is translated by an entropy

decoder 142, a dequantizer 144, an inverse transformer 146, and a postprocessor 148 that ultimately presents output data 150, such as image or video data suitable for presenting on a display or any other devices. For purposes of simplifying the description herein, the technology will be primarily described with reference to the encoder 100 / entropy coder 110, as those skilled in the art will recognize that the decoder components basically mirror the encoder components.

[0026] The context of coding monochrome images have been well studied and widely used. More particularly, when color images are coded, both the luminance component and chrominance components are usually coded independently, by using the context found in the monochrome images. However, although the use of monochrome images simplify coding system design, coding efficiency is reduced, primarily because the correlation among different color components is not exploited.

[0027] The technology described herein presents new inter-color context events that can be used for coding the chrominance components. Because these inter-color context events well model the inter-color correlation existing between luminance and chrominance components, better coding

efficiency when compressing the chrominance components is achieved, e.g., a bit rate reduction on the order of ten percent in coding the chrominance components.

[0028] In a typical high efficient image codec such as JPEG 2000, context-based arithmetic coding, whose coding mechanism is based on some predefined contexts events generated from previous coded samples, is adopted by the entropy coder 110. This allows the entropy coder 110 to effectively exploit statistical correlations in the data.

[0029] More particularly, in context-based arithmetic coding, a symbol \boldsymbol{s} is encoded under a group of context events C_1 , $C_2...C_N$, where C_i is a certain context event. A context event is an event that has some correlation to the symbol \boldsymbol{s} , such that the value of the symbol can be predicted from the value of the context event. For example, in grayscale image coding, the values of the neighboring wavelet coefficients are often selected as context events when a wavelet coefficient is coded. Usually, both the symbol and the context events are binary, i.e., the individual value of \boldsymbol{s} , C_1 , ..., and C_N is either one or zero.

[0030] It is pointed out herein that the shapes of the chrominance (U and V) components can often be recognized

from that of the luminance (Y) component. This phenomenon indicates that most chrominance changes in a real scene are accompanied by a luminance change. Through statistical analysis, in one example implementation, four inter-color context events in the wavelet domain for coding the chrominance (U and V) components are provided.

[0031] In this example implementation, represented in FIG. 2, four new context events are obtained from the Y component when the U or V component is coded. For example, as represented on the left side of FIG. 2, when coding the U or V component for a coefficient S with (x,y) coordinates, S(x,y), let $S_Y(x,y)$ denote the corresponding coefficient with the same coordinates (x,y) in the Y component. In this example implementation, the four intercolor context events are obtained from the following (where T is a comparison threshold and |x| denotes the absolute value of x):

- $C_1 = 1$, if $|S_Y(x,y)| >= T$; otherwise, $C_1 = 0$;
- $C_2 = 1$, if $|S_Y(x,y-1)| >= T$ OR $|S_Y(x,y+1)| >= T$; otherwise, $C_2 = 0$;
- $C_3 = 1$, if $|S_Y(x-1,y)| >= T$ OR $|S_Y(x+1,y)| >= T$; otherwise, $C_3 = 0$;
- $C_4 = 1$, if $|S_Y(x-1,y-1)| >= T$ OR $|S_Y(x+1,y-1)| >= T$ OR $|S_Y(x-1,y+1)| >= T$ OR $|S_Y(x+1,y+1)| >= T$; otherwise, $C_4 = 0$.

As can be readily appreciated, additional context events may be generated by expanding the surrounding area beyond x±1 and/or y±1, at least in part. Further, different logical combinations may be used, e.g., AND-ing or XOR-ing instead of OR-ing in computing any of C2 through C_4 , or more complex combinations, e.g., using diagonally adjacent coefficients to some extent in computing C2 and C3, and using horizontally and/or vertically adjacent coefficients at least in part in computing C_4 . Note that when no adjacent coefficient or coefficients exist due to being at an edge, the "missing" coefficient can be considered as not meeting the threshold, or as meeting it, or a special case of one or zero may be defined as the resulting context event, regardless of other computations. Alternatively, a different computation may be used, e.g., based on the value of another coefficient and/or on a combination of other coefficients that are available. Still further, the same or different threshold values may be used in the comparisons, e.g., the luminance component's coefficients can be compared against another threshold value, such as one based on the threshold value used in coding the symbol.

[0033] To use the inter-color context events in color image coding in one example, for the embedded coding of a

coefficient S(x,y), the coefficient compared with a series of decreasing thresholds T (or bit plane, the value of a certain threshold is half of its prior threshold). Coding at each threshold is known as a coding pass. Usually, there are three types of successive coding passes, namely zero coding, sign coding, and magnitude refinement coding (as explained in the aforementioned JPEG 2000 reference). As will be understood, the new inter-color context events are used in the zero coding pass when the U and V components are coded.

[0034] FIG. 3 represents components within the entropy coder 110, including a component for the zero coding pass 330, a component for the sign coding pass 332, and a component for the magnitude refinement coding pass 334. In general, the zero coding pass is performed first, with data 338 comprising the coefficient S(x,y) and coefficients for the corresponding coefficient with the same coordinates (x,y) in the Y component, $S_Y(x,y)$, and those horizontally, vertically and diagonally adjacent thereto, e.g., $S_Y(x+1,y)$, $S_Y(x-1,y)$ are horizontally adjacent, $S_Y(x,y+1)$, $S_Y(x,y-1)$, are vertically adjacent, and $S_Y(x+1,y+1)$, $S_Y(x+1,y-1)$, $S_Y(x-1,y-1)$ are diagonally adjacent.

[0035] The symbol s generated from the coefficient S(x,y) in a symbol generation part 340 for this zero coding pass 330, and if S(x,y) is obtained from the Y component, then the conventional context events like those in JPEG2000 are generated, represented in FIG. 3 via block 342. Alternatively, if S(x,y) is obtained from the U or V component, then the above-described four inter-color context events, C_1 , C_2 , C_3 and C_4 based on the corresponding Y component coefficients are used in the combination of the conventional context events, represented in FIG. 3 via block 344.

[0036] More particularly, as represented in the flow diagram of FIG. 4, for symbol generation 340, the zero coding pass 330 (step 400), S(x,y) is compared with a list 336 of decreasing thresholds T until the absolute value of the coefficient S(x,y) is not less than T (steps 402, 404, 412 and 414). At each comparison at step 402, a binary symbol s is generated: s=1, if |S(x,y)| >= T, otherwise, s=0.

[0037] As represented by step 406, depending on whether coding the Y component or the U or V component, step 408 or 408 is executed to generate the context events of s at threshold T. Thus, if Y coding, conventional context

events are generated via step 408. Alternatively, if S(x,y) is obtained from the U or V component, then the above-described four inter-color context events, C_1 , C_2 , C_3 and C_4 are obtained via step 410. In any event, as can be seen, s may be coded by context-based arithmetic coding with the found context events. In other words, each s bit generated in zero pass coding has corresponding context events, which in the case of U or V component coding, are the context events C_1 , C_2 , C_3 and C_4 in one example implementation.

[0038] As also represented in FIG. 4, once |S(x,y)| is not less than T, that is, s=0 as evaluated at step 412, the sign coding pass is performed, as represented by step 416. To this end, if the coefficient s is positive, a symbol one (binary 1) is generated, otherwise a symbol zero (binary 0) is generated.

[0039] After the sign coding pass, the bit-plane representation of S(x,y) at the remaining thresholds is further checked, which is termed the magnitude refinement pass (step 418). This pass further specifies the magnitude of the coefficient in the remaining successively lower magnitude bit planes.

[0040] By way of an example, consider how a coefficient "100" (decimal) is coded into symbols. The 8-bits bit plane representation of 100 is 01100100. Hence, the comparison thresholds T equal 128, 64, 32, 16, 8, 4, 2, and 1, respectively. When dealing with the zero coding pass 340 (corresponding to step 400), the highest magnitude bit plane (T = 128), it is determined that the coefficient is less than 128. Thus, a symbol 0 is generated. Next, at T = 64, the coefficient is greater than T, and a symbol 1 is generated.

[0041] At this point, the zero coding pass finishes and the sign coding pass 332 (corresponding to step 416) starts. A symbol 1 is generated for the sign coding pass as the coefficient is positive. Next, From T = 32, a series of symbols 1, 0, 0, 1, 0, 0 is generated in the magnitude refinement pass 334 (corresponding to step 418). Thus, the generated symbols 350 for the coefficient "100" are:

Zero coding pass: 0, 1

Sign coding pass: 1

Magnitude refinement pass: 1, 0, 0, 1, 0, 0

[0042] These symbols, along with the context events in block 350 obtained from the corresponding Y coefficient and its adjacent Y coefficients obtained in the zero coding pass, are coded by the context-based arithmetic coding 360. These context events are the inter-color context events when U or V coding.

EXEMPLARY OPERATING ENVIRONMENT

[0043] FIGURE 5 illustrates an example of a suitable computing system environment 500 in which the encoder and/or decoder described above with references to FIGS. 1-4 may be implemented. The computing system environment 500 is only one example of a suitable computing environment and is not intended to suggest any limitation as to the scope of use or functionality of the invention. Neither should the computing environment 500 be interpreted as having any dependency or requirement relating to any one or combination of components illustrated in the exemplary operating environment 500.

[0044] The invention is operational with numerous other general purpose or special purpose computing system environments or configurations. Examples of well known

computing systems, environments, and/or configurations that may be suitable for use with the invention include, but are not limited to: personal computers, server computers, handheld or laptop devices, tablet devices, multiprocessor systems, microprocessor-based systems, set top boxes, programmable consumer electronics, network PCs, minicomputers, mainframe computers, distributed computing environments that include any of the above systems or devices, and the like.

[0045] The invention may be described in the general context of computer-executable instructions, such as program modules, being executed by a computer. Generally, program modules include routines, programs, objects, components, data structures, and so forth, which perform particular tasks or implement particular abstract data types. The invention may also be practiced in distributed computing environments where tasks are performed by remote processing devices that are linked through a communications network. In a distributed computing environment, program modules may be located in local and/or remote computer storage media including memory storage devices.

[0046] With reference to FIG. 5, an exemplary system for implementing various aspects of the invention may include a

general purpose computing device in the form of a computer 510. Components of the computer 510 may include, but are not limited to, a processing unit 520, a system memory 530, and a system bus 521 that couples various system components including the system memory to the processing unit 520. The system bus 521 may be any of several types of bus structures including a memory bus or memory controller, a peripheral bus, and a local bus using any of a variety of bus architectures. By way of example, and not limitation, such architectures include Industry Standard Architecture (ISA) bus, Micro Channel Architecture (MCA) bus, Enhanced ISA (EISA) bus, Video Electronics Standards Association (VESA) local bus, and Peripheral Component Interconnect (PCI) bus also known as Mezzanine bus.

[0047] The computer 510 typically includes a variety of computer-readable media. Computer-readable media can be any available media that can be accessed by the computer 510 and includes both volatile and nonvolatile media, and removable and non-removable media. By way of example, and not limitation, computer-readable media may comprise computer storage media and communication media. Computer storage media includes volatile and nonvolatile, removable and non-removable media implemented in any method or technology for storage of information such as computer-

readable instructions, data structures, program modules or other data. Computer storage media includes, but is not limited to, RAM, ROM, EEPROM, flash memory or other memory technology, CD-ROM, digital versatile disks (DVD) or other optical disk storage, magnetic cassettes, magnetic tape, magnetic disk storage or other magnetic storage devices, or any other medium which can be used to store the desired information and which can accessed by the computer 510. Communication media typically embodies computer-readable instructions, data structures, program modules or other data in a modulated data signal such as a carrier wave or other transport mechanism and includes any information delivery media. The term "modulated data signal" means a signal that has one or more of its characteristics set or changed in such a manner as to encode information in the signal. By way of example, and not limitation, communication media includes wired media such as a wired network or direct-wired connection, and wireless media such as acoustic, RF, infrared and other wireless media. Combinations of the any of the above should also be included within the scope of computer-readable media.

[0048] The system memory 530 includes computer storage media in the form of volatile and/or nonvolatile memory such as read only memory (ROM) 531 and random access memory

(RAM) 532. A basic input/output system 533 (BIOS), containing the basic routines that help to transfer information between elements within computer 510, such as during start-up, is typically stored in ROM 531. RAM 532 typically contains data and/or program modules that are immediately accessible to and/or presently being operated on by processing unit 520. By way of example, and not limitation, FIG. 5 illustrates operating system 534, application programs 535, other program modules 536 and program data 537.

[0049] The computer 510 may also include other removable/non-removable, volatile/nonvolatile computer storage media. By way of example only, FIG. 5 illustrates a hard disk drive 541 that reads from or writes to non-removable, nonvolatile magnetic media, a magnetic disk drive 551 that reads from or writes to a removable, nonvolatile magnetic disk 552, and an optical disk drive 555 that reads from or writes to a removable, nonvolatile optical disk 556 such as a CD ROM or other optical media. Other removable/non-removable, volatile/nonvolatile computer storage media that can be used in the exemplary operating environment include, but are not limited to, magnetic tape cassettes, flash memory cards, digital versatile disks, digital video tape, solid state RAM, solid

state ROM, and the like. The hard disk drive 541 is typically connected to the system bus 521 through a non-removable memory interface such as interface 540, and magnetic disk drive 551 and optical disk drive 555 are typically connected to the system bus 521 by a removable memory interface, such as interface 550.

[0050] The drives and their associated computer storage media, described above and illustrated in FIG. 5, provide storage of computer-readable instructions, data structures, program modules and other data for the computer 510. In FIG. 5, for example, hard disk drive 541 is illustrated as storing operating system 544, application programs 545, other program modules 546 and program data 547. Note that these components can either be the same as or different from operating system 534, application programs 535, other program modules 536, and program data 537. Operating system 544, application programs 545, other program modules 546, and program data 547 are given different numbers herein to illustrate that, at a minimum, they are different copies. A user may enter commands and information into the computer 510 through input devices such as a tablet, or electronic digitizer, 564, a microphone 563, a keyboard 562 and pointing device 561, commonly referred to as mouse, trackball or touch pad. Other input devices not shown in

FIG. 5 may include a joystick, game pad, satellite dish, scanner, or the like. These and other input devices are often connected to the processing unit 520 through a user input interface 560 that is coupled to the system bus, but may be connected by other interface and bus structures, such as a parallel port, game port or a universal serial bus (USB). A monitor 591 or other type of display device is also connected to the system bus 521 via an interface, such as a video interface 590. The monitor 591 may also be integrated with a touch-screen panel or the like. Note that the monitor and/or touch screen panel can be physically coupled to a housing in which the computing device 510 is incorporated, such as in a tablet-type personal computer. In addition, computers such as the computing device 510 may also include other peripheral output devices such as speakers 595 and printer 596, which may be connected through an output peripheral interface 594 or the like.

[0051] The computer 510 may operate in a networked environment using logical connections to one or more remote computers, such as a remote computer 580. The remote computer 580 may be a personal computer, a server, a router, a network PC, a peer device or other common network node, and typically includes many or all of the elements

described above relative to the computer 510, although only a memory storage device 581 has been illustrated in FIG. 5. The logical connections depicted in FIG. 5 include one ore more local area networks (LAN) 571 and one or more wide area networks (WAN) 573, but may also include other networks. Such networking environments are commonplace in offices, enterprise-wide computer networks, intranets and the Internet.

[0052] When used in a LAN networking environment, the computer 510 is connected to the LAN 571 through a network interface or adapter 570. When used in a WAN networking environment, the computer 510 typically includes a modem 572 or other means for establishing communications over the WAN 573, such as the Internet. The modem 572, which may be internal or external, may be connected to the system bus 521 via the user input interface 560 or other appropriate mechanism. A wireless networking component 574 such as comprising an interface and antenna may be coupled through a suitable device such as an access point or peer computer to a WAN or LAN. In a networked environment, program modules depicted relative to the computer 510, or portions thereof, may be stored in the remote memory storage device. By way of example, and not limitation, FIG. 5 illustrates remote application programs 585 as residing on memory

device 581. It may be appreciated that the network connections shown are exemplary and other means of establishing a communications link between the computers may be used.

[0053] An auxiliary subsystem 599 (e.g., for auxiliary display of content) may be connected via the user interface 560 to allow data such as program content, system status and event notifications to be provided to the user, even if the main portions of the computer system are in a low power state. The auxiliary subsystem 599 may be connected to the modem 572 and/or network interface 570 to allow communication between these systems while the main processing unit 520 is in a low power state.

CONCLUSION

[0054] While the invention is susceptible to various modifications and alternative constructions, certain illustrated embodiments thereof are shown in the drawings and have been described above in detail. It should be understood, however, that there is no intention to limit the invention to the specific forms disclosed, but on the contrary, the intention is to cover all modifications,

alternative constructions, and equivalents falling within the spirit and scope of the invention.

WHAT IS CLAIMED IS:

1. A computer-readable medium having computer executable instructions, which when executed performs steps, comprising:

coding (404) a coefficient representative of a chrominance component corresponding to an (x, y) coordinate pair into a symbol;

computing (410) a plurality of context events based upon a corresponding coefficient of a luminance component at the (x, y) coordinate pair and at least one other coefficient of another luminance component having a coordinate relationship with the (x, y) coordinate pair; and

associating (350) the symbol with the context events for context-based arithmetic coding (360).

- 2. The computer-readable medium of claim 1 wherein the steps of coding the coefficient and computing the plurality of context events occurs in a zero coding pass (400) of coding via an entropy coder (110).
- 3. The computer-readable medium of claim 1 wherein the steps of coding the coefficient and computing the plurality of context events comprises comparing (404) the

coefficients against a decreasing threshold value (414) until the coefficient representative of the chrominance component meets the threshold value.

- 4. The computer-readable medium of claim 3 further comprising halving (414) the threshold value into a new threshold value when the coefficient representative of the chrominance component does not meet the threshold value.
- 5. The computer-readable medium of claim 1 having further computer-executable instructions comprising performing a sign coding pass (416) and a magnitude refinement pass (418) to obtain further symbols for the context-based arithmetic coding.
- 6. The computer-readable medium of claim 1 wherein computing the plurality of context events based upon the corresponding coefficient of the luminance component at the (x, y) coordinate pair and at least one other coefficient of another luminance component having a coordinate relationship with the (x, y) coordinate pair comprises, generating (410) four context events, including one context event based on the corresponding coefficient in the luminance component, and three context events based on

other luminance coefficients that have adjacent horizontal, vertical and diagonal relationships with the corresponding coefficient.

7. The computer-readable medium of claim 1 wherein computing the plurality of context events based upon the corresponding coefficient of the luminance component at the (x, y) coordinate pair and at least one other coefficient of another luminance component having a coordinate relationship with the (x, y) coordinate pair comprises, generating (410) four context events C_1 - C_4 based upon the following computations, where each $|S_Y(x-coordinate identifier, y-coordinate identifier)|$ refers to an absolute value of a coefficient of the luminance component at coordinates relative to the (x, y) coordinate pair, and T refers to a threshold value:

 $C_1 = 1$, if $|S_Y(x,y)| >= T$; otherwise, $C_1 = 0$;

- $C_2 = 1$, if $|S_Y(x,y-1)| >= T$ OR $|S_Y(x,y+1)| >= T$; otherwise, $C_2 = 0$;
- $C_3 = 1$, if $|S_Y(x-1,y)| >= T$ OR $|S_Y(x+1,y)| >= T$; otherwise, $C_3 = 0$;
- $C_4 = 1$, if $|S_Y(x-1,y-1)| >= T$ OR $|S_Y(x+1,y-1)| >= T$ OR $|S_Y(x-1,y+1)| >= T$ OR $|S_Y(x+1,y+1)| >= T$; otherwise, $C_4 = 0$.

8. The computer-readable medium of claim 1 wherein computing the plurality of context events comprises, comparing (410) the luminance coefficients that have adjacent horizontal, vertical and diagonal relationships with the corresponding coefficient against a threshold value.

9. The computer-readable medium of claim 8 wherein computing the plurality of context events comprises, separately comparing each horizontally-related coefficient against the threshold value and logically combining a result of each horizontally-related coefficient comparison to obtain one value for one context event, separately comparing each vertically-related coefficient against the threshold value and logically combining a result of each vertically-related coefficient comparison to obtain another value for another context event, and separately comparing each diagonally-related coefficient against the threshold value and logically combining a result of each diagonally-related coefficient comparison to obtain yet another value for yet another context event.

10. In an image encoding environment, a method comprising:

- (a) comparing (404) a coefficient representing a component of an image against a threshold value, and if the threshold value is met, setting a symbol to a first value;
- (b) generating (408) a first context event set for associating with the symbol if the image component is a luminance component; and
- (c) generating (410) a second context event set for associating with the symbol if the image component is a chrominance component, including using at least two coefficients representing luminance components that each have a relationship with the chrominance component.
- 11. The method of claim 10 wherein the threshold value is not met, and further comprising, decreasing (414) the threshold and returning to (a).
- 12. The method of claim 10 wherein the threshold value is met, and further comprising, generating at least one other symbol by performing a sign coding pass (416) and a magnitude refinement pass (418), and providing the symbols and first or second context event set to a context-based arithmetic coding component.

13. The method of claim 10 wherein the image component comprises a chrominance component, and wherein generating the second context event set comprises, comparing (410) a corresponding coefficient of the luminance component at an (x, y) coordinate pair that corresponds to the chrominance component's (x, y) coordinate pair against data based on the threshold, and comparing at least one other coefficient of another luminance component having a coordinate relationship with the (x, y) coordinate pair.

14. The method of claim 10 wherein the image component comprises a chrominance component, and wherein generating (410) the second context event set comprises, generating four context events, including one context event based on a corresponding luminance coefficient of the luminance component at an (x, y) coordinate pair that corresponds to the chrominance component's (x, y) coordinates, and three context events based on other luminance coefficients that have adjacent horizontal, vertical and diagonal relationships with the corresponding luminance coefficient.

15. The method of claim 10 wherein the image component comprises a chrominance component, and wherein generating (410) the second context event set comprises, generating four context events C_1 - C_4 based upon the following computations, where each $|S_Y(x-coordinate)|$ identifier, y-coordinate identifier) | refers to an absolute value of a coefficient of a luminance component at coordinates relative to an (x, y) coordinate pair of the chrominance component, and T refers to the threshold value: $C_1 = 1$, if $|S_Y(x,y)| >= T$; otherwise, $C_1 = 0$; $C_2 = 1$, if $|S_Y(x,y-1)| >= T$ OR $|S_Y(x,y+1)| >= T$; otherwise,

- $C_2 = 0$;
- $C_3 = 1$, if $|S_Y(x-1,y)| >= T$ OR $|S_Y(x+1,y)| >= T$; otherwise,
- $C_4 = 1$, if $|S_Y(x-1,y-1)| >= T$ OR $|S_Y(x+1,y-1)| >= T$ OR $|S_Y(x-1,y-1)| >= T$ OR $|S_Y(x-1,y-1)| >= T$ 1,y+1) >= T OR $|S_Y(x+1,y+1)|$ >= T; otherwise, $C_4 = 0$.
- 16. The method of claim 10 wherein the image component comprises a chrominance component associated with (x, y) coordinates, and wherein generating (410) the second context event set comprises, comparing luminance coefficients that have identical, adjacent horizontal, adjacent vertical and adjacent diagonal (x, y) coordinate relationships with the chrominance component's (x, y)coordinates against data based on the threshold value.

17. In an image processing environment, a system comprising:

an encoder (100) that inputs source data (102) and outputs compressed image data (120a) based on the source image data, the encoder including a zero coding pass component (330), a sign coding pass component (332) and a magnitude refinement coding pass component (334) that provide symbols and associated context events (350) to a context-based arithmetic coding component (360), the zero coding pass component comprising:

a symbol generator (340) that generates at least one zero pass symbol representative of a selected chrominance component; and

an inter-color context events generator (344) that generates the context events for association with each zero pass symbol, the generator generating the context events based on a set (338) of at least two luminance components that each have a relationship with the selected chrominance component.

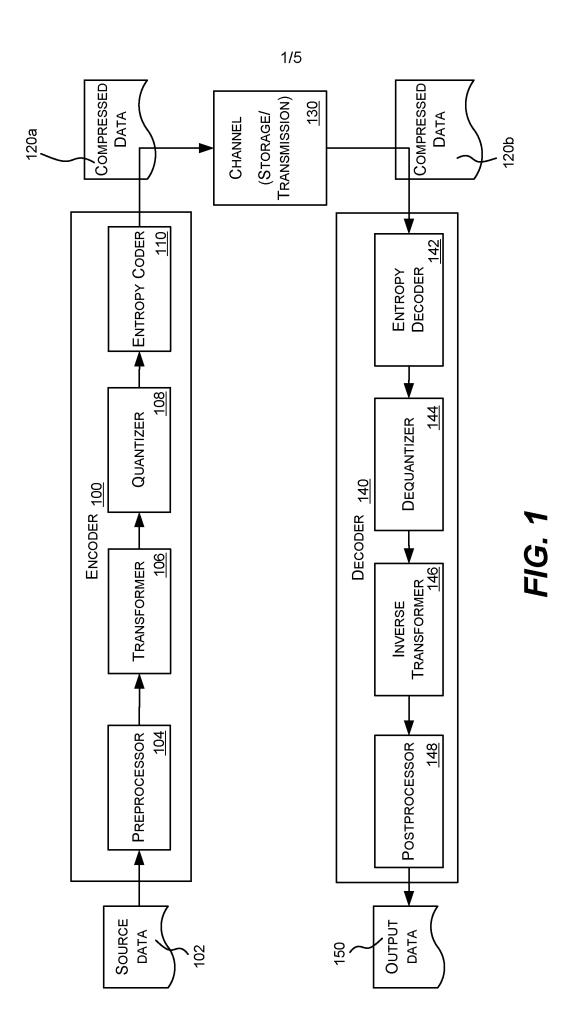
18. The system of claim 17 wherein the relationship with the selected chrominance component comprises is based upon an (x, y) coordinate pair of the selected chrominance component, and wherein the inter-color context events

generator (344) generates four context events by processing coefficients of luminance components, including a first luminance component at an (x, y) coordinate pair that corresponds to the chrominance component's (x, y) coordinate pair, a second context event based on luminance coefficients that have a horizontal relationship with the (x, y) coordinate pair, a third context event based on luminance coefficients that have a vertical relationship with the (x, y) coordinate pair, and a fourth context event based on luminance coefficients that have a diagonal relationship with the (x, y) coordinate pair.

19. The system of claim 17 wherein the encoder (100) includes a preprocessor (104) that separates color data signals into luminance components corresponding to a grayscale image and chrominance components that provide color information, a transformer (106) coupled to the preprocessor to perform frequency transformation on the output of preprocessor into frequency-transformed data values, a quantizer (108) coupled to the transformer to aggregate the data values into aggregated data values, and an entropy coder (110) coupled to the quantizer to generate the compressed image data from the aggregated data values, the entropy coder including the zero coding pass component,

the sign coding pass component, the magnitude refinement coding pass component, and the context-based arithmetic coding component.

20. The system of claim 17 further comprising a channel (130) for transmitting or storing the compressed image data, or for both storing and transmitting the compressed image data, and further comprising a decoder (140) including an entropy decoder (142) that processes the compressed image data into aggregated data values, a dequantizer (144) coupled to the entropy decoder to convert the aggregated data values into frequency transformed data values, and an inverse transformer (146) coupled to the dequantizer to process frequency transformed data values into luminance components corresponding to a grayscale image and chrominance components that provide color information.



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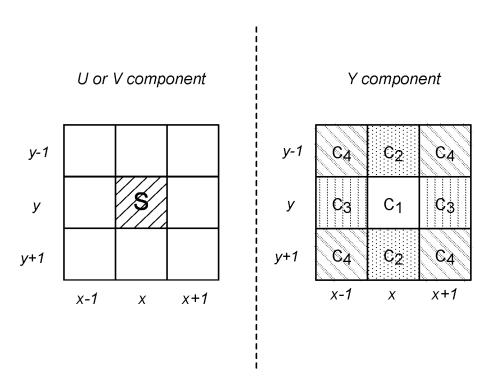


FIG. 2

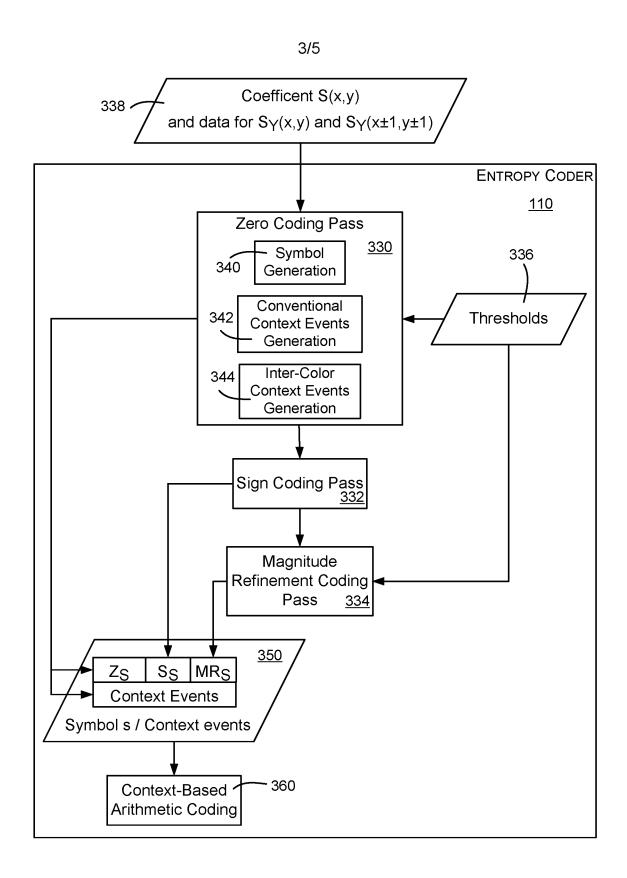
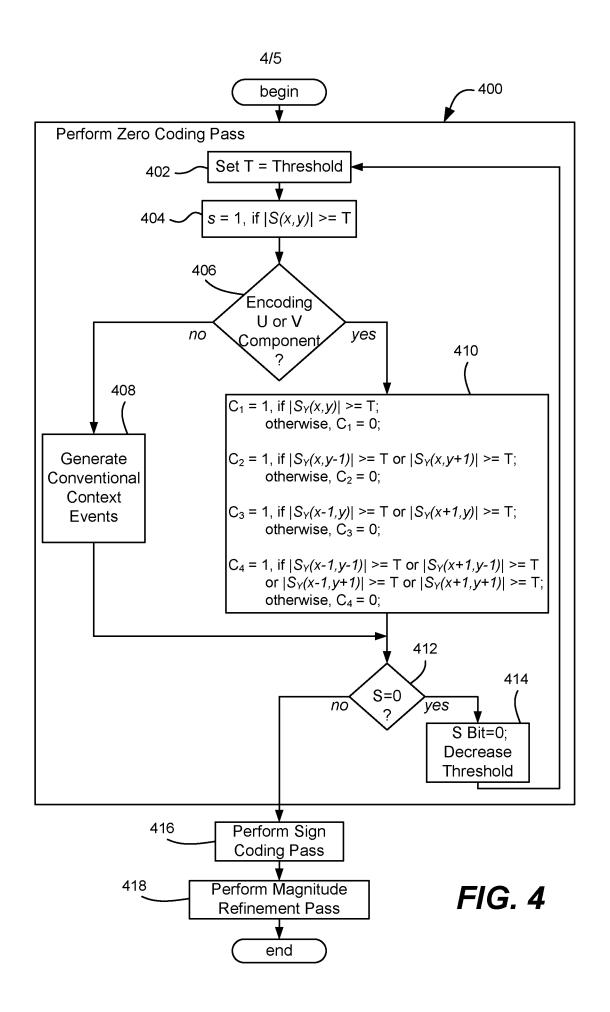
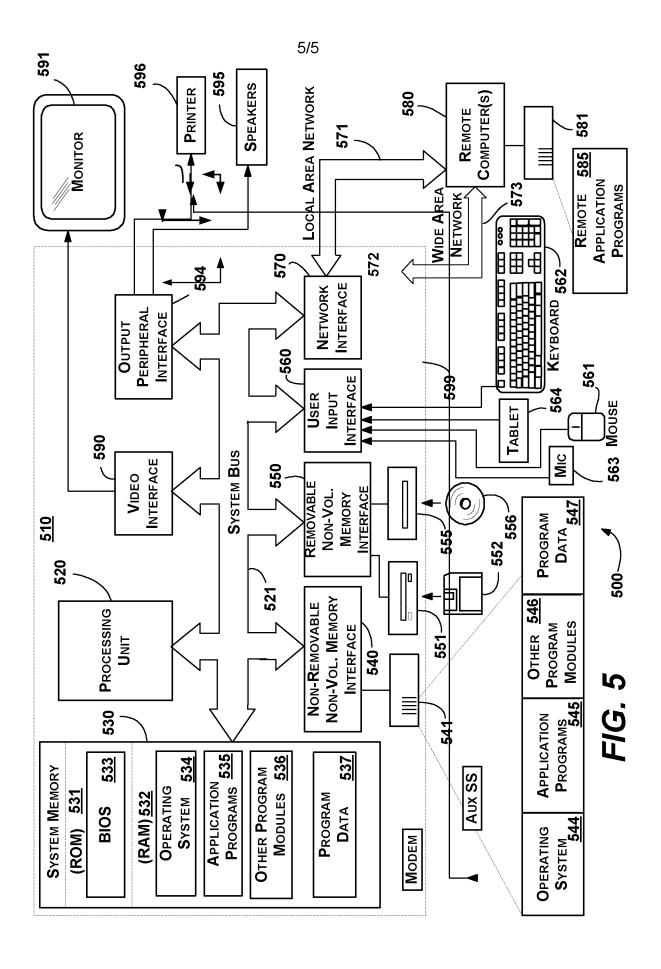


FIG. 3





INTERNATIONAL SEARCH REPORT

International application No. PCT/US2007/080012

A. CLASSIFICATION OF SUBJECT MATTER

H04N 7/24(2006.01)i, H04N 7/26(2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols) IPC 8 H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Korean Utility models and applications for Utility Models: IPC as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) eKIPASS(KIPO internal): "luminance, context-based arithmetic coding"

DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	KR 2005003029 A (DAEWOO ELECTRONICS CO.) 30. Jun. 2003. See abstract, page 2, line 23 - page 3, line 34, claim 3 and 4.	1-20
A	US 6285795 B1 (SUNG-RYUL CHO) 4. Sep. 2001. See abstract, column 1, line 5 - column 5, line 67.	1-20
A	US 6243418 B1 (JIN-HUN KIM) 5. Jun. 2001. See abstract, column 1, line 5 - column 4, line 46.	1-20

Further documents are listed in the continuation of Box C.

See patent family annex.

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- "&" document member of the same patent family

Date of the actual completion of the international search

13 FEBRUARY 2008 (13.02.2008)

Date of mailing of the international search report

13 FEBRUARY 2008 (13.02.2008)

Name and mailing address of the ISA/KR



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INTERNATIONAL SEARCH REPORT

International application No.

Information on patent family members

PCT/US2007/080012

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