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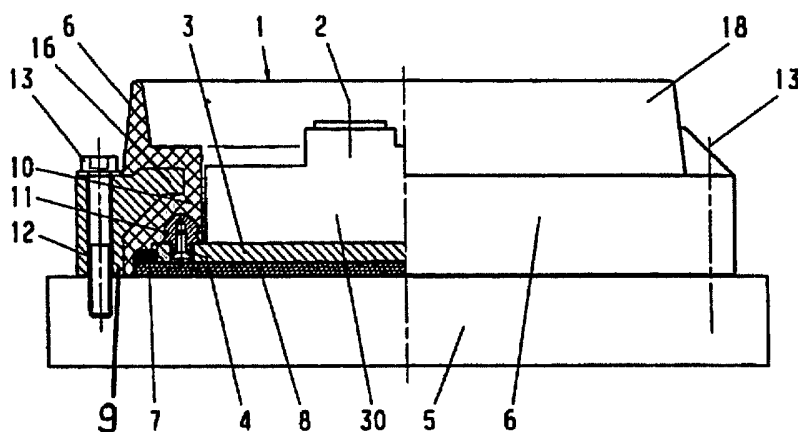
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(54) Title: SEMICONDUCTOR MODULE



(57) Abstract: A semiconductor module (1) is proposed whose semiconductor chips are arranged on a base/intermediate plate (3), having an insulating frame (6), a ceramic plate (8), which is arranged between the base/intermediate plate and a cooler (5) provided for the mounting of the semiconductor module, the insulating frame (6) enclosing the semiconductor module and being designed for the fixing thereof on the cooler (5) and also having at least the structural height of the semiconductor module, and there being a potential connection (14, 15) between gate and/or emitter (2, 21) of the

semiconductor module (1), on the one hand, and the base/intermediate plate (3), on the other hand.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01L23/50 H01L25/07 H01L23/60

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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Y	EP 0 547 877 A (MITSUBISHI ELECTRIC CORP) 23 June 1993 (1993-06-23) figures 3,4,6 ---	1,2,8
Y	EP 0 825 649 A (FUJI ELECTRIC CO LTD) 25 February 1998 (1998-02-25) abstract; figure 1A ---	1,2,8
A	HERRMANN J ET AL: "FARADAY SHIELD IMPLEMENTATION IN A POWER STACK FOR A HYBRID CIRCUIT" MOTOROLA TECHNICAL DEVELOPMENTS, MOTOROLA INC. SCHAUMBURG, ILLINOIS, US, vol. 10, 1 March 1990 (1990-03-01), page 99 XP000114671 ISSN: 0887-5286 the whole document --- -/--	1-11



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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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