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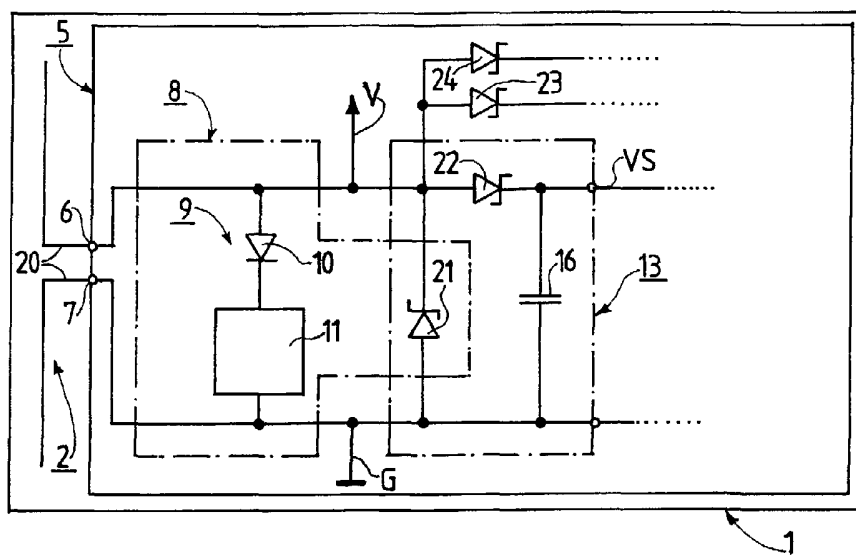
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(54) Title: DATA CARRIER COMPRISING AN INTEGRATED CIRCUIT WITH AN ESD PROTECTION CIRCUIT



(57) Abstract: In the case of a data carrier (1) or an integrated circuit (5) for the data carrier (1), an ESD protection circuit (8) is formed by means of a series connection (9) comprising a first protection diode (10) and a protection stage (11) together with a second protection diode connected in parallel with the series connection (9), and a rectifier circuit (13) connected with the ESD protection circuit (8) is provided, which comprises a rectifier diode connected in parallel with the ESD protection circuit (8), wherein the rectifier diode takes the form of a Schottky diode (21) with a parasitic p/n junction (26) and wherein the Schottky diode (21) with the parasitic p/n junction (26) forms the second protection diode of the ESD protection circuit (8).

INTERNATIONAL SEARCH REPORT

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Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, IBM-TDB, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO 01/09640 A (FORSTER IAN JAMES ; MARCONI CASWELL LTD (GB)) 8 February 2001 (2001-02-08) abstract page 6, line 13 - line 17 page 13, line 11 - line 25 figure 1	1-4
Y	US 5 338 964 A (BERNIER ERIC) 16 August 1994 (1994-08-16) abstract column 1, line 41 - line 49 column 3, line 9 - line 44 figures 2,3a	1-4

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☒ Patent family members are listed in annex.

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