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(54) **AUDIO AMPLIFICATION ELECTRONIC DEVICE WITH INDEPENDENT PITCH AND BASS RESPONSE ADJUSTMENT**

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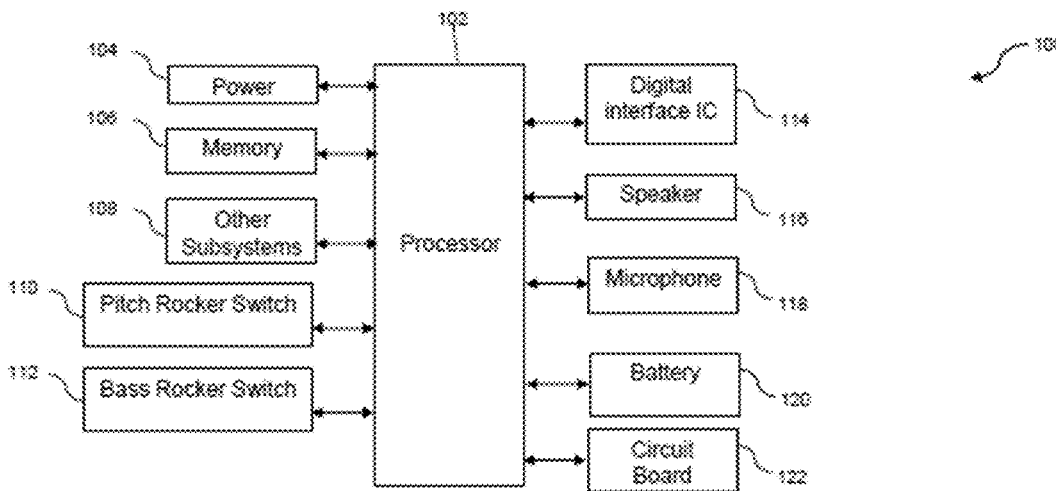
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(57) **ABSTRACT**

Techniques used to selectively amplify audio signals are described herein in connection with audio amplification electronic devices, such as hearing aids, including over-the-ear hearing aids. A device and its operation are described to facilitate setting low and high tone/volume controls separately, using at least two selection mechanisms. In one aspect, a first selection mechanism includes a pitch frequency control rocker switch and the second selection mechanism includes a bass frequency control rocker switch disposed separately. In one aspect, the bass frequency control rocker switch causes a processor to bias the frequency response of the sound amplifier for frequencies below 1 kHz. In another aspect, the pitch frequency control rocker switch causes a processor to bias the frequency response of the hearing for frequencies above 1 kHz. In another aspect, the selection mechanism involves the separate attenuation of treble and bass adjustments in response to a user selection of a rocker switch setting for each adjustment.



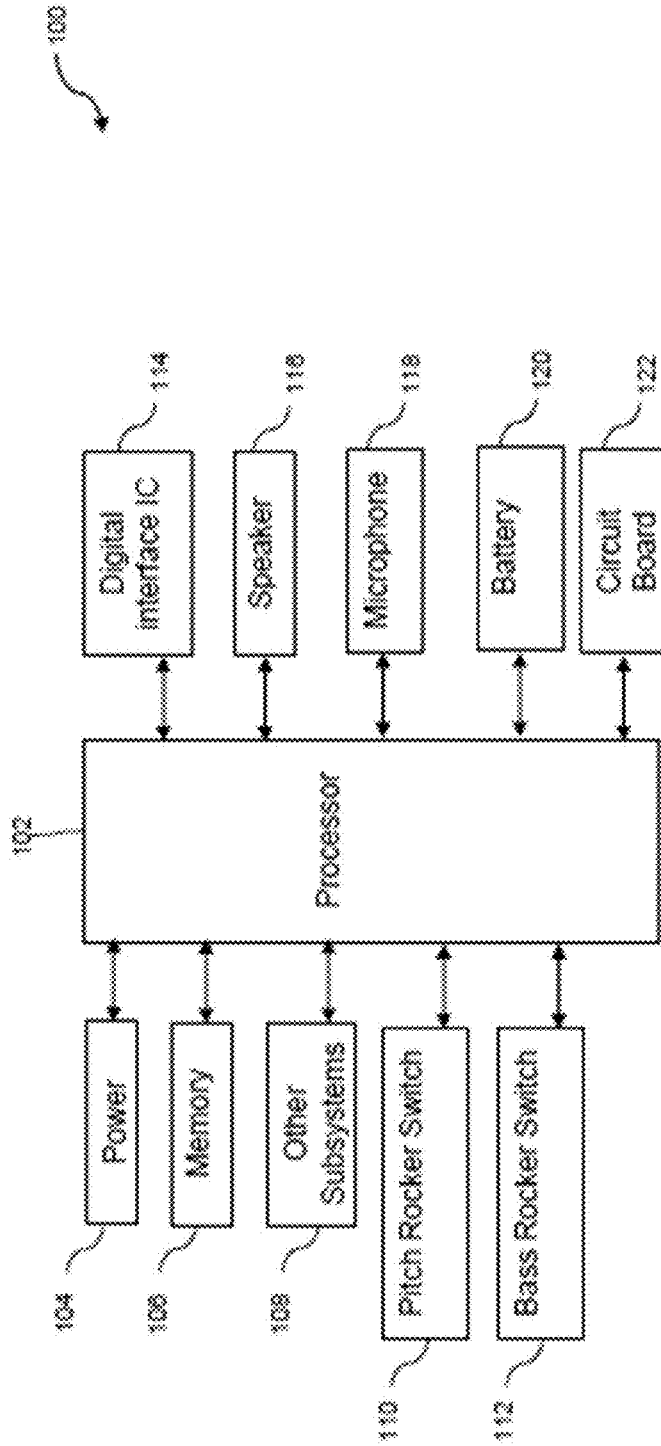


FIG. 1

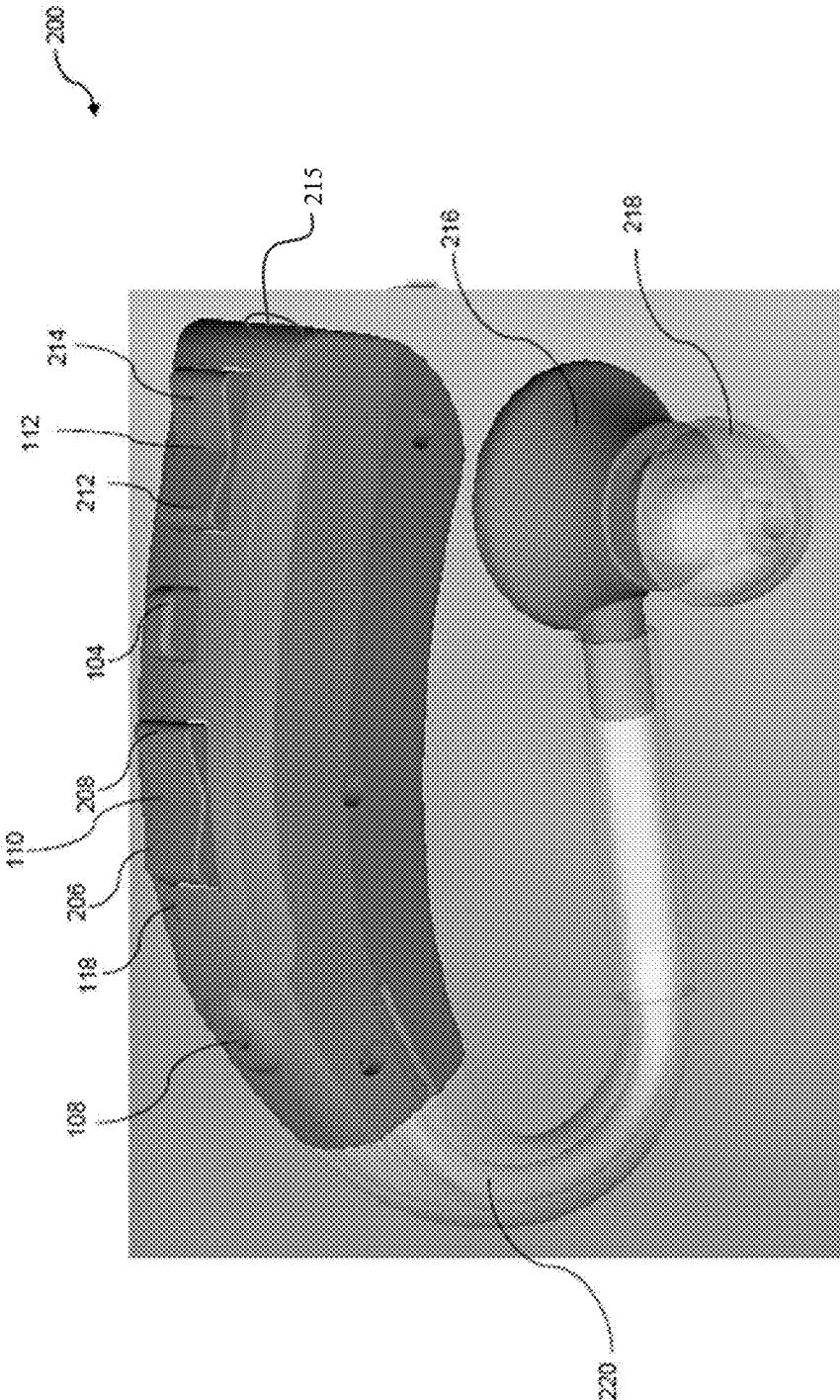


FIG. 2

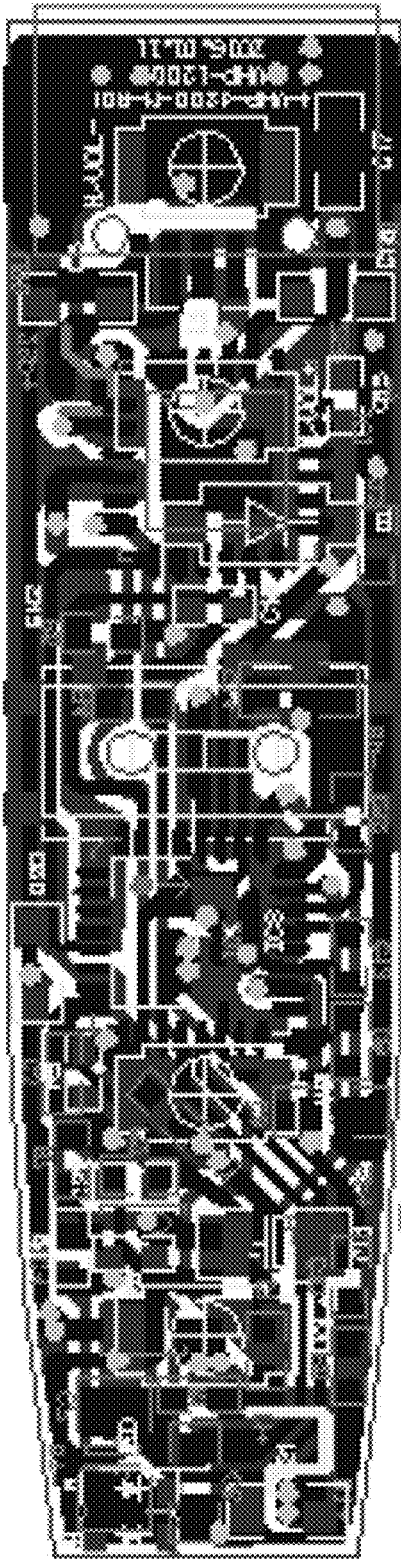


FIG. 3
300

FIG. 5

NO	PART(S)	SIZE	POSITION	QTY
1	Chip Resistor	500 200 2.0K0	31, 33	2
2	Chip Resistor	500 200 330.0	32	2
3	Chip Resistor	500 200 10.0	34	2
4	Chip Resistor	500 200 3.7K0	35, 36	2
5	Chip Resistor	500 200 10.0	37	2
6	Chip Resistor	500 200 670.0	38	2
7	Chip Resistor	500 200 100.0	39	2
8	Chip Resistor	500 200 100K0	40, 41	2
9	Chip Resistor	500 200 200.0	42, 43	2
10	Chip Resistor	500 200 100.0	44, 45	2
11	Chip Resistor	500 200 200	46, 47	2
12	Chip Resistor	500 200 200	48, 49	2
13	Chip Resistor	500 200 200	50, 51	2
14	Chip Resistor	500 200 200	52, 53	2
15	Chip Resistor	500 200 200	54, 55	2
16	Chip Resistor	500 200 200	56, 57	2
17	Chip Resistor	500 200 200	58, 59	2
18	Chip Resistor	500 200 200	60, 61	2
19	Chip Resistor	500 200 200	62, 63	2
20	Chip Resistor	500 200 200	64, 65	2
21	Chip Resistor	500 200 200	66, 67	2
22	Chip Resistor	500 200 200	68, 69	2
23	Chip Resistor	500 200 200	70, 71	2
24	Chip Resistor	500 200 200	72, 73	2
25	Chip Resistor	500 200 200	74, 75	2
26	Chip Resistor	500 200 200	76, 77	2
27	Chip Resistor	500 200 200	78, 79	2
28	Chip Resistor	500 200 200	80, 81	2
29	Chip Resistor	500 200 200	82, 83	2
30	Chip Resistor	500 200 200	84, 85	2
31	Chip Resistor	500 200 200	86, 87	2
32	Chip Resistor	500 200 200	88, 89	2
33	Chip Resistor	500 200 200	90, 91	2
34	Chip Resistor	500 200 200	92, 93	2
35	Chip Resistor	500 200 200	94, 95	2
36	Chip Resistor	500 200 200	96, 97	2
37	Chip Resistor	500 200 200	98, 99	2
38	Chip Resistor	500 200 200	100, 101	2
39	Chip Resistor	500 200 200	102, 103	2
40	Chip Resistor	500 200 200	104, 105	2
41	Chip Resistor	500 200 200	106, 107	2
42	Chip Resistor	500 200 200	108, 109	2
43	Chip Resistor	500 200 200	110, 111	2
44	Chip Resistor	500 200 200	112, 113	2
45	Chip Resistor	500 200 200	114, 115	2
46	Chip Resistor	500 200 200	116, 117	2
47	Chip Resistor	500 200 200	118, 119	2
48	Chip Resistor	500 200 200	120, 121	2
49	Chip Resistor	500 200 200	122, 123	2
50	Chip Resistor	500 200 200	124, 125	2
51	Chip Resistor	500 200 200	126, 127	2
52	Chip Resistor	500 200 200	128, 129	2
53	Chip Resistor	500 200 200	130, 131	2
54	Chip Resistor	500 200 200	132, 133	2
55	Chip Resistor	500 200 200	134, 135	2
56	Chip Resistor	500 200 200	136, 137	2
57	Chip Resistor	500 200 200	138, 139	2
58	Chip Resistor	500 200 200	140, 141	2
59	Chip Resistor	500 200 200	142, 143	2
60	Chip Resistor	500 200 200	144, 145	2
61	Chip Resistor	500 200 200	146, 147	2
62	Chip Resistor	500 200 200	148, 149	2
63	Chip Resistor	500 200 200	150, 151	2
64	Chip Resistor	500 200 200	152, 153	2
65	Chip Resistor	500 200 200	154, 155	2
66	Chip Resistor	500 200 200	156, 157	2
67	Chip Resistor	500 200 200	158, 159	2
68	Chip Resistor	500 200 200	160, 161	2
69	Chip Resistor	500 200 200	162, 163	2
70	Chip Resistor	500 200 200	164, 165	2
71	Chip Resistor	500 200 200	166, 167	2
72	Chip Resistor	500 200 200	168, 169	2
73	Chip Resistor	500 200 200	170, 171	2
74	Chip Resistor	500 200 200	172, 173	2
75	Chip Resistor	500 200 200	174, 175	2
76	Chip Resistor	500 200 200	176, 177	2
77	Chip Resistor	500 200 200	178, 179	2
78	Chip Resistor	500 200 200	180, 181	2
79	Chip Resistor	500 200 200	182, 183	2
80	Chip Resistor	500 200 200	184, 185	2
81	Chip Resistor	500 200 200	186, 187	2
82	Chip Resistor	500 200 200	188, 189	2
83	Chip Resistor	500 200 200	190, 191	2
84	Chip Resistor	500 200 200	192, 193	2
85	Chip Resistor	500 200 200	194, 195	2
86	Chip Resistor	500 200 200	196, 197	2
87	Chip Resistor	500 200 200	198, 199	2
88	Chip Resistor	500 200 200	200, 201	2
89	Chip Resistor	500 200 200	202, 203	2
90	Chip Resistor	500 200 200	204, 205	2
91	Chip Resistor	500 200 200	206, 207	2
92	Chip Resistor	500 200 200	208, 209	2
93	Chip Resistor	500 200 200	210, 211	2
94	Chip Resistor	500 200 200	212, 213	2
95	Chip Resistor	500 200 200	214, 215	2
96	Chip Resistor	500 200 200	216, 217	2
97	Chip Resistor	500 200 200	218, 219	2
98	Chip Resistor	500 200 200	220, 221	2
99	Chip Resistor	500 200 200	222, 223	2
100	Chip Resistor	500 200 200	224, 225	2
101	Chip Resistor	500 200 200	226, 227	2
102	Chip Resistor	500 200 200	228, 229	2
103	Chip Resistor	500 200 200	230, 231	2
104	Chip Resistor	500 200 200	232, 233	2
105	Chip Resistor	500 200 200	234, 235	2
106	Chip Resistor	500 200 200	236, 237	2
107	Chip Resistor	500 200 200	238, 239	2
108	Chip Resistor	500 200 200	240, 241	2
109	Chip Resistor	500 200 200	242, 243	2
110	Chip Resistor	500 200 200	244, 245	2
111	Chip Resistor	500 200 200	246, 247	2
112	Chip Resistor	500 200 200	248, 249	2
113	Chip Resistor	500 200 200	250, 251	2
114	Chip Resistor	500 200 200	252, 253	2
115	Chip Resistor	500 200 200	254, 255	2
116	Chip Resistor	500 200 200	256, 257	2
117	Chip Resistor	500 200 200	258, 259	2
118	Chip Resistor	500 200 200	260, 261	2
119	Chip Resistor	500 200 200	262, 263	2
120	Chip Resistor	500 200 200	264, 265	2
121	Chip Resistor	500 200 200	266, 267	2
122	Chip Resistor	500 200 200	268, 269	2
123	Chip Resistor	500 200 200	270, 271	2
124	Chip Resistor	500 200 200	272, 273	2
125	Chip Resistor	500 200 200	274, 275	2
126	Chip Resistor	500 200 200	276, 277	2
127	Chip Resistor	500 200 200	278, 279	2
128	Chip Resistor	500 200 200	280, 281	2
129	Chip Resistor	500 200 200	282, 283	2
130	Chip Resistor	500 200 200	284, 285	2
131	Chip Resistor	500 200 200	286, 287	2
132	Chip Resistor	500 200 200	288, 289	2
133	Chip Resistor	500 200 200	290, 291	2
134	Chip Resistor	500 200 200	292, 293	2
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136	Chip Resistor	500 200 200	296, 297	2
137	Chip Resistor	500 200 200	298, 299	2
138	Chip Resistor	500 200 200	300, 301	2
139	Chip Resistor	500 200 200	302, 303	2
140	Chip Resistor	500 200 200	304, 305	2
141	Chip Resistor	500 200 200	306, 307	2
142	Chip Resistor	500 200 200	308, 309	2
143	Chip Resistor	500 200 200	310, 311	2
144	Chip Resistor	500 200 200	312, 313	2
145	Chip Resistor	500 200 200	314, 315	2
146	Chip Resistor	500 200 200	316, 317	2
147	Chip Resistor	500 200 200	318, 319	2
148	Chip Resistor	500 200 200	320, 321	2
149	Chip Resistor	500 200 200	322, 323	2
150	Chip Resistor	500 200 200	324, 325	2
151	Chip Resistor	500 200 200	326, 327	2
152	Chip Resistor	500 200 200	328, 329	2
153	Chip Resistor	500 200 200	330, 331	2
154	Chip Resistor	500 200 200	332, 333	2
155	Chip Resistor	500 200 200	334, 335	2
156	Chip Resistor	500 200 200	336, 337	2
157	Chip Resistor	500 200 200	338, 339	2
158	Chip Resistor	500 200 200	340, 341	2
159	Chip Resistor	500 200 200	342, 343	2
160	Chip Resistor	500 200 200	344, 345	2
161	Chip Resistor	500 200 200	346, 347	2
162	Chip Resistor	500 200 200	348, 349	2
163	Chip Resistor	500 200 200	350, 351	2
164	Chip Resistor	500 200 200	352, 353	2
165	Chip Resistor	500 200 200	354, 355	2
166	Chip Resistor	500 200 200	356, 357	2
167	Chip Resistor	500 200 200	358, 359	2
168	Chip Resistor	500 200 200	360, 361	2
169	Chip Resistor	500 200 200	362, 363	2
170	Chip Resistor	500 200 200	364, 365	2
171	Chip Resistor	500 200 200	366, 367	2
172	Chip Resistor	500 200 200	368, 369	2
173	Chip Resistor	500 200 200	370, 371	2
174	Chip Resistor	500 200 200	372, 373	2
175	Chip Resistor	500 200 200	374, 375	2
176	Chip Resistor	500 200 200	376, 377	2
177	Chip Resistor	500 200 200	378, 379	2
178	Chip Resistor	500 200 200	380, 381	2
179	Chip Resistor	500 200 200	382, 383	2
180	Chip Resistor	500 200 200	384, 385	2
181	Chip Resistor	500 200 200	386, 387	2
182	Chip Resistor	500 200 200	388, 389	2
183	Chip Resistor	500 200 200	390, 391	2
184	Chip Resistor	500 200 200	392, 393	2
185	Chip Resistor	500 200 200	394, 395	2
186	Chip Resistor	500 200 200	396, 397	2
187	Chip Resistor	500 200 200	398, 399	2
188	Chip Resistor	500 200 200	400, 401	2
189	Chip Resistor	500 200 200	402, 403	2
190	Chip Resistor	500 200 200	404, 405	2
191	Chip Resistor	500 200 200	406, 407	2
192	Chip Resistor	500 200 200	408, 409	2
193	Chip Resistor	500 200 200	410, 411	2
194	Chip Resistor	500 200 200	412, 413	2
195	Chip Resistor	500 200 200	414, 415	2
196	Chip Resistor	500 200 200	416, 417	2
197	Chip Resistor	500 200 200	418, 419	2
198	Chip Resistor	500 200 200	420, 421	2
199	Chip Resistor	500 200 200	422, 423	2
200	Chip Resistor	500 200 200	424, 425	2
201	Chip Resistor	500 200 200	426, 427	2
202	Chip Resistor	500 200 200	428, 429	2
203	Chip Resistor	500 200 200	430, 431	2
204	Chip Resistor	500 200 200	432, 433	2
205	Chip Resistor	500 200 200	434, 435	2
206	Chip Resistor	500 200 200	436, 437	2
207	Chip Resistor	500 200 200	438, 439	2
208	Chip Resistor	500 200 200	440, 441	2
209	Chip Resistor	500 200 200	442, 443	2
210	Chip Resistor	500 200 200	444, 445	2
211	Chip Resistor	500 200 200	446, 447	2
212	Chip Resistor	500 200 200	448, 449	2
213	Chip Resistor	500 200 200	450, 451	2
214	Chip Resistor	500 200 200	452, 453	2
215	Chip Resistor	500 200 200	454, 455	2
216	Chip Resistor	500 200 200	456, 457	2
217	Chip Resistor	500 200 200	458, 459	2
218	Chip Resistor	500 200 200	460, 461	2
219	Chip Resistor	500 200 200	462, 463	2
220	Chip Resistor	500		

1	Max. saturation sound pressure level(OSPL90)	$\leq 129+3\text{dB}$
2	Full on Acoustic Gain	$38\pm 5\text{ dB}$
3	Total Harmonic Distortion(THD)	$\leq 10\%$
4	Equivalent Input Noise	$\leq 32\text{dB}$
5	Frequency Response	$450\sim 3000\text{Hz}$
6	Current Drain	$\leq 1.5\text{mA}$

FIG. 6

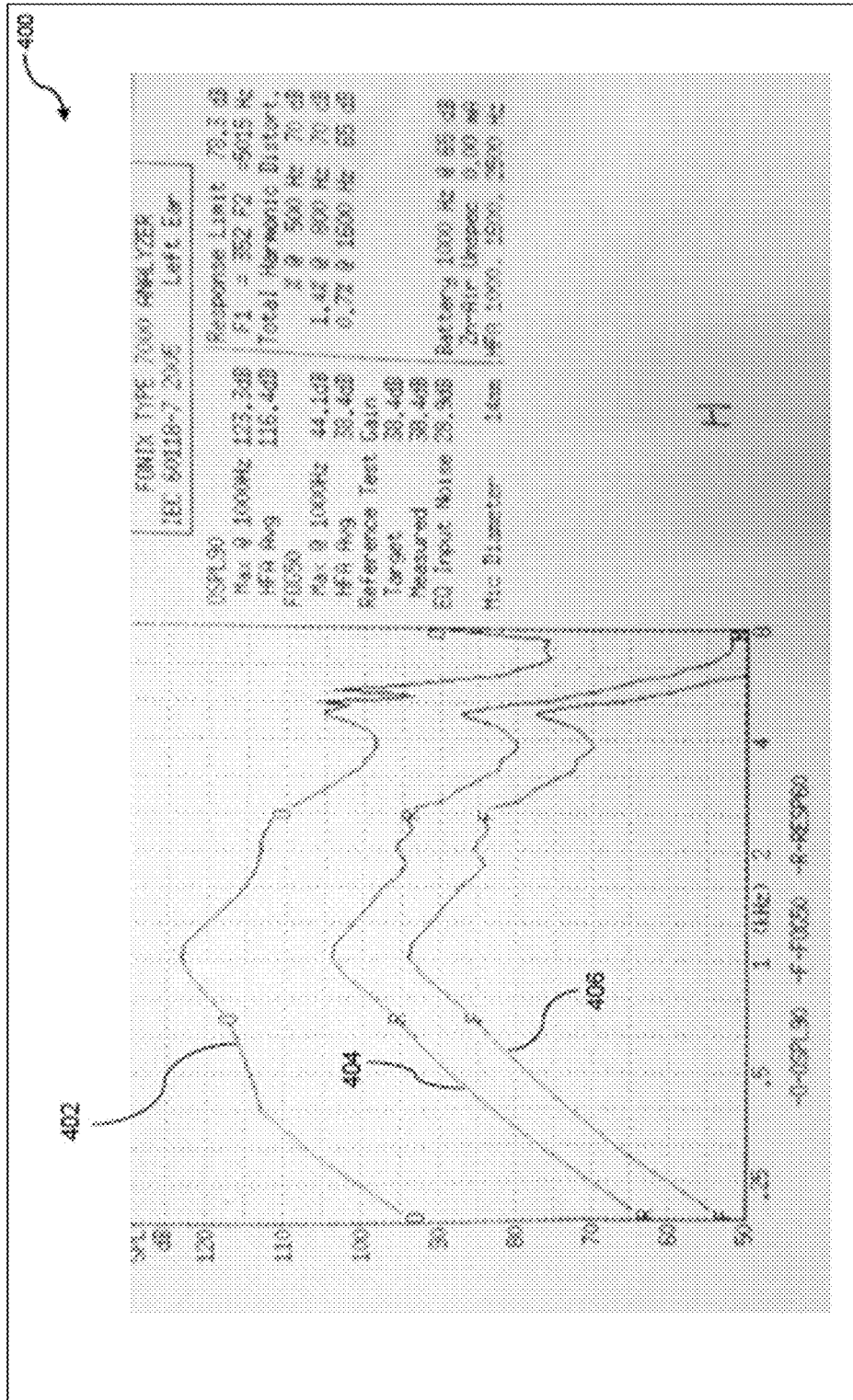


FIG. 7

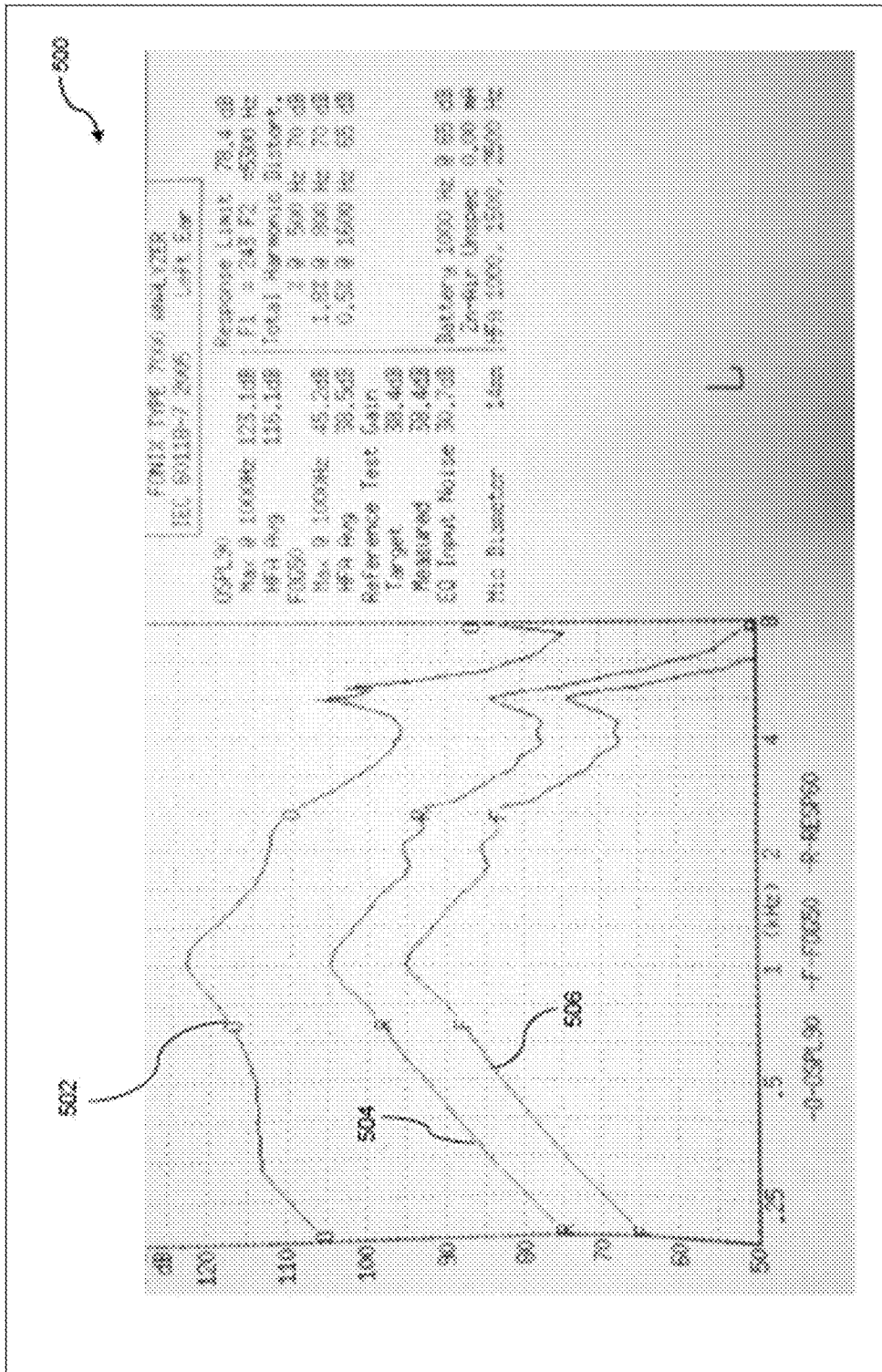


FIG. 8

**AUDIO AMPLIFICATION ELECTRONIC
DEVICE WITH INDEPENDENT PITCH AND
BASS RESPONSE ADJUSTMENT**

CROSS-REFERENCE TO RELATED
APPLICATION

[0001] This application claims the benefit of U.S. Provisional Application No. 62/320,672, filed Apr. 11, 2016, the content of which is incorporated herein by reference in its entirety.

TECHNICAL FIELD

[0002] The present disclosure relates to audio amplification electronic devices, and more specifically to sound amplifiers, such as hearing aid devices.

BACKGROUND

[0003] Hearing loss is a common condition within the human population and the manifestation of hearing loss can have a significant impact to the quality of human life. There are many factors that can induce hearing loss which may include disease, genetic disposition, injury, and normal aging. However, different human individuals often exhibit varying levels and manifestations of hearing loss that may change over time. Furthermore, the audio environment that the individual is placed in may have a significant impact to the ability to hear desired sounds. For example, an individual that is in a small room setting while attempting to listen to another individual speak within a relatively quiet amount of ambient background noise may have difficulty depending on the speech characteristics of the person trying to speak, while the same individual who is trying to listen is placed in a crowded room or environment, such as a restaurant, may hear a high amount of sound energy, but the ambient background noise is relatively high resulting in a poor ability for the hearing individual to hear and understand individuals who may be speaking to the hearing individual.

[0004] The hearing loss may manifest as an attenuation of hearing sensitivity across the full hearing audio spectrum range, the spectrum range comprising approximately 100 Hz to approximately 8000 Hz. Furthermore, an individual's hearing loss may manifest as an ability to hear higher frequencies (above 1000 Hz), but not lower frequencies (below 1000 Hz). The converse may also be true, wherein the hearing loss manifests as an ability to hear lower frequencies (below 1000 Hz), but not hear well above 1000 Hz.

[0005] Therefore, it is desirable for a manufacturer of hearing aids and like devices to be able to accommodate many individuals with varying degrees and type of hearing loss that can be adjusted for the individual in a compact device that can be worn on the body and is relatively low cost.

SUMMARY

[0006] The present disclosure is directed to an improved audio amplification electronic device. The device is configured to facilitate setting low and high tone/volume controls separately, using at least two selection mechanisms. In one aspect, a first selection mechanism includes a pitch frequency control rocker switch and the second selection mechanism includes a bass frequency control rocker switch disposed separately. In one aspect, the bass frequency con-

trol rocker switch causes a processor to bias the frequency response of the sound amplifier for frequencies below 1 kHz. In another aspect, the pitch frequency control rocker switch causes a processor to bias the frequency response of the hearing for frequencies above 1 kHz.

[0007] In another aspect, the selection mechanism involves the separate attenuation of treble and bass adjustments in response to a user selection of a rocker switch setting for each adjustment.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] FIG. 1 shows a block diagram in accordance with an exemplary embodiment of an electronic device as a sound amplifier according to the present invention in the form of a hearing aid device generally designated at 100.

[0009] FIG. 2 shows a mechanical representation, generally designated at 200, in accordance with the exemplary embodiment.

[0010] FIG. 3 shows an example printed circuit board layout of a circuit board 122 in accordance with the exemplary embodiment.

[0011] FIG. 4 shows the circuit diagram 300 for the hearing aid device designated at 100.

[0012] FIG. 5 is a table showing the component count and specification for the circuit diagram 300 and the assembly of the hearing aid 100 in the current exemplary embodiment.

[0013] FIG. 6 is a table showing the technical specification details for the hearing aid 100 in the current exemplary embodiment.

[0014] FIG. 7 shows the frequency response of adjusting pitch controls in accordance with the exemplary embodiment.

[0015] FIG. 8 shows the frequency response of adjusting bass controls in accordance with the exemplary embodiment.

DETAILED DESCRIPTION

[0016] The techniques described herein may be used in any device that is used to selectively amplify audio signals. Desired frequency responses may be realized through digital filters such as finite impulse response (FIR) or infinite impulse response (IIR) filters. Furthermore, desired frequency responses may also be realized through use of analog filters, or the combination of digital and analog filters, as is known in the art.

[0017] FIG. 1 shows a block diagram in accordance with the exemplary embodiment of an electronic device as a sound amplifier 100 according to the present invention. In the exemplary embodiment, the sound amplifier 100 is a hearing aid comprising multiple components, such as a processor 102 that controls the overall operation of hearing aid 100. Processor 102 is coupled to memory 106, which may be random access memory (RAM) used during operation (e.g. for manipulating output signals, processing input signals, etc.), and/or Read Only Memory (ROM) or flash memory, where software resides to instruct processor 102 to control the overall operation of hearing aid 100.

[0018] Processor 102 may also have a power control module 104 coupled to manage battery life and minimize power usage of the device. Digital interface IC 114 is coupled to processor 102 and may comprise analog audio conditioning circuitry such as Analogue to Digital (A/D) and Digital to Analogue (D/A) converters, audio power ampli-

fiers, and may have the ability to perform digital or analog filtering of desired responses. Furthermore, digital interface IC 114 may also condition analog signals received from microphone 108. A main inventive step of hearing aid 100 is the ability for a user to independently control the frequency response of amplified ambient audio signals, depending on the user preference, alleviating the need to have a medical doctor or practitioner to perform the necessary tuning of the hearing aid device every time retuning is required. It is desirable to enable the ability to independently control pitch (frequencies above 1000 Hz) and bass (frequencies below 1000 Hz) but in a compact form factor that is easy to use. If too many external controls exist for hearing aid 100, then the device must have a larger physical footprint, which is not desirable. Therefore, hearing aid 100 further comprises pitch rocker switch 110 and bass rocker switch 112 which are coupled to processor 102 and are large enough for an average user to actuate, but small enough to not impact the overall physical footprint of hearing aid 100. Hearing aid 100 further comprises speaker 116, microphone 108, battery 120, and circuit board 122 coupled to processor 102. Speaker 116 outputs an amplified audio signal that is heard by the user of hearing aid 100. Circuit board 122 is a compact electronic multi-layer printed circuit board as known in the art, and all electrical components of hearing aid 100 are coupled to it, using techniques known in the art. Hearing aid 100 may further comprise other subsystems 108 coupled to processor 102. Examples of other subsystems 108 may include a USB charging port, one or more light indicators (not shown), and the like.

[0019] FIG. 2 shows a mechanical representation 200 of hearing aid 100 in accordance with the exemplary embodiment. The exterior of hearing aid 100 comprises charging light indicator 118, microphone 108, bass rocker switch 110, power switch 104 and pitch rocker switch 112. Rocker switch 110 comprises a three position switch which functions to increase bass frequency response when pressed into position 206, decrease bass frequency response when pressed into position 208, and not adjusting the frequency response from the current setting which is the middle position that is the default when rocker switch 110 is not being actuated by a user. In a similar manner, rocker switch 112 comprises a three position switch which functions to increase pitch frequency response when pressed into position 212, decrease pitch frequency response when pressed into position 214, and not adjusting the frequency response from the current setting which is the middle position that is the default when rocker switch 112 is not being actuated by a user. Rocker switches 110 and 112 are known in the art, and the configuration of which position of either rocker switches 110 and 112 corresponds to increasing or decreasing a frequency response may be reversed, as a skilled artisan would understand. Mechanically, hearing aid 100 further comprises a charging port 215 (mini USB, or micro USB, or other compact port specification), a mechanical audio coupler 220, 216, and earpiece 218 which channel audio output by speaker 116 into a user's ear. The mechanical audio coupler 220 is formed into an ear hook component for securing the hearing aid device onto its user's ear, which in turn is mechanically coupled via a tube to an ear mold 216 upon which the earpiece 218 is attached. The ear mold 216 helps the earpiece 218 be accurately positioned at the outer opening of the ear canal. Speaker 116 is located at the end of the mechanical audio coupler near or on the printed circuit

board in the main body of the hearing aid 100 and away from the ear mold 216. Hearing aid 100 is classified as an "over the ear" device, a designation known well in the art.

[0020] In a variation of the present exemplary embodiment of the invention shown in FIG. 2, speaker 116 may be located in the ear mold 216 and close to the ear canal outer opening. The cables connecting speaker 116 with the other electronic components of the hearing aid 100 run inside the ear hook 220 and the attached tube.

[0021] FIG. 3 shows an example printed circuit board layout of circuit board 122 in accordance with the exemplary embodiment. Circuit board 122 demonstrates that all components comprising hearing aid 100 can be compactly put together into a functioning unit. In the alternative exemplary embodiment, previously discussed, speaker 116 is not located inside the printed circuit 122 but external to it, electrically coupled to the printed circuit by means of wires running inside the tube and ear hook 220.

[0022] FIG. 4 shows a circuit diagram 300 for the hearing aid device designated at 100. Circuit diagram 300 comprises a number of ICs (IC1-IC4) and other electronic components, including resistors (R1-R14), capacitors (C1-C18), speaker (SPK1), microphone (MIC1), switches (SW1, S1-S4), battery (BT1), LEDs (G, R), transistor (Q1) and USB connector (USB).

[0023] Circuit diagram 300 is characterized by four main sub-circuits 310, 320, 330 and 340.

[0024] Controller sub-circuit 310 includes IC3 which is a microprocessor or similar component and is responsible for capturing user adjustments to pitch and bass frequency amplification bias via signals from switches S1-S4. Controller sub-circuit 310 also commands the sound signal amplification sub-circuit 320 to selectively amplify the sound input signal frequencies received from microphone MIC1. These components are connected via capacitors C1-C5 and resistors R1-R3 and R15.

[0025] Sound signal amplification sub-circuit 320 comprises IC4, resistors R6-10, capacitors C6-C16 and transistor Q1. Sub-circuit 320 performs the selective sound signal amplification according to the signals received from IC3.

[0026] Battery sub-circuit 330 comprises Li-Ion battery BT1 of 3.7 volts, voltage regulating IC2 (which outputs a steady DC voltage of 1.5V feeding all sub-circuits of the circuit diagram 300), and switch SW1 which when open (default position) allows uninterrupted voltage supply to the all sub-circuits.

[0027] USB charging sub-circuit 340 allows charging battery BT1 by supplying 5-6V DC to IC1. USB charging circuit 340 is also directly connected to LEDs G (Green) and R (Red) which are also connected to IC1 and are lit by IC1 when the USB charging is in progress (Green LED is on and SW1 is closed) or disconnected (Red LED is on and SW1 is open). The USB charging sub-circuit 340 also comprises capacitors C17-C18 and resistors R12-R14.

[0028] FIG. 5 is a table showing component count and specification for the circuit diagram 300 and the assembly of the hearing aid 100 in the current exemplary embodiment. This information is presented only for exemplary purposes and it is understood that modifications to both the count and specification of the components as well as the circuit diagram 300 are possible and fall within the purpose and content of the present invention as they can be conceived and implemented by any person of ordinary skill in related art. As a result this exemplary embodiment under no cir-

cumstance limits the possible alternative embodiments that also are part of the present invention.

[0029] Similarly, FIG. 6 is a table showing the technical specification details for the hearing aid 100 in the current exemplary embodiment.

[0030] FIG. 7 shows the frequency response of adjusting pitch controls in accordance with the exemplary embodiment. Frequency response 402 depicts the highest pitch frequency response control setting. It can be seen that the relative amplitude frequency response 402 at approximately 1 kHz vs. 250 Hz is approximately 25 db, and the amplitude of the frequency response at higher frequencies (2 kHz) are only about 10 db lower than at 1 kHz. Thus there is a bias towards the higher frequencies above 1 kHz. Frequency responses 404 and 406 correspond to alternating levels of overall amplitude frequency response that the user may select via rocker switch 110. Those skilled artisans would appreciate that the number of possible frequency responses selected may be variable and not limited to 3, simply by using multiple digital or analog filters that can be implemented easily using processor 102.

[0031] FIG. 8 shows the frequency response of adjusting bass controls in accordance with the exemplary embodiment. Adjusting of bass controls is performed in a similar way as that of the pitch controls depicted in FIG. 4. Frequency response 502 depicts the highest bass frequency response control setting. It can be seen that the relative amplitude frequency response 502 at approximately 1 kHz vs. 350 Hz is approximately 10 db, and the amplitude of the frequency response at higher frequencies (2 kHz) are only about 10 db lower than at 1 kHz. Thus there is a bias towards the lower frequencies below 1 kHz. Frequency responses at 2 kHz are not as attenuated as in the pitch response case in FIG. 4 mainly due to the human ear naturally having a decreased frequency response at 2 kHz vs. low frequencies (for example 250 Hz). Frequency responses 404 and 406 correspond to alternating levels of overall amplitude frequency response that the user may select via rocker switch 112. Again, those skilled artisans would appreciate that the number of possible frequency responses selected may be variable and not limited to 3, simply by using multiple digital or analog filters that can be implemented easily using processor 102.

[0032] In accordance with an exemplary scenario, high and low volume control is set separately to address the specific and distinct needs of people with high-pitched hearing loss and low-pitched hearing loss, respectively.

[0033] From a user's perspective, the user is provided with a user manual (user guide) which instructs the user on the appropriate manner to set the device for optimum hearing. In this regard, the user may be instructed to set the hearing aid device one way, when the user suffers from high-pitched hearing loss, and a different way, when the user suffers from low-pitched hearing loss. In both instances, at initial use of operation, the user is instructed to first turn the volume to the lowest level. This is to protect the user from excessively high noise, but also because it provides a reference point to start the setting of the hearing aid device to the optimum setting.

[0034] Having minimized the volume, the user is then instructed to turn "ON" the device (via power switch 104).

[0035] The user is then guided to regulate the volume to a proper level slowly. For this step, it helps if the user is aware of his hearing loss deficiency in terms of high or low pitched hearing loss. In the case of low-pitched hearing loss, low

pitch (bass) rocker switch 110 is moved or pressed to increase bass frequency response (tone/volume control) (i.e., pressed into position 206). To control (lower) the tone/volume control when the optimum setting seems to have been exceeded, the finger is moved from position 206 to position 208 and pressed (one press at a time) to set the device to the optimum tone and volume level. The default position of the rocker switch is a middle position between positions 206 and 208. In one scenario, rocker switches return to the middle position automatically when released from either position 206 or 208. In another scenario, the rocker switch is a toggle switch and the tone/volume control is increased in predetermined time intervals up to a maximum level.

[0036] In a similar manner, in the case of high-pitched hearing loss, high pitch (treble) rocker switch 112 is moved or pressed to increase pitch frequency response (tone/volume control) (i.e., pressed into position 214).

[0037] Below are representative instructions to the user in accordance with a preferred embodiment. Each rocker switch includes (+) and (-) indications to indicate increase and decrease of tone volume control direction. Beeping is provided to provide audible indication of changes (single "beep") as well as indication that the maximum level has been reached (double "beep").

[0038] User Instructions: High Tone/Volume Control (Fit for People Who Have High-Pitched Hearing Loss)

[0039] a) Press and hold "+" to turn up the volume and high pitch level continuously, and you will hear sound "Beep". Number of levels: eight (8). When the sound reaches peak level (level 8), you will hear sound "Beep-Beep".

[0040] b) Press and hold "-" to turn down the volume and high pitch level continuously, and you will hear sound "Beep". When the sound reaches the bottom level, you will hear sound "Beep-Beep".

[0041] User Instructions: Low Tone/Volume Control (Fit for People Who Have Low-Pitched Hearing Loss)

[0042] a) Press and hold "+" to turn up the volume and low pitch level continuously and you will hear sound "Beep". Number of levels: eight (8). When the sound reaches peak level, you will hear sound "Beep-Beep".

[0043] b) Press and hold "-" to turn down the volume and low pitch level continuously, and you will hear sound "Beep". When the sound reaches the bottom level, you will hear sound "Beep-Beep".

[0044] In an alternate exemplary scenario, the user instructions are provided audibly. The instructions may include guidance on how best to set rocker switch settings for people with both high and low tone deficiencies. In some instances, for users that are not sure whether they are high or low tone deficient, they may be guided to experiment toggling between the various levels and settings until a satisfactory (best) level is detected.

[0045] It should be appreciated that one benefit of the present invention is the ability of a user to set a hearing aid device to operate/amplify high or low tones in ways which until now has been traditionally performed by programmably set analog and digital hearing devices, usually under the guidance of a doctor. The latter approach is both expensive and cumbersome. The present approach addresses the need for low cost alternatives.

[0046] While some custom digital hearing aid solutions in particular allow for tone/volume control over a predefined

frequency response curve, conventional devices do not have multiple bass and treble setting tone/volume control mechanisms as contemplated herein.

[0047] While the proposed multiple tone/control mechanisms provide a low cost alternative for people with hearing loss or similar deficiencies, these devices can also be used to amplify treble frequencies (bass frequencies) to improve hearing in outdoor (indoor) environments for better sound reception overall by a user. In a similar manner, low tone/volume control can also provide an ancillary benefit of improving special effects sounds/music for some listeners. In this regard, the presently proposed device can function as a personalized amplification device to accommodate a variety of uses and needs of different users.

[0048] The use of toggle switches is common in traditional hearing aid devices. The use of rocker switches to control tone/volume control has been proven to be easier to use. This is therefore another benefit of a preferred exemplary embodiment.

[0049] The presently proposed approach, as has been shown, is easily incorporated in a small form function as well, allowing its use in hearing aids with a conventional shape with which many elderly are accustomed and comfortable in terms of use, fit, look, and the like. The only difference, of course, is learning to set the two rocker switches to the appropriate levels.

[0050] Traditional amplification devices, particularly those with rotating controls or toggle switches to set volume levels, incorporate the power on/off functionality in the volume control mechanism. In the exemplary embodiment, a separate power switch is provided without compromising the small form factor design of the device.

[0051] Those of skill in the art would understand that signals may be represented using any of a variety of different techniques. For example, data, software, instructions, signals that may be referenced throughout the above description may be represented by voltages, currents, electromagnetic waves, magnetic fields or particles, light or any combination thereof.

[0052] Those of skill would further appreciate that the various illustrative radio frequency or analog circuit blocks described in connection with the disclosure herein may be implemented in a variety of different circuit topologies, on one or more integrated circuits, separate from or in combination with logic circuits and systems while performing the same functions described in the present disclosure.

[0053] Those of skill would also further appreciate that the various illustrative logical blocks, modules, circuits, and algorithm steps described in connection with the disclosure herein may be implemented as electronic hardware, computer software, or combinations of both. To clearly illustrate this interchangeability of hardware and software, various illustrative components, blocks, modules, circuits, and steps have been described above generally in terms of their functionality. Whether such functionality is implemented as hardware or software depends upon the particular application and design constraints imposed on the overall system. Skilled artisans may implement the described functionality in varying ways for each particular application, but such implementation decisions should not be interpreted as causing a departure from the scope of the present disclosure.

[0054] The various illustrative logical blocks, modules, and circuits described in connection with the disclosure herein may be implemented or performed with a general-

purpose processor, a digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA) or other programmable logic device, discrete gate or transistor logic, discrete hardware components, or any combination thereof designed to perform the functions described herein. A general-purpose processor may be a microprocessor, but in the alternative, the processor may be any conventional processor, controller, microcontroller, or state machine. A processor may also be implemented as a combination of computing devices, e.g., a combination of a DSP and a microprocessor, a plurality of microprocessors, one or more microprocessors in conjunction with a DSP core, or any other such configuration.

[0055] The steps of a method or algorithm described in connection with the disclosure herein may be embodied directly in hardware, in a software module executed by a processor, or in a combination of the two. A software module may reside in RAM memory, flash memory, ROM memory, EPROM memory, EEPROM memory, registers, hard disk, a removable disk, a CD-ROM, or any other form of storage medium known in the art. An exemplary storage medium is coupled to the processor such that the processor may read information from, and write information to, the storage medium. In the alternative, the storage medium may be integral to the processor. The processor and the storage medium may reside in an ASIC. The ASIC may reside in a user terminal. In the alternative, the processor and the storage medium may reside as discrete components in a user terminal.

[0056] The previous description of the disclosure is provided to enable any person skilled in the art to make or use the disclosure. Various modifications to the disclosure will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other variations without departing from the scope of the disclosure. Thus, the disclosure is not intended to be limited to the examples and designs described herein but are to be accorded the widest scope consistent with the principles and novel features disclosed herein.

What is claimed is:

1. An electronic device comprising:
 - a processor;
 - a memory;
 - a pitch frequency adjustment rocker switch coupled to the processor, the pitch frequency adjustment rocker switch, when actuated, causing the processor to bias a pitch frequency response of the electronic device for frequencies above 1 kHz; and
 - a bass frequency adjustment rocker switch coupled to the processor, the bass frequency adjustment rocker switch, when actuated, causing the processor to bias a bass frequency response of the electronic device for frequencies below 1 kHz.
2. The electronic device of claim 1, wherein the electronic device is a sound amplifier.
3. The electronic device of claim 2, wherein the sound amplifier is a hearing aid.
4. The electronic device of claim 3, wherein the hearing aid is an over-the-ear-type hearing aid.
5. The electronic device of claim 1, wherein the processor has a power control module for managing a battery life of the electronic device.
6. The electronic device of claim 4, where the over-the-ear-type hearing aid comprises a speaker located on or

substantially close to a printed circuit where the processor is located, said speaker is coupled via a mechanical audio coupler to an ear piece configured for use at an outer opening of an ear canal of a user of the electronic device.

7. The electronic device of claim 4, where the over-the-ear-type hearing aid comprises a speaker located in or substantially close to an ear piece of the electronic device, said speaker is electrically coupled to a printed circuit where the processor is located.

8. A non-transitory computer program product that causes an electronic device to adjust its frequency response to pitch and bass frequencies, the non-transitory computer program product having instructions to:

bias a pitch frequency response of the electronic device for frequencies above 1 kHz when a pitch frequency adjustment rocker switch of the electronic device is actuated; and

bias a bass frequency response of the electronic device for frequencies below 1 kHz when a bass frequency adjustment rocker switch of the electronic device is actuated.

9. The non-transitory computer program product of claim 8, wherein the electronic device is a hearing aid.

10. The non-transitory computer program product of claim 9, wherein the hearing aid is an over-the-ear-type hearing aid.

11. The non-transitory computer program product of claim 10, wherein the processor has a power control module for managing a battery life of the electronic device.

12. The non-transitory computer program product of claim 10, where the over-the-ear-type hearing aid comprises a speaker located on or substantially close to a printed circuit where the processor is located, said speaker is coupled via a mechanical audio coupler to an ear piece configured for use at an outer opening of an ear canal of a user of the electronic device.

13. The non-transitory computer program product of claim 10, where the over-the-ear-type hearing aid comprises a speaker located in or substantially close to an ear piece of the electronic device, said speaker is electrically coupled to a printed circuit where the processor is located.

14. In an electronic device configured with a high tone/volume control mechanism and a low tone/volume control mechanism, a method comprising:

detecting a change in high tone/volume control level at the high tone/volume control mechanism;

detecting a change in low tone/volume control level at the low tone/volume control mechanism; and

adjusting the treble and bass frequency amplification response of the electronic device in response to the detected changes in the high tone/volume control levels.

15. The method of claim 14, further comprising identifying a maximum or minimum tone/volume control level setting and generating a first audible sound.

16. The method of claim 15, further comprising generating a second audible sound in response to a change from one tone/volume control mechanism.

17. The method of claim 16, wherein the electronic device is an over-the-ear-type hearing aid.

18. The method of claim 17, wherein each of the low and high tone/volume control mechanisms is configured to be set to one of a predetermined number of tone/volume control levels, the method further comprising identifying tone/volume control level settings, matching the settings to a corresponding frequency response curve and amplifying a received input signal into a microphone in accordance with the frequency response curve.

19. The method of claim 14, wherein each of the low and high tone/volume control mechanisms is configured to be set to one of a predetermined number of tone/volume control levels, the method further comprising identifying tone/volume control level settings, matching the settings to a corresponding frequency response curve and amplifying a received input signal into a microphone in accordance with the frequency response curve.

20. The method of claim 14, wherein the low and high tone/volume control mechanisms are two separate rocker switches.

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