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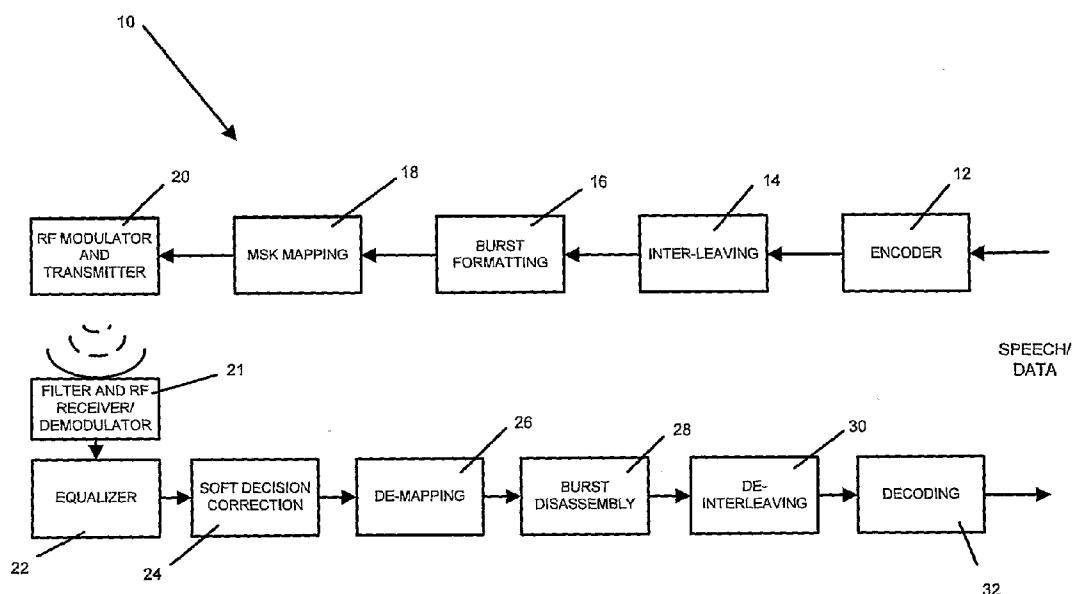
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(54) Title: CORRECTION CIRCUIT FOR IMPROVING PERFORMANCE IN A CHANNEL DECODER



(57) Abstract: A receiver for a mobile communication system includes a channel equalizer for receiving a burst and generating a soft decision output associated with the burst, a soft decision correction circuit follows the channel equalizer and a decoder receives and decodes a block of bursts. The soft decision correction circuit calculates a correction factor based on the soft decision output for the burst and applies the correction factor to the burst prior to the burst entering the decoder.

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CORRECTION CIRCUIT FOR IMPROVING PERFORMANCE IN A CHANNEL DECODER

[0001] The present specification relates to a mobile communication system, in particular preferably to a correction circuit for improving performance in a channel decoder of a receiver.

[0002] GSM (Global Systems for Mobile Communications) is the dominant world standard for 2G/2.5G wireless voice and data communications. EDGE (Enhanced Data rates for GSM Evolution) is a 3G technology that provides increased data transmission speeds of up to 384 kbit/s within the existing GSM spectrum. EDGE is an enhancement to GPRS (General Packet Radio Service) and is becoming more widely used because it effectively triples the gross data rate offered by GSM.

[0003] A major source of performance degradation in wireless telephony and data terminal products is ambient noise. Since ambient noise tends to vary significantly from environment to environment, reducing or eliminating the noise presents a challenge.

[0004] In a typical GSM communication system, speech and/or data is encoded at the source and transmitted over a network to a receiver. Upon receipt of the transmitted data, the receiver performs channel equalization and decoding steps to return the speech and/or data to a recognizable form for delivery to the user.

[0005] The channel decoder used in a GSM system is typically a forward error correction (FEC) decoder, which operates on the channel equalization output of four consecutive data transmission bursts. Conventional equalizers, such as the Viterbi equalizer, for example, do not take into account the signal-to-noise ratio (SNR) fluctuations between the data transmission bursts due to the fading nature of the channel. As such, the soft decision (SD) output of the equalizer does not reflect the input SNR. The stripping of SNR information from the SD compromises the performance of the channel decoder that follows and thus the quality of the speech and/or data that is delivered to the user. A true Maximum A posteriori Probability (MAP) equalizer takes into account the input SNR for each burst. However, this type of equalizer is much more complicated than conventional equalizers. It is therefore desirable to provide a receiver in which output from a more conventional equalizer is corrected to take into account the input SNR in order to improve the performance of the

channel decoder.

[0006] In one aspect there is preferably provided a soft decision correction circuit for a receiver having a channel equalizer for receiving a burst and generating a soft decision output associated with said burst and a decoder for receiving and decoding a block of bursts including said burst, the soft decision correction circuit being intermediate said channel equalizer and said decoder for calculating a correction factor based on said soft decision output and multiplying said burst by said correction factor.

BRIEF DESCRIPTION OF THE DRAWINGS

[0007] The specification will be better understood with reference to the following Figures in which like numerals denote like parts and in which:

[0008] Figure 1 is a block diagram of a GSM data communication system according to one embodiment;

[0009] Figure 2 is block diagram of a correction circuit for improving performance of a channel decoder in the GSM system of Figure 1, according to a first embodiment;

[0010] Figure 3 is a graph comparing soft decision output from a MAP (Maximum A posteriori Probability) equalizer and a Viterbi equalizer;

[0011] Figure 4 is a graph showing correction factor versus SNR;

[0012] Figure 5 is a graph comparing BLER with and without a correction factor for a CS-2 coding scheme;

[0013] Figure 6 is a graph comparing USF error rate with and without a correction factor for a CS-2 coding scheme;

[0014] Figure 7 is a graph comparing BLER with and without a correction factor for a MCS-6 coding scheme;

[0015] Figure 8 is a graph comparing USF error rate with and without a correction factor for a MCS-6 coding scheme;

[0016] Figure 9 is a graph comparing BLER for incremental retries; and

[0017] Figure 10 is an enlarged portion of the graph of Figure 9.

DESCRIPTION OF PREFERRED EMBODIMENTS

[0018] Referring to Figure 1, a block diagram is provided of a GSM mobile data communication system 10 according to one embodiment. The mobile data communication system 10 is operable according to GSM or EDGE communication standards.

[0019] As shown, speech and/or data passes through an encoder 12 (i.e. parity encoding and convolutional encoding), an interleaving block 14, a burst formatting block 16 and an MSK (Minimum Shift Keying) mapping block 18 prior to transmission of the encoded information to a receiver at RF modulator and transmitter 20. The encoded information is received and passes through a filter and RF receiver/demodulator 21. Channel equalization is then performed burst-by-burst using a Viterbi equalizer 22. The transmitted data bursts then pass through a correction circuit 24, a demapping block 26, a burst disassembling block 28, a de-interleaving block 30 and a decoding block 32 prior to being delivered to the user as speech and/or data.

[0020] Referring also to Figure 2, the correction circuit 24 estimates a correction factor within correction factor estimation block 25 for each transmission burst based on the soft decision output of the Viterbi equalizer 22. The correction factor is then applied to the burst prior to the burst passing through the subsequent demapping, burst disassembly, de-interleaving and decoding blocks 26, 28, 30, 32, respectively. The correction factor is a multiplication factor as indicated by the multiplier symbol in Figure 2.

[0021] A formula for estimating the correction factor is derived from the relationship between the output of a Maximum A-posteriori Probability (MAP) equalizer and a Viterbi equalizer. The log-likelihood ratio (LLR) output from a MAP equalizer provides optimal soft decision output because the input SNR information is embedded therein. Referring to Figure 3, soft decision output from a MAP equalizer is plotted against soft decision output from a Viterbi equalizer for a common set of input samples. The slopes of the lines in Figure 3 are plotted on curve 34 in Figure 4 versus the SNRs, which are normalized at SNR = 16 dB.

[0022] The normalization point is implementation-dependent and is selected based on the dynamic range of the SD and the word length used to represent the SD. The optimum normalization point is determined by performing simulations.

[0023] An empirical formula of: $c = \frac{m_{|sd|}^2}{\sigma_{|sd|}^2}$, which is the squared mean of the absolute value of

the soft decision divided by the variance of the absolute value of the soft decision, is plotted on curve 36. The curve 36 loosely fits the curve 34, as shown. A normalization constant of 1/8, which was optimized for a particular HW/SW platform with 4-bit SD representation, is further applied to the correction factor. This correction factor formula avoids estimation of input SNR for each burst, which results in a more accurate SNR estimation. Further, the

correction factor formula avoids the use of a look-up table that converts the SD of the Viterbi equalizer to the LLR of a MAP equalizer.

[0024] Referring back to Figure 1, the type of filter used in block 21 and the type of metrics used in the Viterbi equalizer differ depending on whether the receiver is operating in a GSM or an EDGE environment. A GSM receiver includes a matched filter and correlation metrics are used in the Viterbi equalizer because the noise exiting the matched filter is non-white. In an EDGE receiver, a noise-whitening filter is used and Euclidean distance metrics are used in the Viterbi equalizer because the noise is approximately white. The equivalence of these two metrics has been proven in "Unification of MLSE Receivers and Extension of Time-Varying Channels", Gregory E. Bottomly and Sandeep Chennakeshu, IEEE Trans. Comm. Vol. 46, no. 4, 1998. As such, the soft decision correction circuit can be applied to receivers using both GSM and EDGE technology.

[0025] Simulations were performed for a GSM equalizer having a CS-2 coding scheme. The block error rates (BLER) and Uplink State Flag (USF) error rates are shown in Figures 5 and 6, respectively. Similarly, simulations were performed for an EDGE equalizer having a MCS-6 coding scheme. The BLER and USF error rates are shown in Figures 7 and 8, respectively. A channel profile of TUX6.1-50km-1950MHz was used in each of the simulations. As shown, for GSM, the correction factor results in a gain of approximately 0.7 dB at $\text{BLER} = 10^{-2}$. For EDGE, the correction factor results in a gain of approximately 2.7 dB.

[0026] In some cases, the transmission of a data block fails. In these cases, Mobile Stations (MS) that support incremental redundancy reception could request at least one retransmission of the data block with a different puncturing scheme. The soft decisions of each subsequent incremental retry pass through the soft decision correction circuit 24 and a correction factor is applied. In some coding schemes, such as MCS-7, for example, soft decisions at some bit positions are overlapped between the retries. When this occurs, the soft decisions from each transmission are added following application of the correction factor and the combined sum is passed to the decoder.

[0027] Referring to Figures 9 and 10, the performance of coding scheme MCS-9 in the incremental retries is shown. For a channel profile of TUX6.1-50km-1950MHz, the correction factor results in a gain of approximately 2dB for one retry and a gain of approximately 1.5dB

for two retries at BLER = 10^{-1} .

[0028] A specific embodiment has been shown and described herein. However, modifications and variations may occur to those skilled in the art. All such modifications and variations are believed to be within the sphere and scope of the present embodiment.

Claims:

1. A soft decision correction circuit for a receiver having a channel equalizer for receiving a burst and generating a soft decision output associated with said burst and a
5 decoder for receiving and decoding a block of bursts including said burst, the soft decision correction circuit being intermediate said channel equalizer and said decoder for calculating a correction factor based on said soft decision output and multiplying said burst by said correction factor.
2. The circuit of claim 1, wherein said correction factor is $c = \frac{m_{|sd|}^2}{\sigma_{|sd|}^2}$, where $m_{|sd|}^2$ is the
10 square of the mean of the absolute value of the soft decision and $\sigma_{|sd|}^2$ is the variance of the absolute value of the soft decision.
3. The circuit of claim 1 or claim 2, further comprising demapping, de-interleaving and de-puncturing blocks provided between said channel equalizer and said decoder.
15
4. The circuit of any one of claims 1 to 3, wherein said decoder is a forward error correction decoder.
5. The circuit of any one of claims 1 to 4, wherein said receiver is used in a mobile
20 communication system comprising any of: Global Systems for Mobile Communications "GSM", Enhanced Data rates for GSM Evolution "EDGE" and General Packet Radio Service "GPRS".
6. The circuit of any one of claims 1 to 5, wherein said channel equalizer is a Viterbi
25 equalizer.
7. The circuit of any one of claims 1 to 6, wherein said receiver supports incremental redundancy reception for requesting at least one retransmission of a failed data block including said burst with a different puncturing scheme.

8. A soft decision correction method comprising:
 receiving a burst and generating a soft decision output associated with said burst;
 calculating a correction factor based on said soft decision output;
 multiplying said burst by said correction factor; and
 5 decoding a block of bursts including said burst having said correction factor applied.
9. The method of claim 8, wherein said correction factor is $c = \frac{m_{|sd|}^2}{\sigma_{|sd|}^2}$, where $m_{|sd|}^2$ is the
 square of the mean of the absolute value of the soft decision and $\sigma_{|sd|}^2$ is the variance of the
 absolute value of the soft decision.
- 10
10. The method of claim 8 or claim 9, further comprising demapping, de-interleaving and
 de-puncturing provided prior to decoding said block of bursts.
11. The method of claim 10, further comprising requesting at least one retransmission of
 15 a failed data block including said burst with a different puncturing scheme.
11. A mobile data communications system comprising the soft decision correction circuit
 of any one of claims 1 to 7 for a receiver thereof.

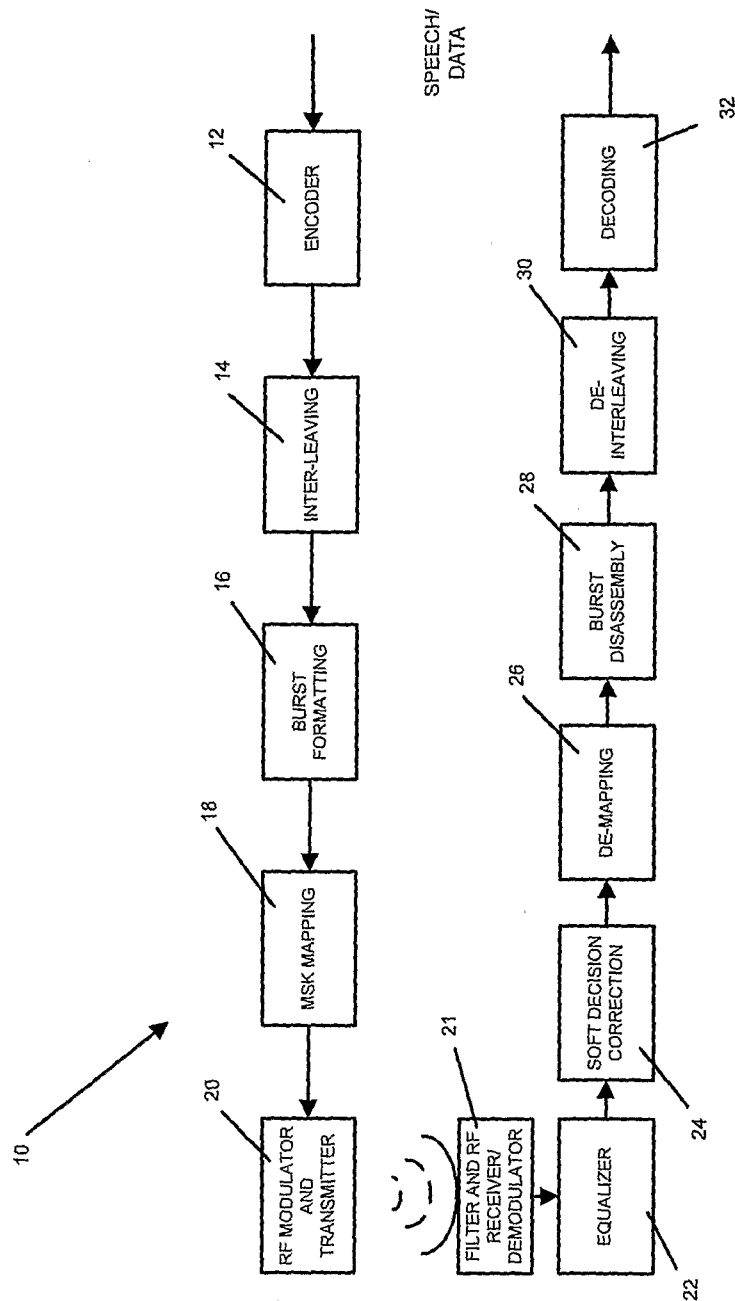


Fig. 1

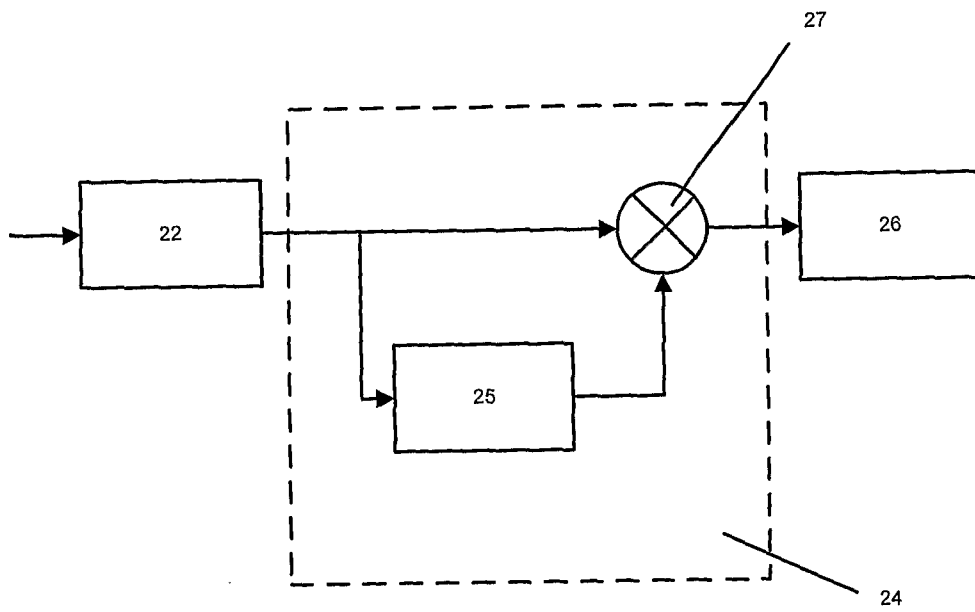


Fig. 2

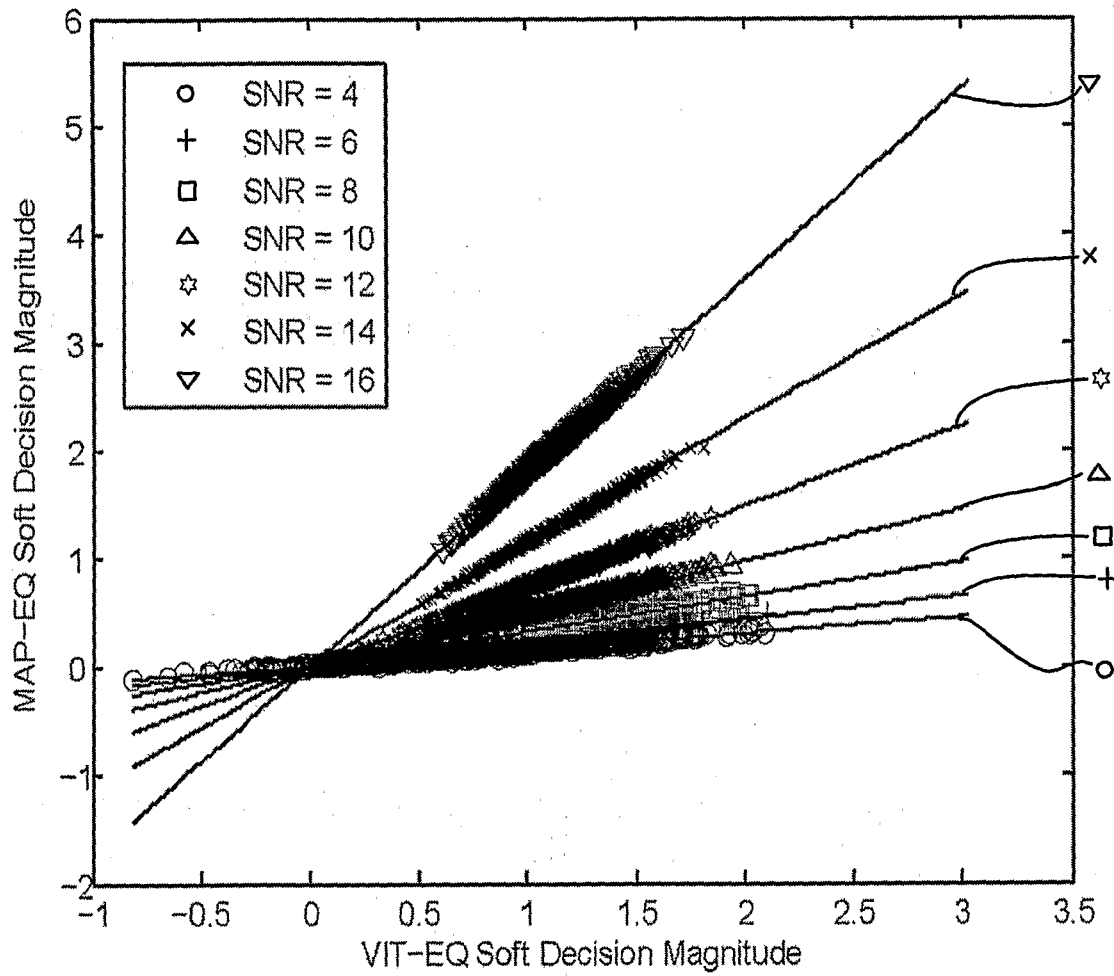


Fig. 3

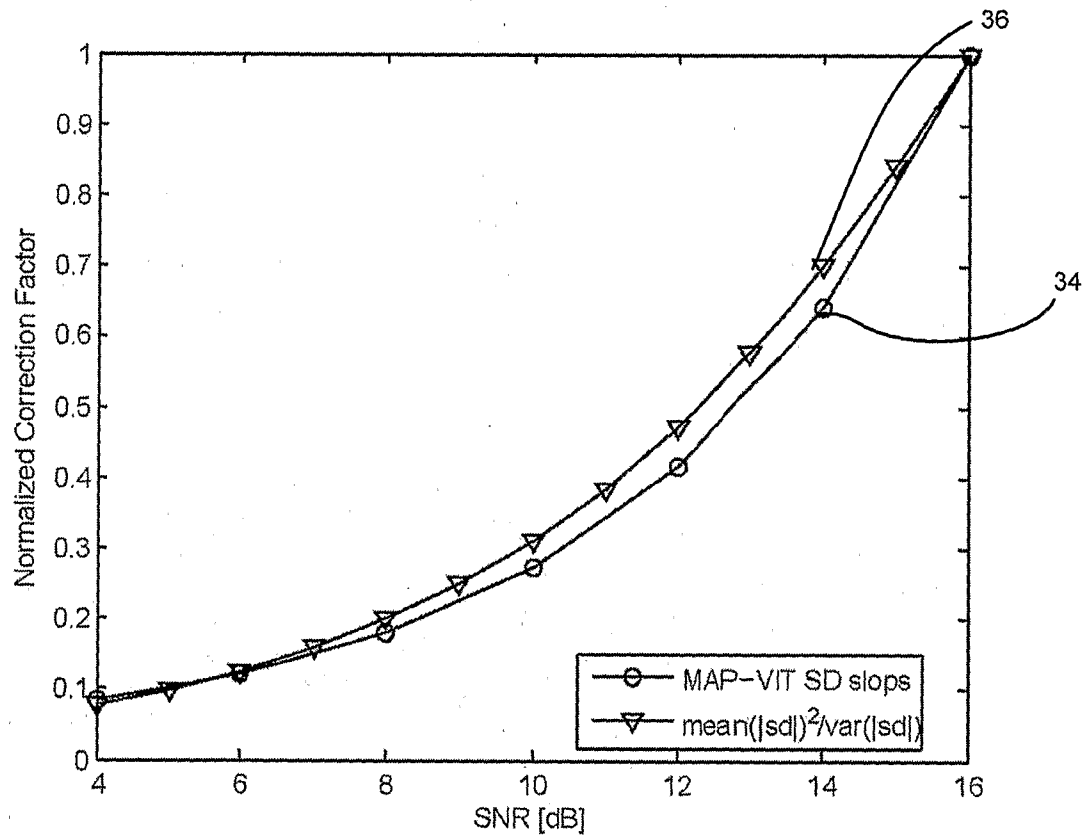


Fig. 4

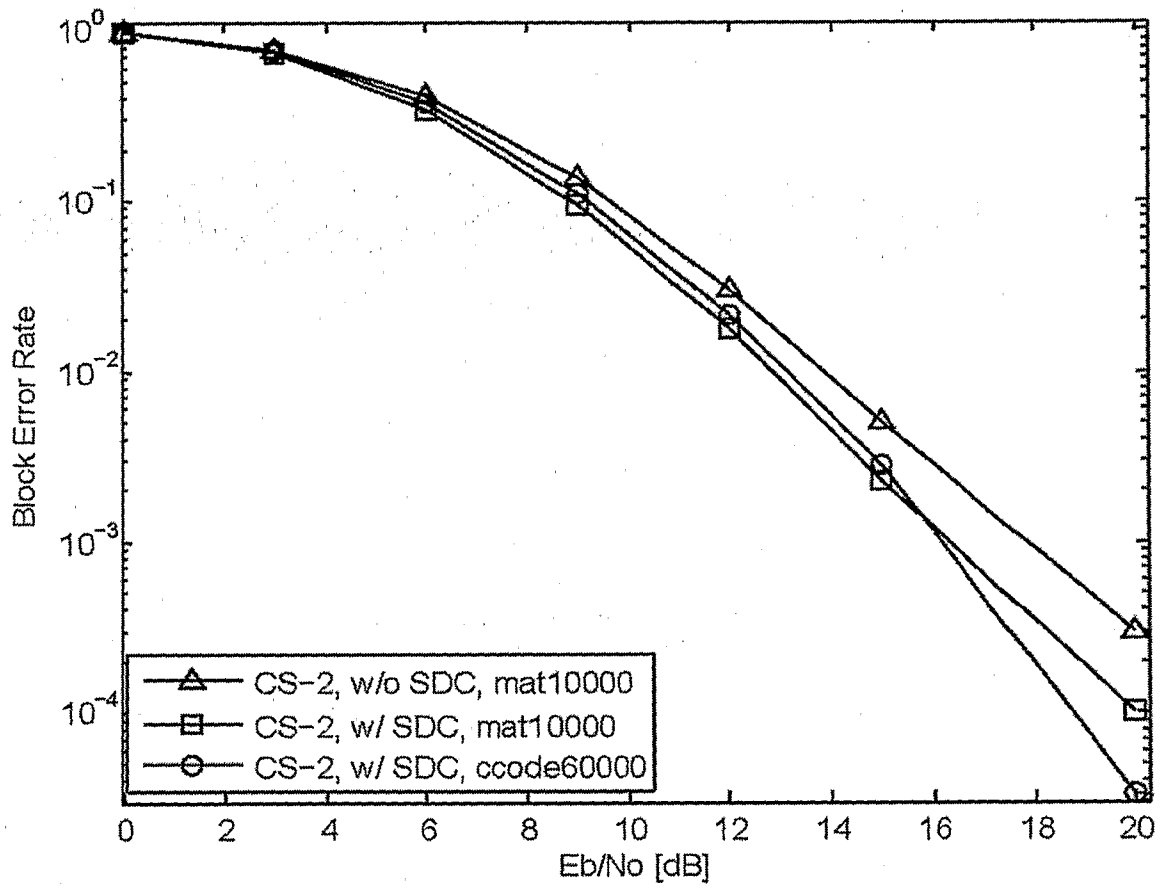


Fig. 5

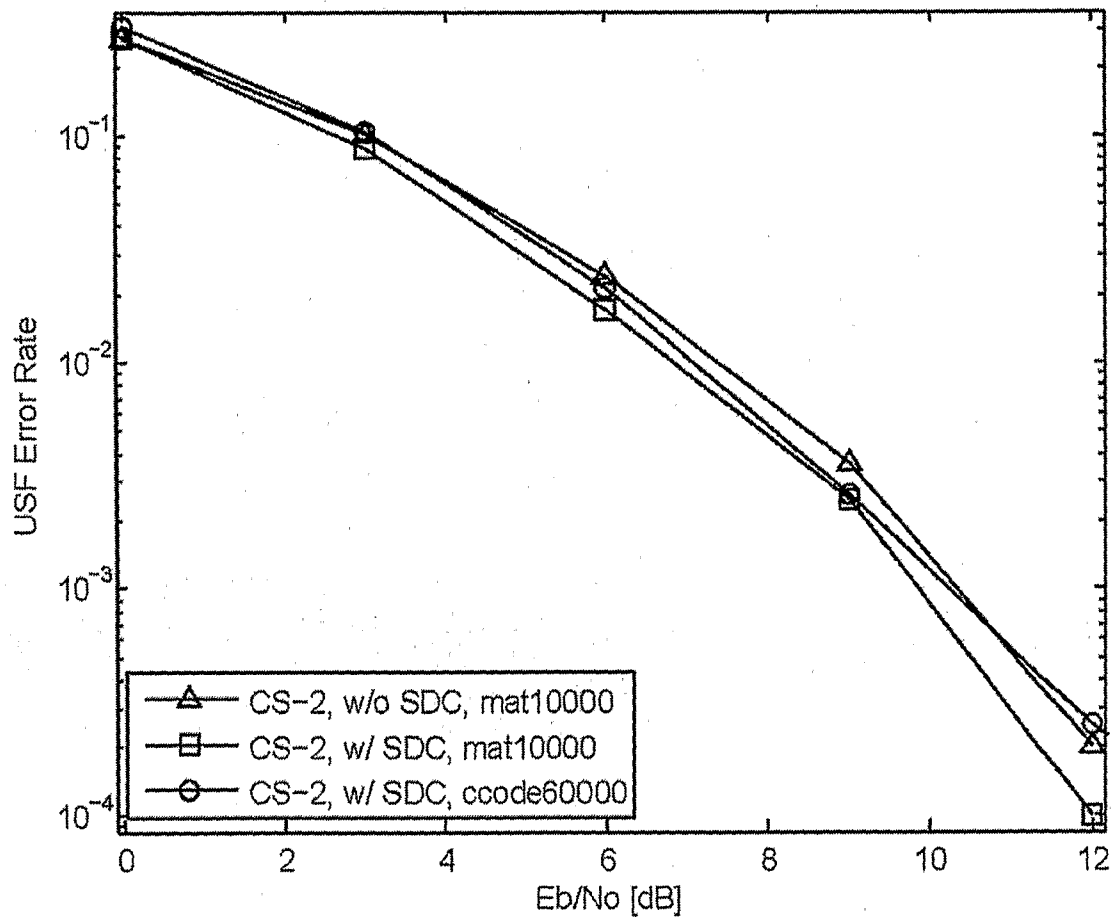


Fig. 6

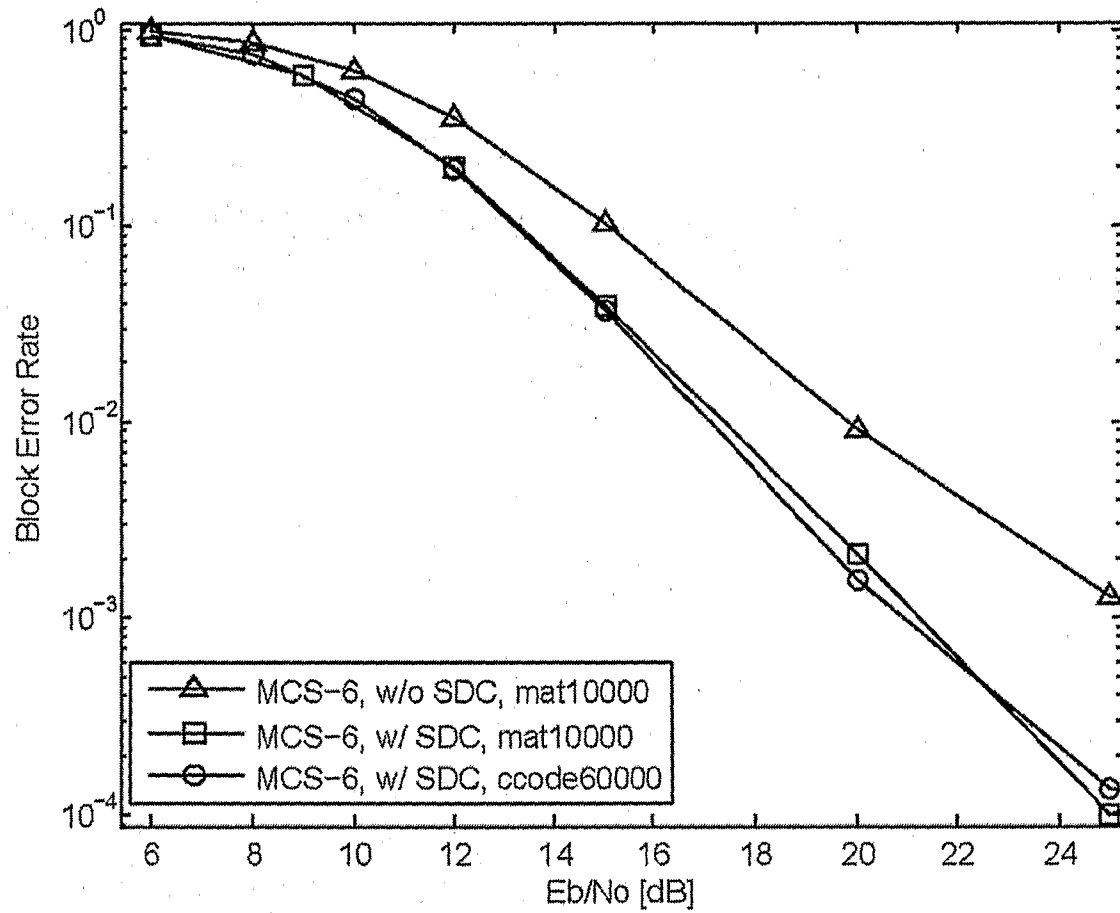


Fig. 7

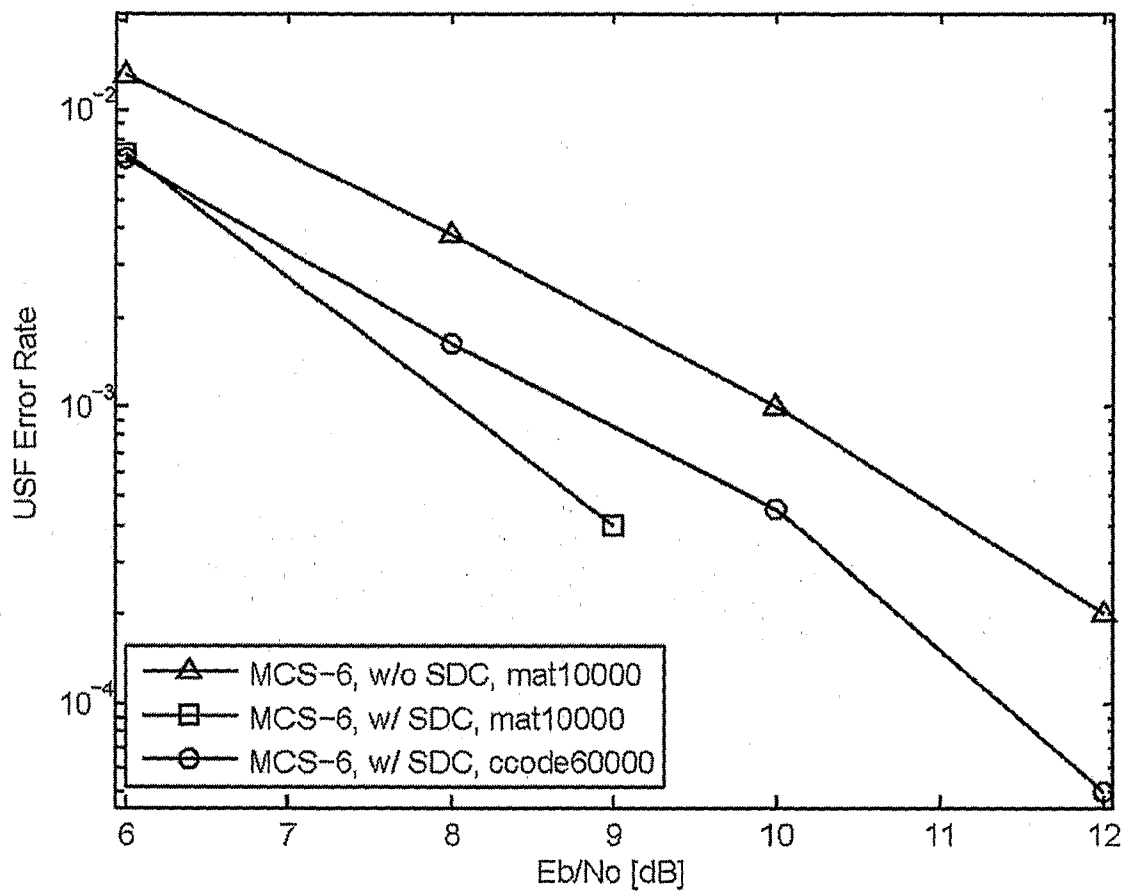


Fig. 8

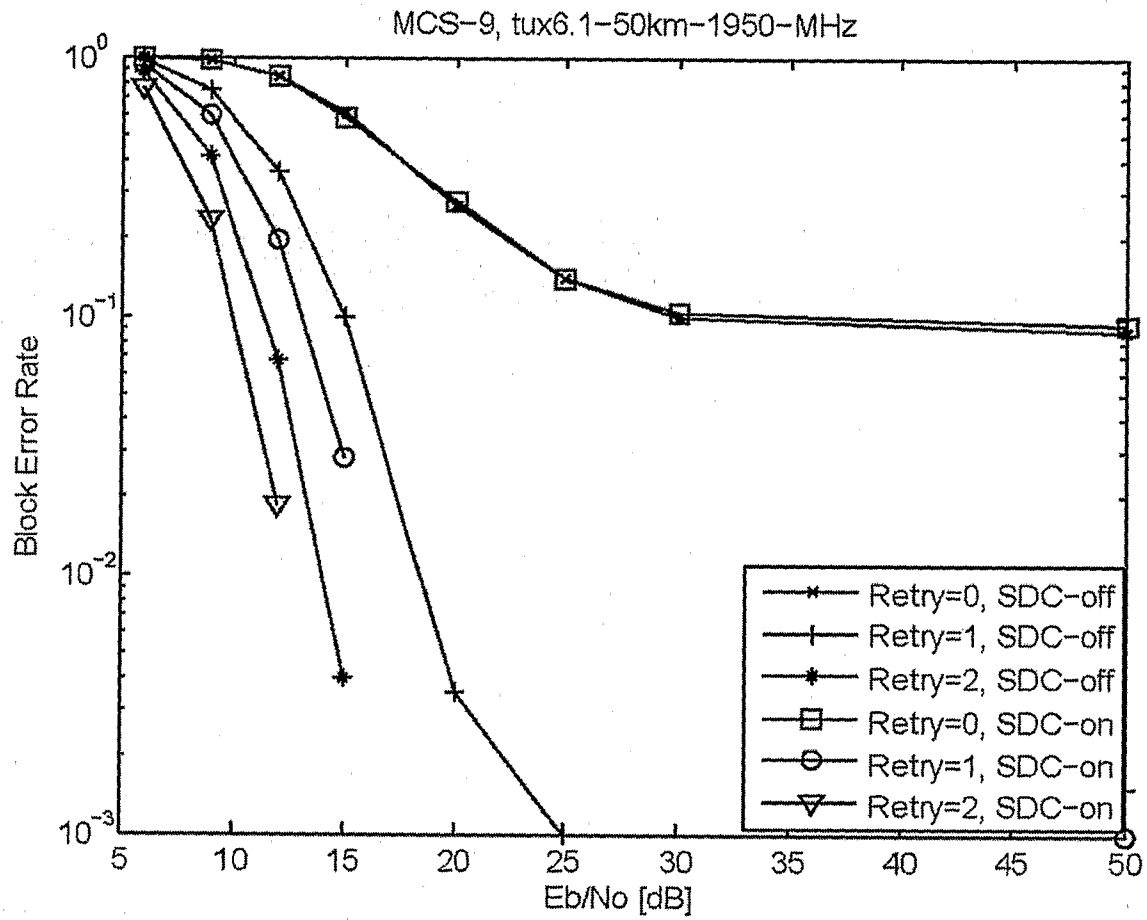


Fig. 9

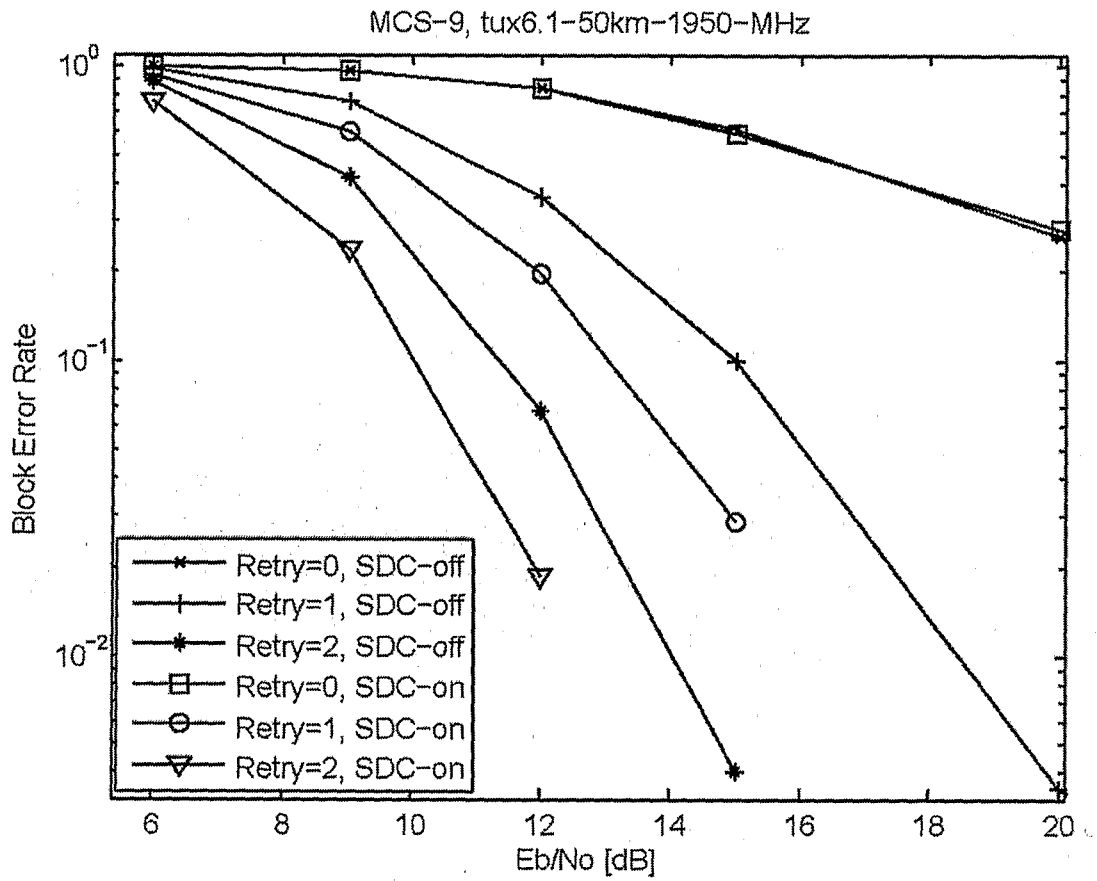


Fig. 10

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CA2006/002058

A. CLASSIFICATION OF SUBJECT MATTER

IPC: **H03M 13/45** (2006.01) , **H04L 1/00** (2006.01) , **H04L 1/22** (2006.01)

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC⁷ H03M* H04L* (keywords)

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic database(s) consulted during the international search (name of database(s) and, where practicable, search terms used)

Databases: Delphion, Canadian Patent Database, USPTO

Keywords: soft, decision, correct, circuit, decoder, equalizer

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6,269,124 (NAGAYASU et al.) 31 Jul. 2001 (31-07-2001) Abstract Col. 8, lines 38-43	1, 3, 8, 10 and 12
Y	Figs 3 and 4 Whole document	1-12
Y	US 5,844,946 (NAGAYASU) 1 Dec. 1998 (01-12-1998) Abstract	6

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents :	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"E" earlier application or patent but published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family
"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

6 March 2007 (06-03-2007)

Date of mailing of the international search report

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.
PCT/CA2006/002058

Patent Document Cited in Search Report	Publication Date	Patent Family Member(s)	Publication Date
US6269124	31-07-2001	AU709067B B2	19-08-1999
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