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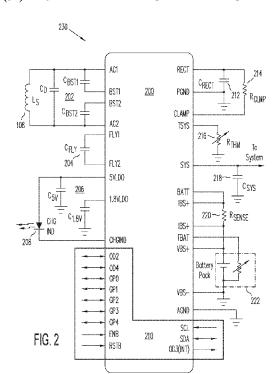
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#### (54) Title: BATTERY MANAGEMENT INTEGRATED CIRCUIT



(57) Abstract: In accordance with aspects of the present invention, a wireless power integrated circuit is presented. The wireless power integrated circuit includes a wireless power receiver circuit; a battery charger circuit; and a microprocessor coupled to control the wireless power receiver and the batter charger circuit.

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#### **Declarations under Rule 4.17:**

- as to the identity of the inventor (Rule 4.17(i))
  as to applicant's entitlement to apply for and as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))

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# BATTERY MANAGEMENT INTEGRATED CIRCUIT

### **Related Application**

[0001] This present application claims priority to U.S. Application No. 15/642,774 filed on July 6, 2017, which claims priority to U.S. Application Serial No. 62/359,630 filed on July 7, 2016, the contents of both of which are incorporated herein by reference in their entireties for all purposes.

#### **Technical Field**

[0002] Embodiments of the present invention are related to wireless power receivers and, specifically, to an efficient integrated circuit with a battery management circuit in a wireless power receiver.

#### **Discussion of Related Art**

**[0003]** Mobile devices, for example smart phones, tablets, wearable devices and other devices are increasingly using wireless power charging systems. There are multiple different standards for wireless transfer of power, which utilize a variety of different transmission frequencies. Frequencies used can vary widely, for example from less than 100 KHz to over 6.78MHz.

[0004] The more common standards for wireless transmission of power include the Alliance for Wireless Power (A4WP) standard and the Wireless Power Consortium standard, the Qi Standard. Under the A4WP standard, for example, up to 50 watts of power can be inductively transmitted to multiple charging devices in the vicinity of a coil at a power transmission frequency of around 6.78 MHz. Under the Wireless Power Consortium, the Qi specification, a resonant inductive coupling system is utilized to charge a single device at the resonance frequency of the device. In the Qi standard, the receiving device coil is placed in close proximity with the transmission coil while in the A4WP standard, the receiving device coil is placed near the transmitting coil, potentially along with other receiving coils that belong to other charging devices.

[0005] Therefore, there is a need to develop better wireless receiver technologies.

#### Summary

**[0006]** In accordance with aspects of the present invention, a wireless power integrated circuit is presented. The wireless power integrated circuit includes a wireless power receiver circuit; a battery charger circuit; and a microprocessor coupled to control the wireless power receiver and the battery charger circuit.

[0007] These and other embodiments are further discussed below with respect to the following figures.

# **Brief Description of the Drawings**

[0008] Figure 1 illustrates a wireless power transmission system.

**[0009]** Figure 2 illustrates a receiver of a wireless power transmission system according to some embodiments.

**[0010]** Figure 3 illustrates a comparison of a battery management IC according to some embodiments with a conventional IC.

**[0011]** Figure 4 illustrates comparison of total power loss with a battery management IC according to some embodiments.

[0012] Figure 5 illustrates another comparison of total power loss with a battery management IC according to some embodiments.

[0013] Figure 6 illustrates the IC shown in Figure 2 used in a device.

**[0014]** Figure 7 illustrates a block diagram of a battery management IC according to some embodiments.

[0015] Figure 8 illustrates another block diagram of a battery management IC according to some embodiments.

#### **Detailed Description**

[0016] In the following description, specific details are set forth describing some embodiments of the present invention. It will be apparent, however, to one skilled in the art that some embodiments may be practiced without some or all of these specific details. The specific embodiments disclosed herein are meant to be illustrative but not limiting. One skilled in the art

may realize other elements that, although not specifically described here, are within the scope and the spirit of this disclosure.

[0017] This description and the accompanying drawings that illustrate inventive aspects and embodiments should not be taken as limiting--the claims define the protected invention. Various changes may be made without departing from the spirit and scope of this description and the claims. In some instances, well-known structures and techniques have not been shown or described in detail in order not to obscure the invention.

[0018] Elements and their associated aspects that are described in detail with reference to one embodiment may, whenever practical, be included in other embodiments in which they are not specifically shown or described. For example, if an element is described in detail with reference to one embodiment and is not described with reference to a second embodiment, the element may nevertheless be claimed as included in the second embodiment.

[0019] Figure 1 illustrates a system 100 for wireless transfer of power. As illustrated in Figure 1, a wireless power transmitter 102 drives a coil 106 to produce a magnetic field. A power supply 104 provides power to wireless power transmitter 102. Power supply 104 can be, for example, a battery based supply or may be powered by alternating current for example 120V at 60Hz. Wireless power transmitter 102 drives coil 106 at, typically, a range of frequencies, typically according to one of the wireless power standards.

[0020] There are multiple standards for wireless transmission of power, including the Alliance for Wireless Power (A4WP) standard and the Wireless Power Consortium standard, the Qi Standard. Under the A4WP standard, for example, up to 50 watts of power can be inductively transmitted to multiple charging devices in the vicinity of coil 106 at a power transmission frequency of around 6.78 MHz. Under the Wireless Power Consortium, the Qi specification, a resonant inductive coupling system is utilized to charge a single device at the resonance frequency of the device. In the Qi standard, coils 108 is placed in close proximity with coil 106 while in the A4WP standard, coil 108 is placed near coil 106 along with other coils that belong to other charging devices. Figure 1 depicts a generalized wireless power system 100 that operates under any of these standards.

[0021] As is further illustrated in Figure 1, the magnetic field produced by coil 106 induces a current in coil 108, which results in power being received in a receiver 110. Receiver 110

receives the power from coil 108 and provides power to a load 112, a battery charger 114, and/or other components of a mobile device. Receiver 110 typically includes rectification to convert the received AC power to DC power for load 112. As is further shown in Figure 1, a battery charger 114 and fuel gauge 116 may also be coupled to charge a battery 118 from power received by wireless power receiver 110. However, in conventional receiver systems these components are separate components, each requiring their own supporting circuitry and controls. These conventional configurations, therefore, use a large amount of space on a printed circuit board (PCB) and also may be inefficient.

[0022] Figure 2 illustrates an example of receiver 230 according to some embodiments of the present invention. As is illustrated in Figure 2, receiver 230 includes a battery management integrated circuit 200 and external circuitry to support the functions on IC 200. In particular, IC 200 incorporates a wireless receiver, a battery charger, a fuel gauge, and other circuitry onto a single chip integrated circuit. IC 200 can then be controlled and operated by a single microcontroller system in such a way that is highly efficient. Further, external circuitry includes the resistive, inductive, and capacitive components that cannot be easily formed on IC 200 but are used on supporting the remaining circuitry that can be formed on IC 200. Consequently, IC 200 and its supporting components occupy a much smaller area of PCB while being more energy efficient than conventional circuitry.

[0023] In accordance with some embodiments, receiver 230 may be Qi-based and, with IC 200, may include minimal external components outside of IC 200. In some embodiments, receiver 230 may operate at frequencies up to about 1 MHz of operation frequency and produce a rectified voltage at output RECT, VRECT, of from 3.2V to 6.0 V in 10mV steps.

**[0024]** As is illustrated in Figure 2, the wireless power receiver, battery charger, and fuel gauge are incorporated in battery management integrated circuit 200. Figure 2 illustrates wireless power receiver components 202, which includes wireless power receive coil 108 in parallel with a capacitor  $C_D$  coupled between pins AC1 and AC2. A capacitor  $C_{BST1}$  is coupled between the BST1 pin and the AC1 pin. A capacitor  $C_{BST2}$  is coupled between the BST2 pin and the AC2 pin. As is further indicated, a fly capacitor  $C_{FLY}$  is coupled between pins FLY1 and FLY2 and may be internally coupled to a charge pump and low drop-out (LDO) regulator. Other LDO regulators, for example a 1.8V LDO and a 5V LDO, on integrated circuit 200 can be

coupled to LDO capacitors  $C_{1.8V}$  and  $C_{5V}$  at 1.8LDO pin and 5LDO pin, respectively. An LED 208 may also be coupled to the 5V LDO pin.

[0025] Integrated circuit 200 may further includes a microprocessor that controls and coordinates the functioning of IC 200. Consequently, several interfacing pins 210 may be utilized to communicate with and configure integrated circuit 200. Additionally, interface pins 210 may include enable ENB and reset RSTB pins. Interfaces may include any interface, including an I2C interface and EEPROM interfaces. Further, some of interface pins 210 may be dedicated for debugging purposes.

[0026] Integrated circuit 200 may further include onboard rectification coupled to receive wireless power from receive coil 108. Consequently, a rectifier capacitor 212 may be coupled between a rectifier output pin RECT and ground at pin PGND. Additionally, a clamping resistor 214 may be coupled between the RECT pin and a CLAMP pin. In some embodiments, there may be a thermal resistor coupled to a TSYS pin. A capacitor 218 may be coupled to a SYS pin, which provides output voltage to a further system.

**[0027]** A battery pack 222 may be coupled to receive a charging current from integrated circuit 200. A BAT pin provides current to battery pack 222. Current can be monitored by providing a sense resistor 220 between the BAT pin and battery pack 222 and monitoring the voltage across sense resistor 220 between the IBS pins. Battery pack 222 may include a thermal resistor that can be monitor at a TBAT pin of integrated circuit 200. The voltage across battery pack 222 can be monitored at VBS+ and VBS- pins.

[0028] Various processing and control for receiving power from receiver coil 108, output power at the SYS pin, and providing current for charging battery pack 222 at the BATT pin is supplied on integrated circuit 200. Integrated circuit 200 may include, for example, a linear charger. Example characteristics of such a linear charger may include Pre-Q operation (+/- 10% current accuracy); pulsing constant current (CC) operation supporting fast charging (in some embodiments with a maximum CC current of 800 mA); LDO regulation for continuous voltage (CV) operation; battery and ambient temperature monitoring; thermal regulation, for example according to Japan Electronics and Information Technology industries Association (JEITA) regulation; high battery voltage sense accuracy (e.g., +/-0.5%); high full scale current sense accuracy (e.g., +/-0.5%); and charging status indication. IC 200 may further include a fuel gauge

with column counting FG, sleep mode current (e.g., 10uA), battery empty warning, and state-of-charge (SOC) percentage display. In some embodiments, IC 200 may be packaged in a 5x7 bump array 0.4 mm WLCSP in a 3.0mm X 2.2mm package size. These specifications are exemplary only of some embodiments and should not be considered limiting.

[0029] Embodiments of IC 200 may provide for fast charging when the battery is dead (i.e., discharged) or near empty. IC 200 may include, for example, an ARM Cortex-M0 processor and multi-time programmable (MPT) non-volatile memory for flexible programmability and self-learning FG (Asynchronous Buffered Computation Design and Engineering Framework Generator) programming. IC 200 may include an application (AP) reset function, I2C communication, and shelf mode for storage or shipping. Use of IC 200 may reduce the component inventory and include the lowest bill of materials (BOM) and smallest printed circuit board (PCB) area for a complete battery management system (WP power, charger, and fuel gauge). IC 200 according to some embodiments provides for a self-sufficient and complete battery management IC for variable devices.

**[0030]** The following table provides some comparison of an embodiment of IC 200 with conventional wireless power receiver and battery charger systems. As is illustrated, IC 200 can be more power efficient, requires less printed circuit board (PCB) space, fewer external components, and can provide power in the presence of a wireless power transmitter even if the battery is dead.

| Problem Solution                    | Competitor A | Competitor B | Embodiment | User Experience       |
|-------------------------------------|--------------|--------------|------------|-----------------------|
|                                     |              |              | of IC 200  |                       |
| Power dissipation in CC mode (mw)   | 450          | 350          | 200        | Lower device          |
| (Vout=4.5V, Vbat-3.5V, Ibat=200 mA) |              |              |            | temperature           |
| Power dissipation in fast charging  | 950          | 770          | 300        | New feature of        |
| mode (mW)                           |              |              |            | charging 5 minutes    |
| (ICC=400 mA)                        |              |              |            | for 2-4 hour activity |
| Total PCB space                     | ~48          | ~46          | ~28        | ~40% smaller PCB      |
| (mm^2)                              |              |              |            | area                  |
| Number of External Components       | 30           | 24           | 20         | Lower system          |
|                                     |              |              |            | solution cost         |
| Power path                          | No           | No           | Yes        | Provides power with   |
|                                     |              |              |            | wireless transmitter  |
|                                     |              |              |            | when battery is dead. |

[0031] Figure 3 illustrates a comparison between printed circuit board area used by a popular Integrated Device Technology P9027LP-R wireless power receiver for charging a battery and an

example embodiment of IC 200. As illustrated in the layout of PCB 302 for circuit 308, which is the IDT P9027LP-R, includes components 314, which may be resistors and capacitors and includes one negative thermal coefficient (NTC) thermal resistor, a battery charger 310, a fuel gauge 312, and further components 316 which may include resistors, capacitors, and a LED. In total, the area of PCB 302 is 46 mm<sup>2</sup> with a total of 24 components. As is further illustrated, PCB 304 with an implementation of IC 200 also includes external resistors capacitors 306, along with thermal resistor 216 and LED 208, and includes a total of 21 components. The area occupied on PCB 304 can be 28 mm<sup>2</sup>. This represents a total reduction in PCB area used by about 40%.

**[0032]** Figure 4 illustrates total power loss comparison for constant current charging for the systems highlighted in the above table. In Figure 4, the 200mA charging current line is highlighted illustrating the power loss for each of the competitor products. Figure 5 illustrates total power loss comparison for fast charging characteristics for the systems highlighted in the above table. Again, the 400 mA line is illustrated for comparison.

**[0033]** As is illustrated in Figure 3, the PCT area used by IC 200 and its external circuitry is much reduced from that of its competitor products. Furthermore, as is illustrated in Figures 4 and 5, the power loss performance of IC 200 is greatly improved from that of its competitors as well. This improvement in performance and in area utilization results from the

[0034] Figure 6 illustrates IC 200 used in a device with a power management integrated circuit (PMIC) 602 and an application processor 604. The external components include the components illustrated and discussed above in Figure 2. As is illustrated, the SYS pin from IC 200 provides an input voltage to PMIC 602. PMIC 602 provides voltages to application processor 604. Applications process can communicate with IC 200 through an I2C interface using the SCL/SDA pins on IC 200. Other pins can be used to send or receive data between application processor 604 and IC 200. Furthermore, a switch 606 can provide a reset signal to the reset pin RSTB of IC 200. Additionally, IC 200 may receive an enable signal from a GPIO port of application processor 604 on an ENB pin of IC 200. Application processor 604 can be adapted for any application, including laptops, phones, or other digital devices.

[0035] As is illustrated in Figure 6, there is no involvement from the application processor 604 to manage battery charging, charging parameter setting, or performing fuel gauge functions,

thus extending battery life. All of these functions are performed on IC 200 and can be configured by application processor 604 and monitored by application processor 604. Further, with a power path feature the application processor can be powered directly from the wirelessly transmitted power received on receive coil 108, even if the battery has died. As discussed above, power is provided on the SYS pin of IC 200 and can be used by PMIC 602 to power application processor 604 without involvement from battery pack 222.

[0036] Figure 7 illustrates a block diagram of an embodiment of IC 200. As illustrated in Figure 7, IC 200 includes a microcontroller 726. Microcontroller 726 can be any microprocessor or controller such as the ARM processor discussed above. In some embodiments microcontroller 726 is coupled to memory 730, which can include a combination of volatile and non-volatile memory. Memory 730 stores programming instructions that can be executed on microcontroller 726, data, and operating parameters for operation of IC 200. In particular, microcontroller 726 is coupled to operate wireless power receiver 702, battery charging circuit 740, and monitor and fuel gauge 742.

[0037] Microcontroller 726 is coupled to interface 730 to receive digital data on interface pins 210. Further interface pins 210 may be coupled to enable and reset 714. Further interface pins 210 may be coupled to a GPIO interface 732. Consequently, IC 200 can be configured through interface 730, new programming may be loaded and operation parameters may be provided through interface 730.

[0038] Microcontroller 726 may further be coupled to timers 732 and 734 that can provide timing for various operations of IC 200, including charging and wireless power receipt. Microcontroller is further coupled to receive data through interface 724, which can be used to provide digital instructions to various components of IC 200 and receive digitized data. As is illustrated in Figure 7, an ADC 728 is coupled to gauge 742 that receives various input operating parameters, for example including the temperature T<sub>DIE</sub> measured from resistor 216 coupled to the THM pin through GPIO interface 732; battery temperature T<sub>BATT</sub> measured from battery pack 222 at the TBAT pin; the current I<sub>SENSE</sub> measured at the output of switching transistor 718; the current I<sub>BAT</sub> measured across the IBS+ and IBS- pins of IC 200; the rectifier voltage VRECT; the battery voltage V<sub>BATT</sub>; and other parameters received through GPIO interface 732. A multiplexer 736 receives each of the analog signals and provides a selected one of them to ADC

728, which provides digitized data to interface 724 for use in control electronics and microcontroller 726. Multiplexer 736, for example, can receive a selection signal from microcontroller 726 to measure one of the plurality of analog signals input to multiplexer 736.

[0039] As illustrated in Figure 7, IC 200 includes a wireless power receiver 702. Receiver circuit 702 in this includes a rectifier 746 formed by FETs Q1, Q2, Q3, and Q4 that are driven by synchronous rectifier driving circuit 704 in order to receive power from receiver coil 108. Rectifier circuit 746 provides the rectifier voltage VRECT on the RECT pin of IC 200. A frequency detecting circuit 706 detects the frequency of wireless power transfer to determine how transistors Q1, Q2, Q3, and Q4 are drive by synchronous rectifier driving circuit 704. In some embodiments, frequency detection 706 provides data to microcontroller 726, which uses that data to provide operating instructions to synchronous rectifier driving circuit 704 appropriate for receiving synchronous data.

**[0040]** A charge pump and low drop-out regulator (LDO) 708 provides a voltage Vdd. The voltage Vdd can be provided to LDOs 710 and 712 to provide power, for example a 5V regulated output and a 1.8 V regulated output, for other aspects of IC 200.

**[0041]** IC 200 further includes a charging circuit 740. Charging circuit 740 includes switching transistors 716 and 718 that are driving by a charging driving circuit 720. As is illustrated, and discussed above, charging circuit provides charging current to an external battery pack 222 through a battery pin BATT.

[0042] In the embodiment illustrated in Figure 7, the external system can be powered through the BATT pin. Figure 8 illustrates another embodiment that illustrates a power path that is better able to supply received wireless power to an external system if battery pack 222 is dead. As illustrated in Figure 8, IC 200 can include a power control circuit 802 that directs power to the BATT pin, in order to charge battery pack 222. As illustrated, battery to the system can be supplied through a SYS pin. A power pack control signal, which is provided by microcontroller 726, supplied to circuit 802 controls whether power is supplied to battery pack 222. As such, if battery pack 222 is uncharged and a system is coupled to the SYS pin, then circuit 802 may be inactive to provide power to the system.

[0043] As is illustrated in Figures 7 and 8, microprocessor 726 is coupled in IC 200 to control wireless power receiver 702 and battery charging circuit 740. Microprocessor 726, and its

surrounding circuit, is also coupled to monitor analog parameters through gauge circuit 742. Such parameters, as described above, are used by microprocessor 726 to control functions of wireless receiver 702 and battery charging circuit 740. These parameters are also used to provide fuel gauge functions (e.g. monitoring the state of charge of external battery pack 222). Fuel gauge results can be communicated to external devices such as, for example, application processor 604, through interface 730. Instructions to microprocessor 726 to monitor and control each aspect of IC 200 are stored in memory 730.

**[0044]** The above detailed description is provided to illustrate specific embodiments of the present invention and is not intended to be limiting. Numerous variations and modifications within the scope of the present invention are possible. The present invention is set forth in the following claims.

#### **Claims**

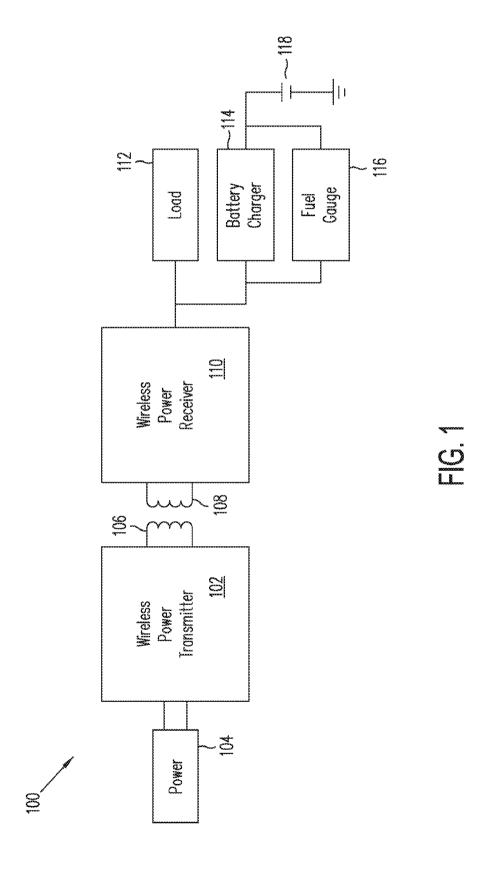
#### What is claimed is:

- 1. A wireless power integrated circuit, comprising:
  - a wireless power receiver circuit;
  - a battery charger circuit; and
- a microprocessor coupled to control the wireless power receiver and the battery charger circuit.
- 2. The integrated circuit of claim 1, wherein the wireless power receiver circuit includes a rectifier that receives power from an external receiver coil and provides a rectifier voltage.
- 3. The integrated circuit of claim 2, wherein transistors in the receiver are controlled by a synchronous rectifier driving circuit operating to efficiently receive wireless power.
- 4. The integrated circuit of claim 1, wherein the battery charger circuit includes switching transistors controlled by a charging driving circuit to provide charging to a battery pack coupled to the integrated circuit.
- 5. The integrated circuit of claim 1, further including a power path control circuit to provide power to a system output chip when a battery pack coupled to the charging circuit through the power path control circuit is discharged.
- 6. The integrated circuit of claim 1, further including a gauge circuit configured to receive and digitize analog data.
- 7. The integrated circuit of claim 6, wherein the analog data includes one or more of a battery current, a battery pack temperature, a temperature, a battery voltage, a rectifier voltage, and an output current.

- 8. The integrated circuit of claim 1, further including a digital interface coupled to the microprocessor.
- 9. The integrated circuit of claim 8, wherein the digital interface is configured to receive parameters by which the microprocessor controls the integrated circuit.
- 10. An integrated circuit, comprising:
  - a microprocessor;
  - a wireless power receiver coupled to the microprocessor; and
  - a battery charging circuit coupled to the microprocessor,
  - wherein the microprocessor is configured to execute instructions to
    - control the receipt of wireless power;
    - control charging of an external battery pack.
- 11. The integrated circuit of claim 10, further including gauge circuitry coupled to the microprocessor and wherein the microprocessor is further configured to execute instructions to provide battery status parameters.
- 12. The integrated circuit of claim 10, wherein the gauge circuitry receives and digitized analog signals.
- 13. The integrated circuit of claim 12, wherein the gauge circuit includes a multiplexer that receives a plurality of analog signals and an analog-to-digital converted coupled to the multiplexer to digitize one of the plurality of analog signals in response to selection signals.
- 14. The integrated circuit of claim 13, wherein the microprocessor adjusts control of the wireless power receiver and/or the charging circuit according to one or more of the plurality of analog signals.

15. The integrated circuit of claim 10, further including a power path circuit and wherein the microprocessor further executes instructions to direct power to a system coupled to the integrated circuit even if a battery pack coupled to the integrated circuit is discharged.

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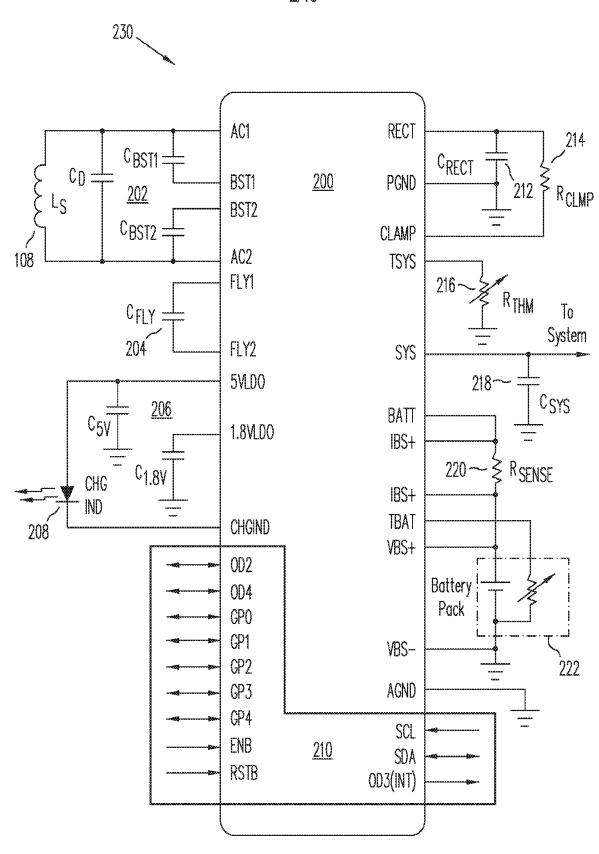
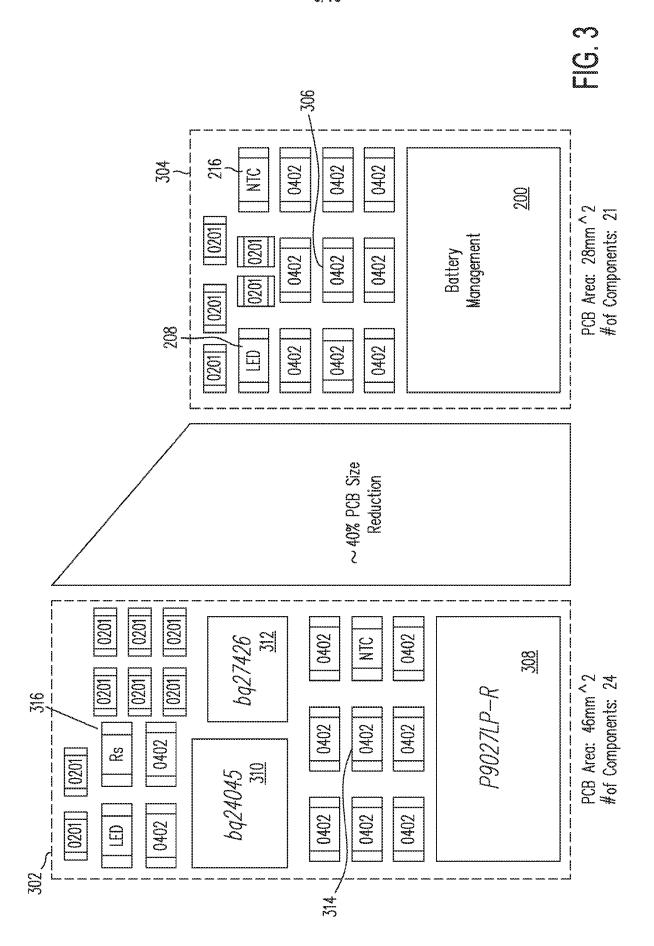
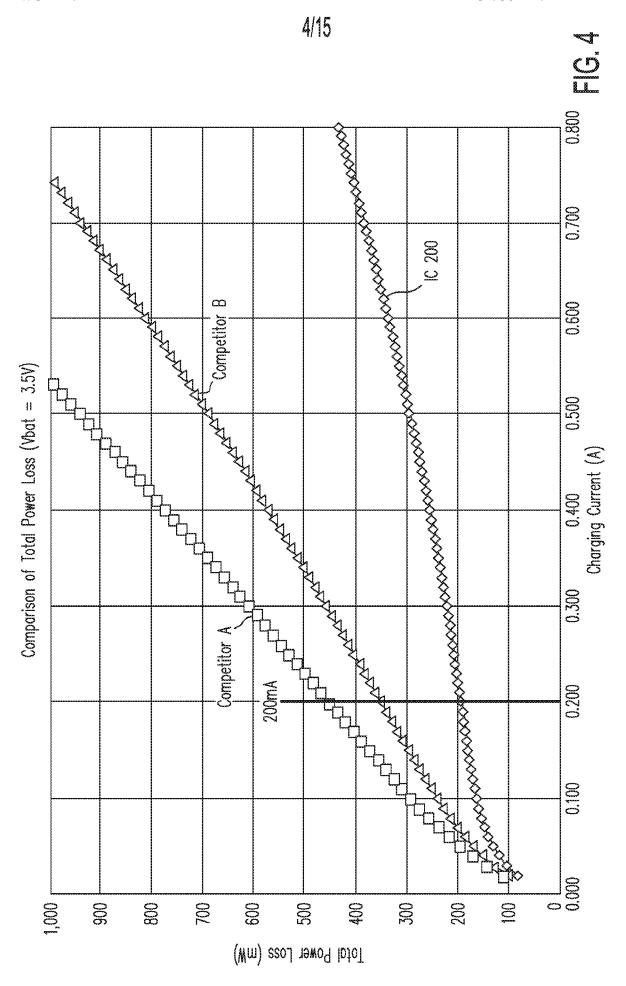
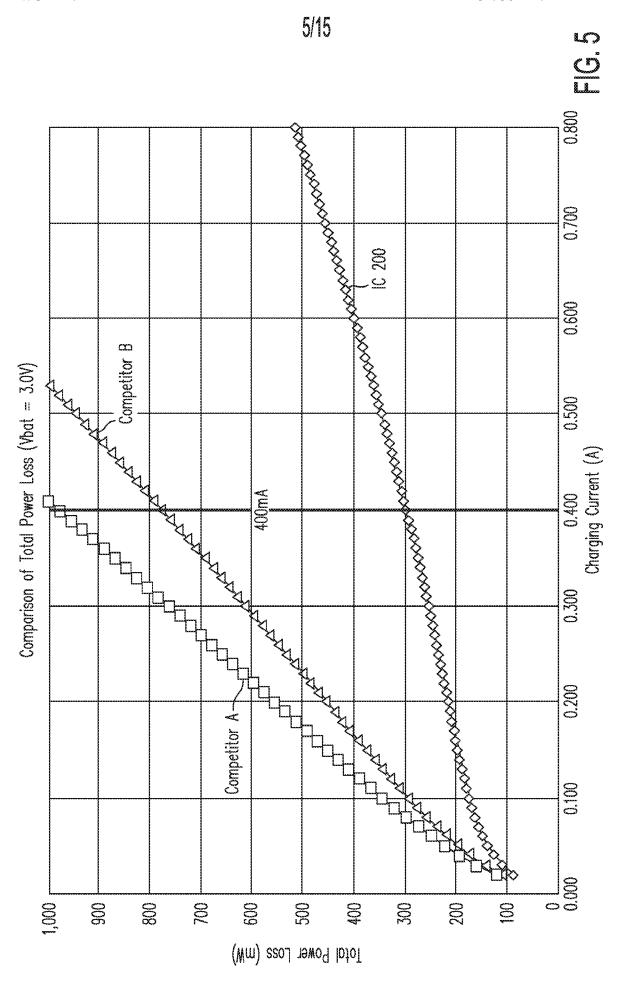


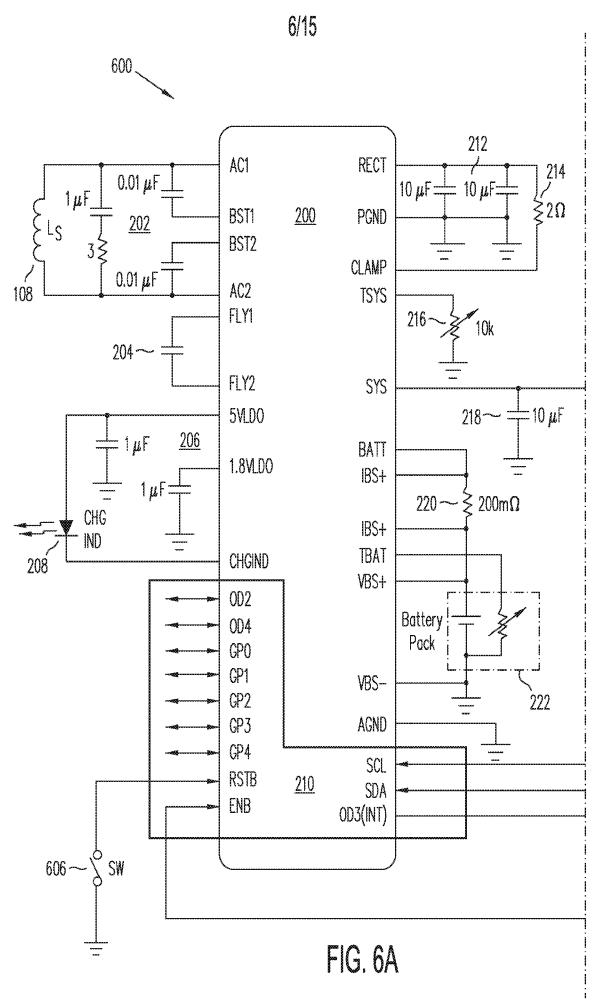
FIG. 2



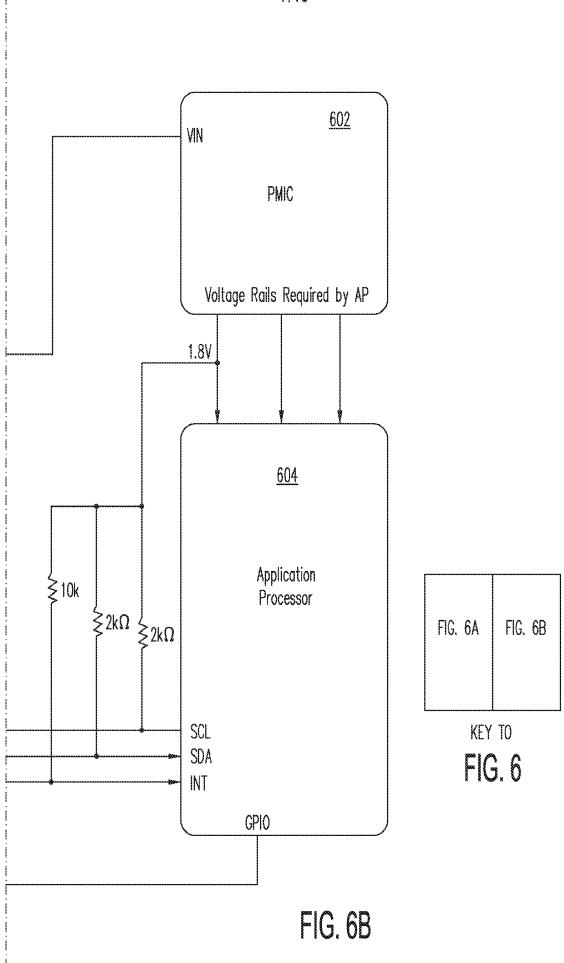
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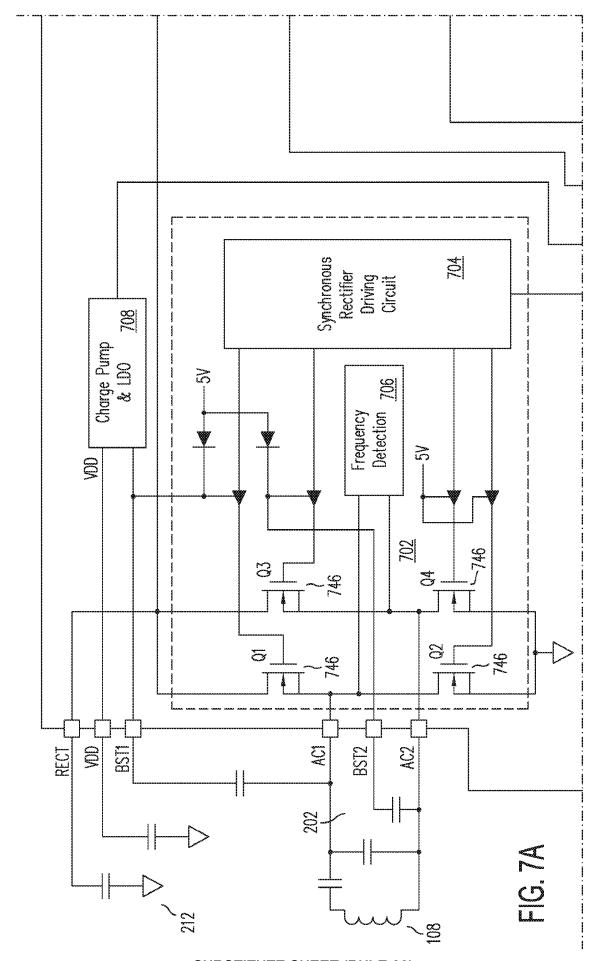




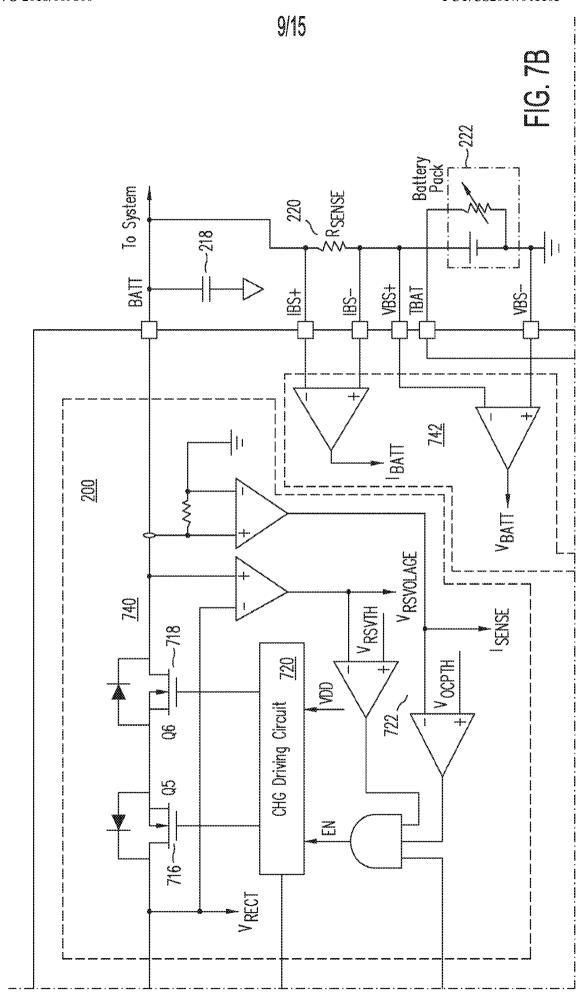
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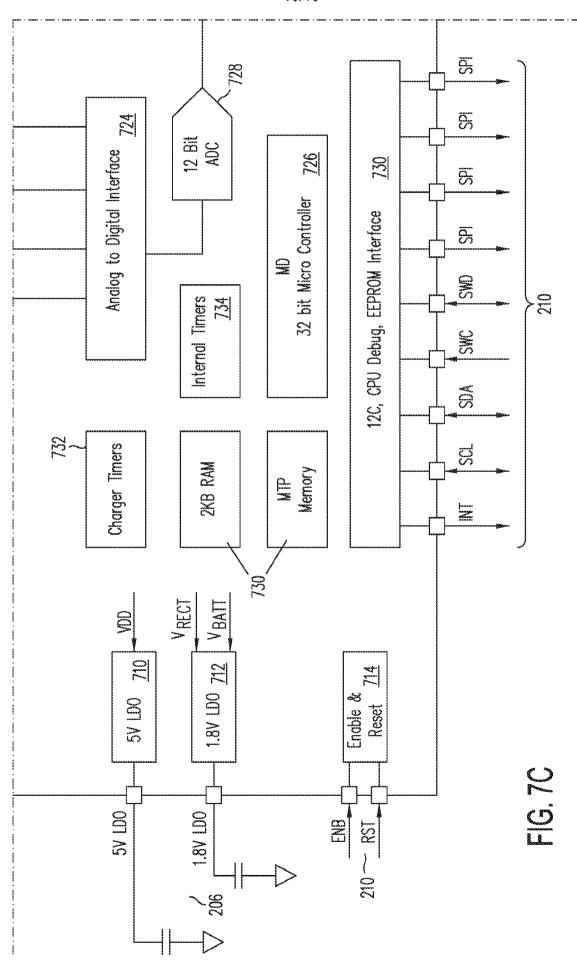
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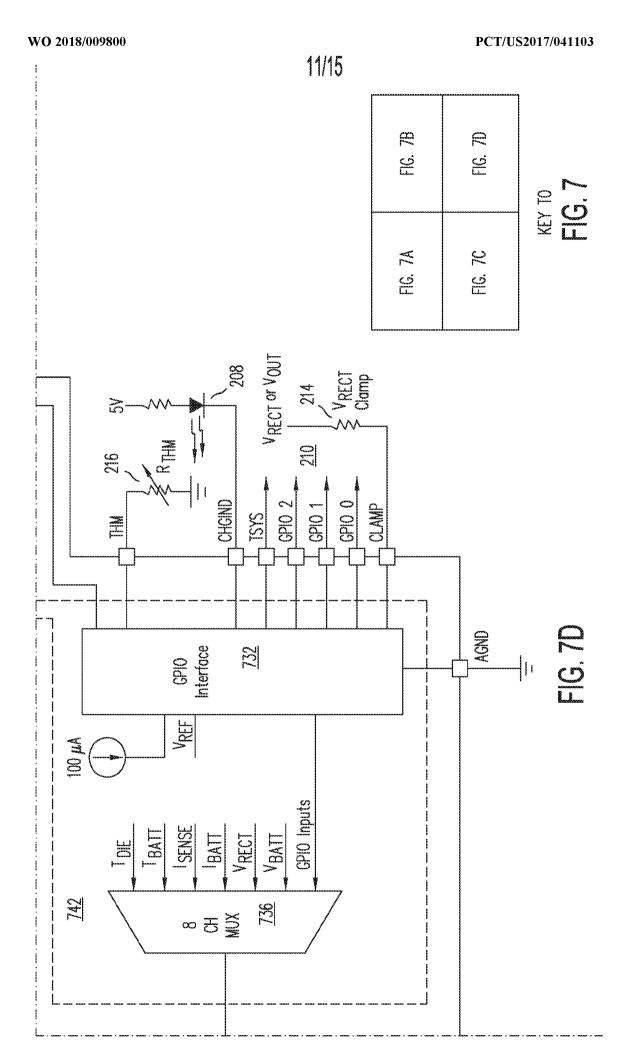
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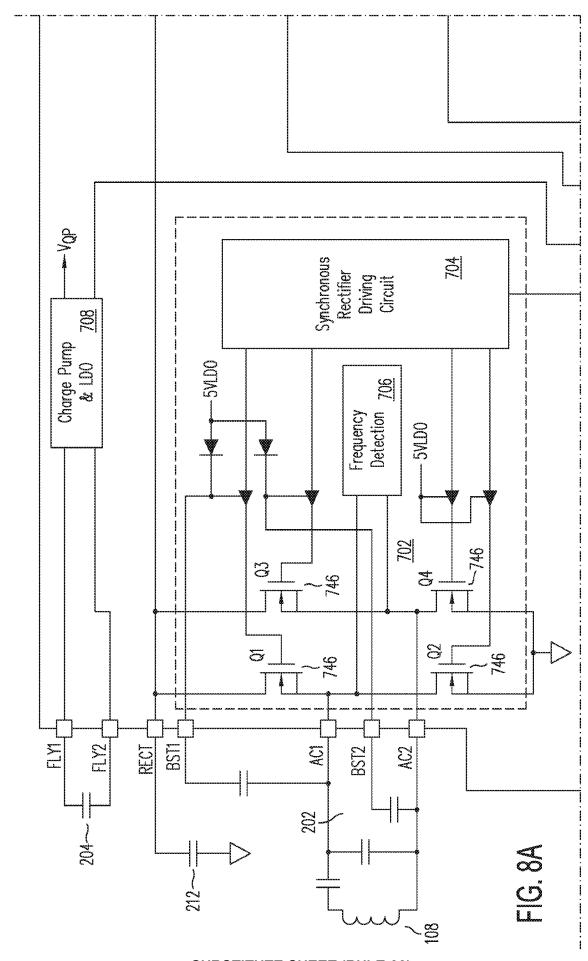


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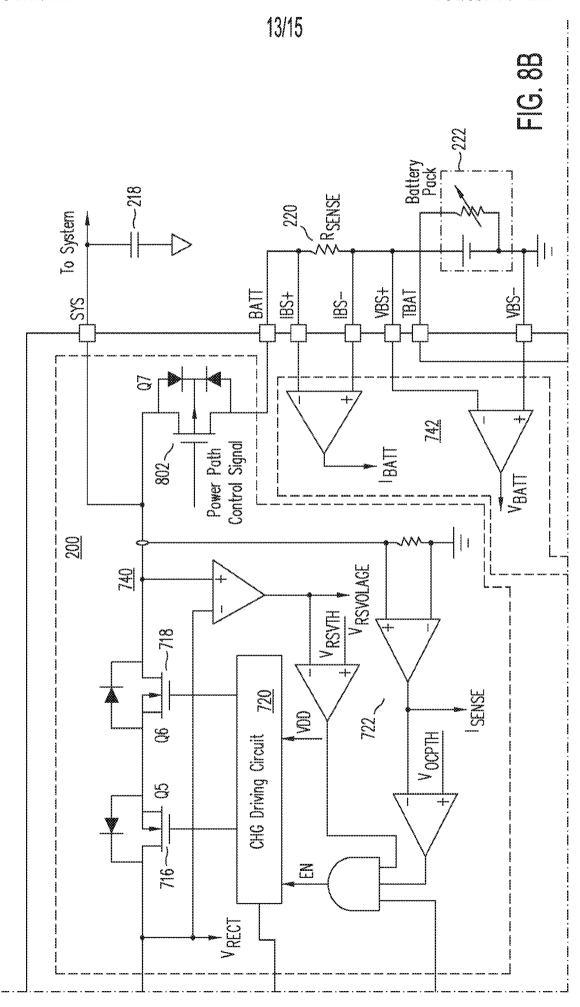


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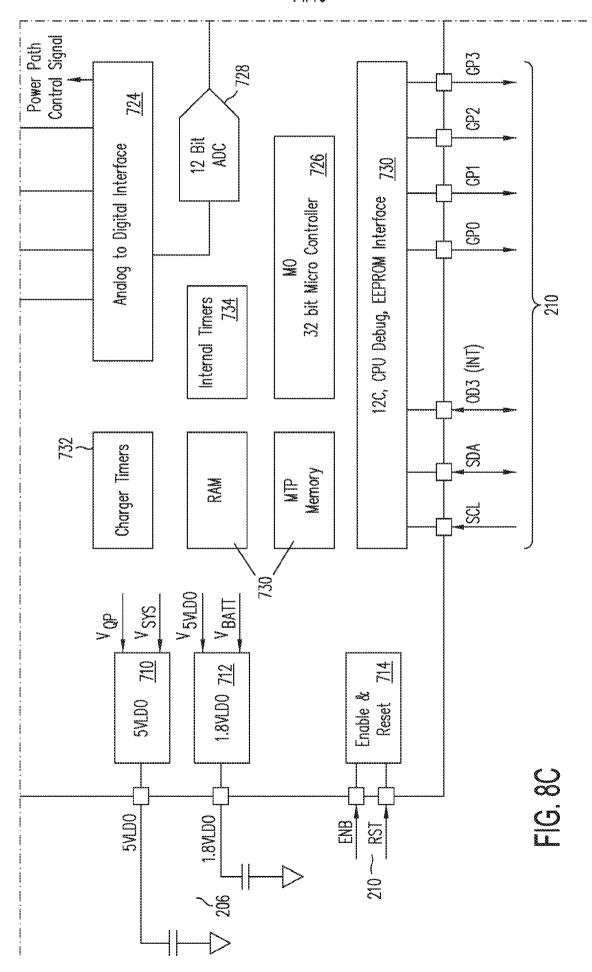




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WO 2018/009800 PCT/US2017/041103 15/15  $\otimes$ 8 FIG. 먑. FIG. 8C **∞** 띮. VRECT OF VOUT 210 CHGIND TSYS 007 90 GP4 AGND GP10 Interface 732 -||11 VREF 736 ∞∄

## INTERNATIONAL SEARCH REPORT

International application No. PCT/US2017/041103

| A. CLASSIFICATION OF SUBJECT MATTER IPC(8) - H02J 7/00; H02J 7/02; H02J 17/00; H04B 5/00 (2017.01) CPC - H02J 17/00; H02J-005/00/5; H02J-007/02/5; H04B-005/00/37 (2017.08)                   |  |  |                       |  |  |
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| According to International Patent Classification (IPC) or to both national classification and IPC   |  |  |                       |  |  |
| B. FIELDS SEARCHED  |  |  |                       |  |  |
| Minimum documentation searched (classification system followed by classification symbols)  See Search History document  |  |  |                       |  |  |
| Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched USPC - 307/104.000; 320/108.000; 455/41.100 (keyword delimited) |  |  |                       |  |  |
| Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  See Search History document                                     |  |  |                       |  |  |
| C. DOCUN  | MENTS CONSIDERED TO BE RELEVANT  |  |                       |  |  |
| Category*   | Citation of document, with indication, where appr  | opriate, of the relevant passages  | Relevant to claim No. |  |  |
| x   | US 20160118843 A1 (SAMSUNG ELECTRONICS CC  | 1, 5-12, 15  |                       |  |  |
| <br>Y   | document   | (  | <br>2-4, 13, 14       |  |  |
| Y   | US 20150364928 A1 (MEDIATEK SINGAPORE PTE. entire document   | 2-4, 13, 14  |                       |  |  |
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| Furthe  | r documents are listed in the continuation of Box C.   | See patent family annex.   |                       |  |  |
| "A" docume  | pecial categories of cited documents:  cument defining the general state of the art which is not considered be of particular relevance  "T" later document published after the international filing date or prior date and not in conflict with the application but cited to understate the principle or theory underlying the invention |  |                       |  |  |
| "E" earlier a<br>filing da  | pplication or patent but published on or after the international te  | "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone |                       |  |  |
| cited to  | nt which may throw doubts on priority claim(s) or which is establish the publication date of another citation or other reason (as precified).  | "Y" document of particular relevance; the claimed invention cannot b   |                       |  |  |
| special reason (as specified)  "O" document referring to an oral disclosure, use, exhibition or other means   |  | considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art   |                       |  |  |
|   | document published prior to the international filing date but later than "&" document member of the same patent family the priority date claimed   |  |                       |  |  |
| Date of the actual completion of the international search 29 August 2017  Date of mailing of the international search report 2 9 S E P 2017   |  |  | ch report             |  |  |
| Name and mailing address of the ISA/US  |  | Authorized officer   |                       |  |  |
| Mail Stop PCT, Attn: ISA/US, Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450  |  | Blaine R. Copenheaver  |                       |  |  |
| Facsimile No. 571-273-8300  |  | PCT Helpdesk: 571-272-4300<br>PCT OSP: 571-272-7774  |                       |  |  |