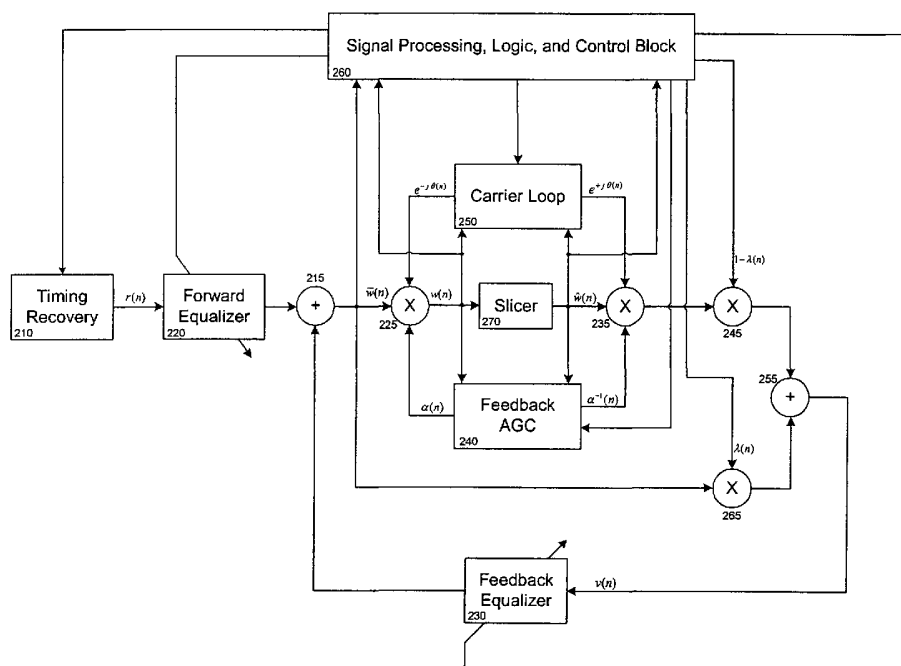




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[Continued on next page]

(54) Title: JOINT, ADAPTIVE CONTROL OF EQUALIZATION, SYNCHRONIZATION, AND GAIN IN A DIGITAL COMMUNICATIONS RECEIVER



(57) Abstract: Various aspects and embodiments of the present invention derive statistics of received signal quality and use these statistics to jointly control operation of timing recovery, carrier recovery, automatic gain control, and equalization functions.



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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC, COMPENDEX

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 073 030 A (BRUN ZDENEK ET AL) 6 June 2000 (2000-06-06) column 7, line 49 - column 8, line 5 column 8, line 30 - line 46 -----	1
X	US 4 607 230 A (ENDO CHIHIRO ET AL) 19 August 1986 (1986-08-19) column 6, line 42 - line 58 column 7, line 7 - line 63 ----- -/--	1

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

20 August 2004

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>SIRIKIAT ARIYAVISITAKUL ED - INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS: "EQUALIZATION OF A HARD-LIMITED SLOWLY-FADING MULTIPATH SIGNAL USING A PHASE EQUALIZER WITH A TIME-REVERSAL STRUCTURE"</p> <p>6 May 1990 (1990-05-06), VEHICULAR TECHNOLOGY CONFERENCE. ORLANDO, MAY 6 - 9, 1990, NEW YORK, IEEE, US, PAGE(S) 520-526, XP000204168</p> <p>page 523, right-hand column - page 524, left-hand column</p> <p>-----</p>	1

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International Application No

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