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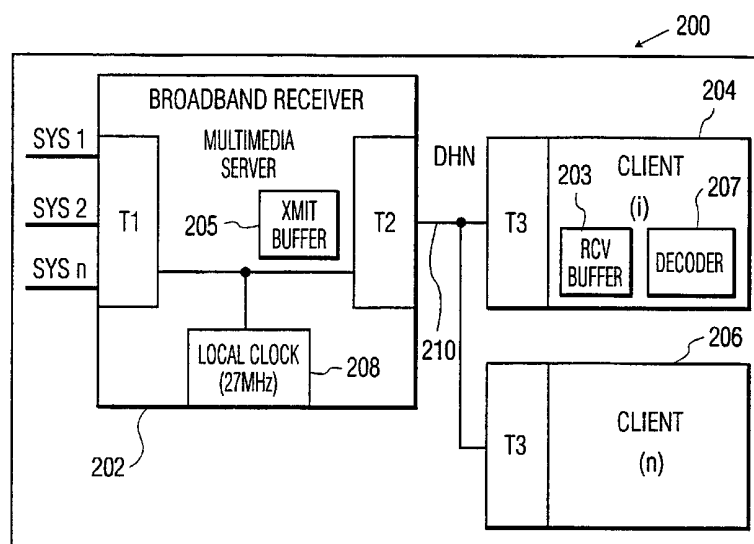
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(54) Title: MULTI-MEDIA JITTER REMOVAL IN AN ASYNCHRONOUS DIGITAL HOME NETWORK



(57) Abstract: A method and an apparatus using a system level clocking scheme to remove jitter from multi-media packets distributed over an asynchronous network. The present invention overcomes the problems associated with jitter introduced in an asynchronous network by using various time stamps to synchronize a client device clock to a headend clock and to control the data flow in the client device to match the rate that the data is received by a broadband receiver coupled to the headend. The present invention allows the client device to synchronize to a selected one of a plurality of headend clock by including a clock adjustment factor along with the time stamps. The time stamps are added at the physical layer so that the time stamps correspond to the time the data packets are placed onto and received from the asynchronous network.



For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 602 992 A (DANNEELS GUNNER) 11 February 1997 (1997-02-11) column 1, line 60 -column 2, line 11; figures 3-7	1-3, 9-12, 18, 19
A	US 5 822 317 A (SHIBATA KOICHI) 13 October 1998 (1998-10-13) column 3, line 55 -column 4, line 48; figure 1	1-20



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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T later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

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Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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			SG	47984 A1	17-04-1998
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