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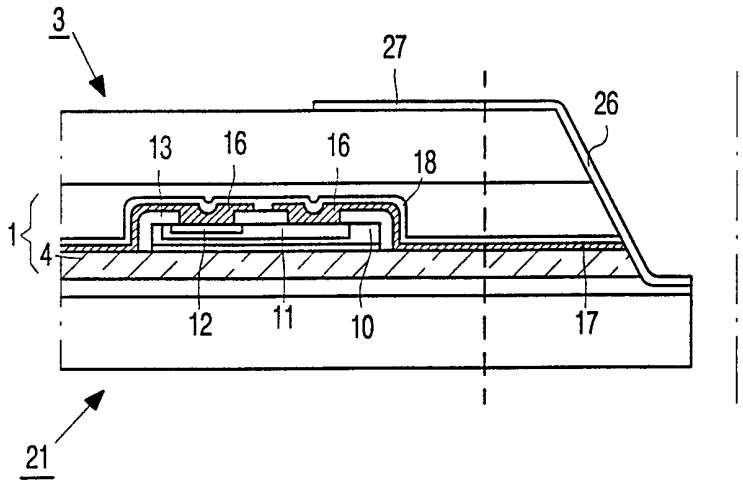
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(54) Title: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICES WITH "CHIP SIZE PACKAGE"

(57) Abstract

A method of manufacturing enveloped semiconductor devices, in which: use is made of a slice of semiconductor material (1) which is provided on its first side (3) with an intermediate layer (4) of an insulating material on which a top layer of a semiconductor material is formed, the semiconductor elements are formed in this top layer, prior to the formation of the metallization of the first side of the slice, the top layer is removed from the insulating layer at the locations of the free paths and by removing semiconductor material from the second side, the layer of an insulating material situated below the top layer is exposed.



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INTERNATIONAL SEARCH REPORT

International application No.
PCT/IB 99/00394

A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H01L 21/56
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 9519645 A1 (SHELLCASE LTD.), 20 July 1995 (20.07.95), see the whole document -- --------	1-5

☐ Further documents are listed in the continuation of Box C. ☒ See patent family annex.

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Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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