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(12) **United States Design Patent**
McMurray et al.

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(54) **MICROFLUIDIC GLASS CHIP INTERFACE BRACKET**

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- (*) Notice: This patent is subject to a terminal disclaimer.
- (**) Term: **15 Years**

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(51) **LOC (14) Cl.** **24-02**
(52) **U.S. Cl.**
USPC **D24/224; D13/182**

(58) **Field of Classification Search**
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D13/154, 155, 182, 184, 199; D15/140,
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D8/363, 364; D24/169, 216, 224, 225,
D24/226, 227, 230, 232, 233
CPC G01N 1/00; G01N 27/00; G01N 27/44;
G01N 27/791; B01L 3/00; B01L 3/50;
B01L 3/502; B01L 3/5027; B01L
3/502707; B01L 3/502715; B01L 3/5023;
B01L 9/00; B01L 9/52; B01L 9/523;
B01L 9/527; B01L 9/56

See application file for complete search history.

(57) **CLAIM**

The ornamental design for a micro fluidic glass chip interface bracket, as shown and described.

DESCRIPTION

The invention was made with government support under Contract No. DE-AC05-00OR22725 awarded by the U.S. Department of Energy. The government has certain rights in the invention.

FIG. 1 is a perspective view of a microfluidic glass chip interface bracket showing our new design;

FIG. 2 is a front view thereof;

FIG. 3 is a right side view thereof, the left side view being a mirror image thereof;

FIG. 4 is a back view thereof;

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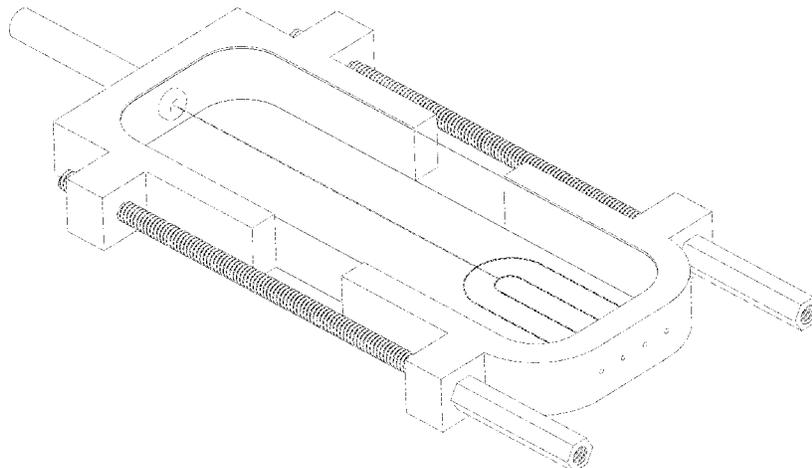


FIG. 5 is a top view thereof; and,
FIG. 6 is a bottom view thereof.
The left side view is a mirror image of the right side view.

1 Claim, 5 Drawing Sheets

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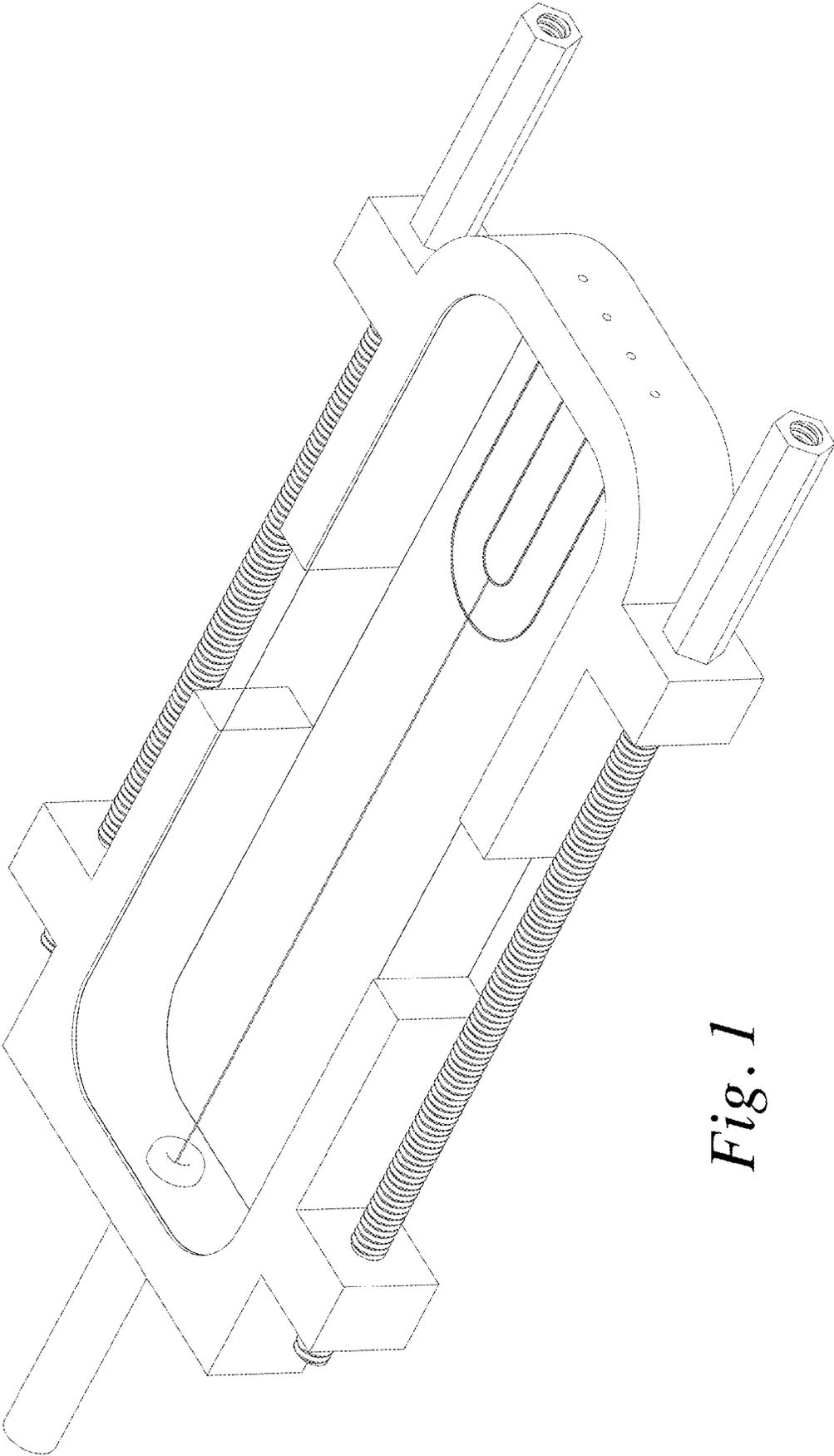


Fig. 1

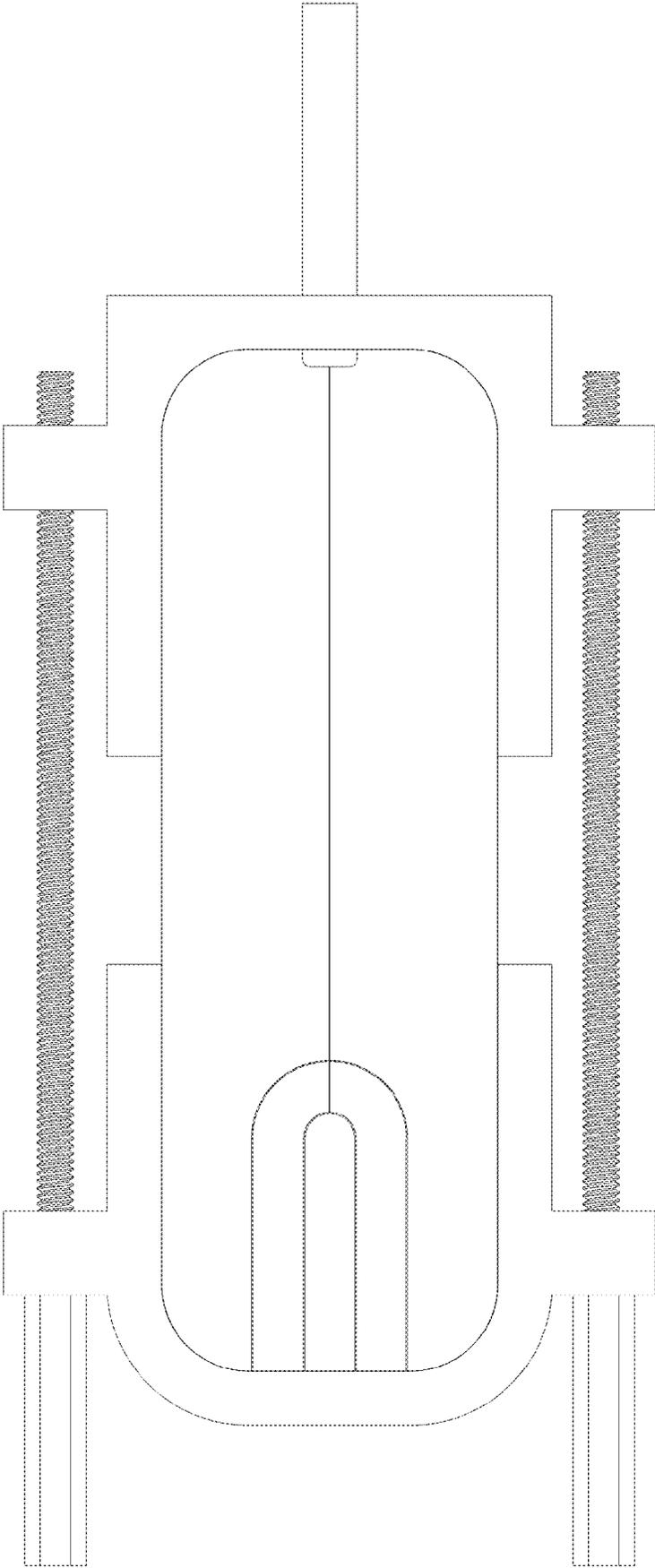


Fig. 2

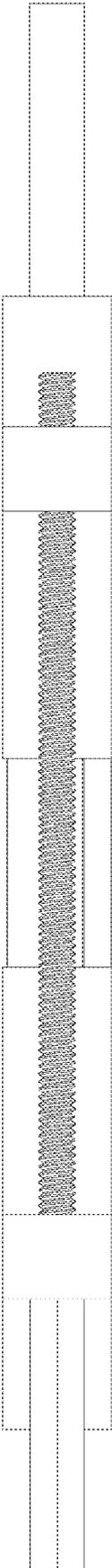


Fig. 3

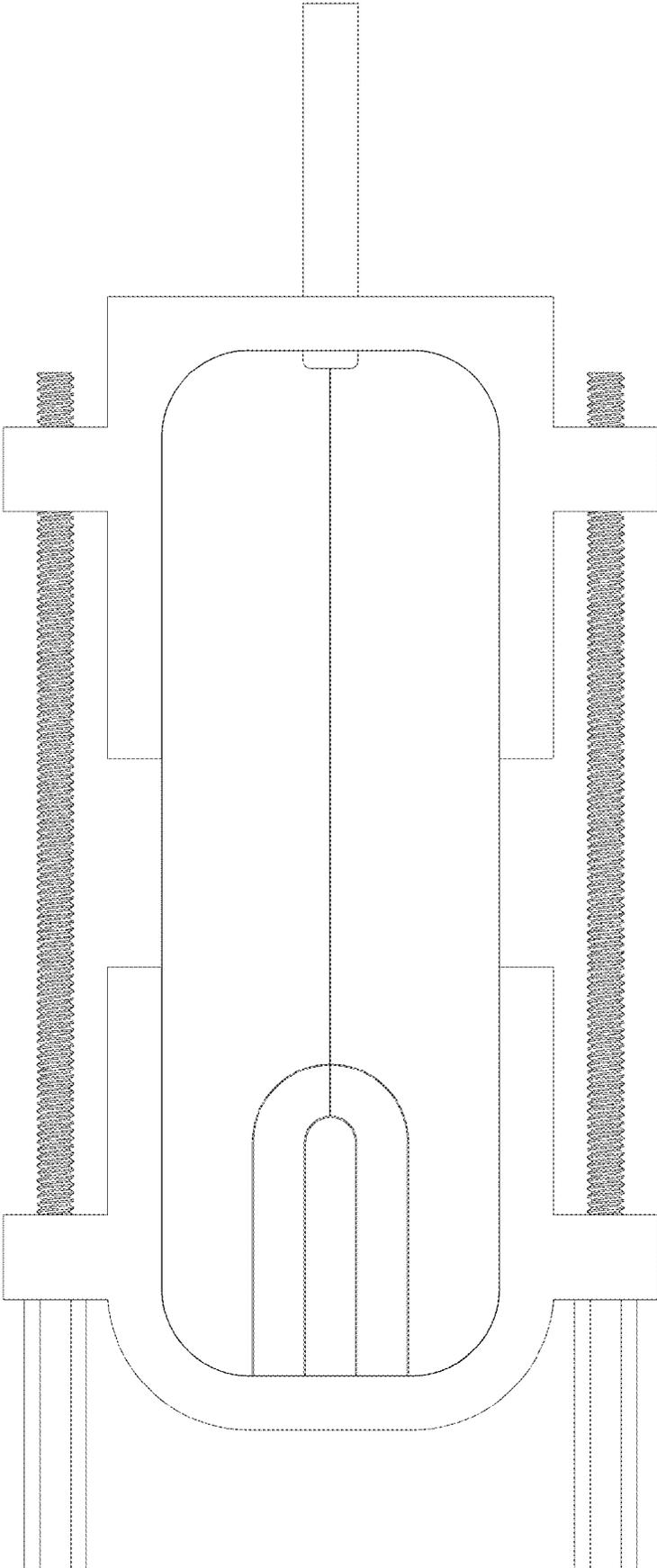


Fig. 4

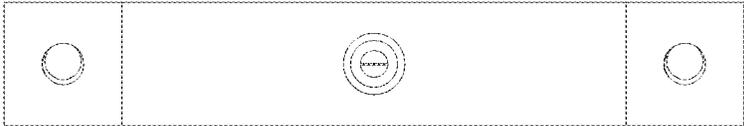


Fig. 5



Fig. 6