Aspects of the present invention provide for a novel photomask for patterning features for an integrated circuit, the photomask including a first area transmitting light in a first phase surrounded by second area, the second area transmitting light in a second phase, the second phase opposite the first phase. No blocking material separates the first area from the second area. After development of photoresist, the transition between the first and second area causes formation of a residual photoresist feature on the photoresist surface due to phase canceling of light. If the first area is small enough, it is nonprinting, i.e., the opposite sides of the residual photoresist feature formed at its perimeter merge, forming a contiguous photoresist feature, and thus a corresponding patterned feature after etch.
Fig. 1a

Fig. 1b

Fig. 1c
Fig. 5

Fig. 6
Fig. 10a

Fig. 10b

Fig. 10c
PHOTOMASK FEATURES WITH CHROMELESS NONPRINTING PHASE SHIFTING WINDOW

BACKGROUND OF THE INVENTION

[0001] The invention relates to a method for patterning fine features for semiconductor devices using a phase shifting mask with no blocking material separating the opposite phases.

[0002] Patterned features making up integrated circuits are conventionally formed using photolithography and etch techniques. A photomask, which transmits light in some areas and blocks it in others, is formed, the blocking areas corresponding to the pattern to be formed on the wafer surface (or its inverse.) The surface to be patterned, for example a semiconductor or dielectric layer, is covered with a layer of photoresist, a photoactive material. Light is projected onto the photoresist surface using the photomask, selectively exposing areas of photoresist. The wafer is then subjected to a developing process, in which exposed photoresist (or unexposed photoresist, in the case of negative photoresist) is removed, leaving patterned photoresist behind.

[0003] The remaining patterned photoresist then typically serves to protect underlying material during a subsequent etch process, creating features in the same pattern as the remaining photoresist.

[0004] Over the years integrated circuits have become denser and patterned features smaller. As projected features become smaller, the limits of resolution are reached and it becomes more difficult to project patterns with sharp edges. Poor resolution can lead to incomplete patterning and to incomplete etching or overetching, causing device flaws.

[0005] Alternating phase shifters, which invert the phase of light in some areas of the photomask, increasing contrast in light intensity at the photoresist surface, are a powerful tool to improve resolution and sharpen edges.

[0006] The use of alternating phase shifters in photomasks, however, has disadvantages. When alternating phase shifters are used, projected light is either incident, in what will be called zero degree phase, or inverted, in what will be called 180 degree phase (this is sometimes also called π phase.) As will be more fully described, as conventionally used, light in opposite phases is transmitted on opposite sides of an obscured area. The configuration of some patterns leads to phase conflicts, in which rules dictate that the same area must see light of opposite phases. To date, this has meant that use of alternating phase shifters has been limited to only certain types of patterns.

[0007] There is a need, therefore, for increased flexibility of phase shifting photomasks.

SUMMARY OF THE INVENTION

[0008] The present invention is defined by the following claims, and nothing in this section should be taken as a limitation on those claims. In general, the invention is directed to an improved method for using phase shifters in a photomask for photolithography.

[0009] A first aspect of the invention provides for a phase shifting photomask comprising a plurality of transmitting nonprinting windows transmitting light in a first phase; a transmitting area transmitting light in a second phase, each transmitting window substantially entirely surrounded by and in contact with the transmitting area with no blocking material intervening, wherein the second phase is substantially opposite the first phase, and wherein a first width of unbroken transmitting area surrounds each transmitting window on all sides, the first width sufficient for the unbroken transmitting area to print when the photomask is used to expose photoresist.

[0010] Another aspect of the invention provides for a phase shifting photomask comprising a transmitting nonprinting window transmitting light in a first phase; and a transmitting area substantially entirely surrounding and in contact with the transmitting window on all sides with no blocking material intervening, wherein the transmitting area transmits light in a second phase, the second phase substantially opposite the first phase, and wherein, when used to pattern photoresist, the transmitting area is printing on all sides of the transmitting window.

[0011] An embodiment of the invention provides for a phase shifting photomask comprising a plurality of spatially separate transmitting nonprinting windows transmitting light in a first phase; a transmitting area transmitting light in a second phase, the second phase substantially opposite the first, the transmitting area entirely surrounding and in contact with each of the transmitting windows of the first plurality; wherein each transmitting window is separated from its nearest neighbor in the plurality by an unbroken length of transmitting area having at least a first dimension, and wherein the smallest dimension of each window is no more than about 160 percent of the first dimension.

[0012] Yet another embodiment of the invention provides for a phase shifting photomask comprising a transmitting nonprinting window having a first shifting degree; a second transmitting area having a second shifting degree, the second transmitting area entirely surrounding and in contact with the first transmitting window, wherein the second transmitting area is printing on all sides of the transmitting window; and wherein the second shifting degree is substantially opposite the first shifting degree.

[0013] Another aspect of the invention provides for a method for forming a patterned feature on a wafer surface, the method comprising transmitting light through a phase shifting photomask onto photoresist covering the wafer surface; forming an isolated first residual photoresist feature between a first wafer area exposed to light in a first phase and a second wafer area exposed to light in a second phase, wherein the first phase is substantially opposite the second phase, and wherein the second wafer area entirely surrounds the first wafer area in the plane of the wafer; and forming the patterned feature from the photoresist feature.

[0014] Still another aspect of the invention provides for a method for forming photoresist features on a wafer surface using a photomask, the method comprising transmitting light through a first mask area onto a first wafer area, the first mask area having a first shifting degree; transmitting light through a second mask area onto a second wafer area, the second mask area having a second shifting degree, wherein the second mask area entirely surrounds and is on all sides in contact with the first mask area, and the first shifting degree is substantially opposite the second shifting degree; and developing photoresist, wherein, after the developing
step, a closed residual photoresist feature remains between the first wafer area and the second wafer area, and wherein the closed residual photoresist feature is isolated and not merged with any adjacent photoresist feature.

[0015] Another aspect of the invention provides for a monolithic three dimensional memory array comprising a plurality of patterned features, the plurality of patterned features patterned using a photomask comprising: a plurality of spatially separate first transmitting windows, wherein the transmitting windows transmit light in a first phase; and a transmitting area of the photomask, each transmitting window substantially surrounded by and in contact with the transmitting area, wherein the transmitting area transmits light in a second phase, the second phase substantially opposite the first phase.

[0016] Each of the aspects and embodiments of the invention can be used alone or in combination with one another.

[0017] The preferred aspects and embodiments will now be described with reference to the attached drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0018] FIG. 1a is a cross section of a portion of a conventional binary photomask.

[0019] FIG. 1b shows the electrical field in the plane of the photomask for the photomask of FIG. 1a.

[0020] FIG. 1c shows the light intensity at the surface of the photoresist for light projected through the photomask of FIG. 1a.

[0021] FIG. 2a is a cross section of a portion of a conventional alternating phase shifting photomask.

[0022] FIG. 2b shows the electrical field in the plane of the photomask for the photomask of FIG. 2a.

[0023] FIG. 2c shows the light intensity at the surface of the photoresist for light projected through the photomask of FIG. 2a.

[0024] FIG. 3 illustrates phase assignment for a line-and-space pattern using an alternating phase shifting mask.

[0025] FIG. 4 illustrates phase conflict for rectangular shapes arranged in a grid using an alternating phase shifting mask.

[0026] FIG. 5 shows the electrical field in the plane of the photomask when an unshifted region is immediately adjacent a shifted region.

[0027] FIG. 6 illustrates a masked feature having an interior nonprinting window surrounded by blocking material.

[0028] FIG. 7 shows successful phase assignment for rectangular shapes arranged in a grid pattern using nonprinting interior alternating phase shifters.

[0029] FIG. 8 shows a prior art phase shifting mask and the residual photoresist features created by it.

[0030] FIG. 9 illustrates adjacent shifting areas separated by very thin, nonprinting transmitting areas according to a prior art photomask.

[0031] FIG. 10a shows a photomask according to the present invention in plan view.

[0032] FIG. 10b shows a photomask according to the present invention in cross section.

[0033] FIG. 10c shows the electrical field in the plane of the photomask for the photomask of FIG. 9a.

[0034] FIG. 10d shows the light intensity at the surface of the photoresist for light projected through the photomask of FIG. 9a.

[0035] FIG. 10e shows, in cross section, residual photoresist features formed on the wafer surface using the photomask of FIG. 9a.

[0036] FIGS. 11a through 11d illustrate formation of a photomask having masked features with interior nonprinting windows.

[0037] FIGS. 12a through 12d illustrate formation of a photomask formed according to the present invention.

[0038] FIGS. 13a and 13b show, in cross section, two projected photoresist features formed using the same photomask with different exposure dose.

[0039] FIGS. 14a-14d show different illumination apertures.

[0040] FIG. 15 shows a photomask according to the present invention in plan view.

[0041] FIG. 16a shows, in plan view, a transmitting window surrounded by a first width of unbroken transmitting area according to the present invention.

[0042] FIG. 16b shows, in plan view, a transmitting window not surrounded by a first width of unbroken transmitting area.

DETAILED DESCRIPTION OF THE INVENTION

[0043] One approach to allow alternating phase shifting photomasks to be used to pattern a wider variety of shapes is taught in Chen, U.S. patent application Ser. No. 10/728,436, filed Dec. 5, 2003, hereinafter the ‘436 application. The ‘436 application is the work of the same inventor as the present invention, is owned by the assignee of the present invention, and is hereby incorporated by reference. The present invention is an improved method to solve the same problem.

[0044] The problem addressed by both the present invention and the ‘436 application will be described.

[0045] The ‘436 application used the term masked feature. A masked feature referred to a feature in a photomask, for example a line, a rectangle, or any other shape. A masked feature in a photomask substantially entirely or partially obscures light, so that when light is projected through the photomask, a corresponding feature in the photoresist is shielded from light, while the area outside of the obscured area is exposed. This corresponding feature in photoresist will be called a projected photoresist feature. The projected photoresist feature will be roughly the same shape as the masked feature, though corners on projected photoresist features tend to be rounded. Typically a linear dimension in a masked feature is four or five times the size of the corresponding dimension in the projected photoresist feature, depending on the stepper used.
In this description, a photomask feature will refer to a feature in a photomask, which may be a line, a rectangle, or any other shape. This is a broader term than masked feature, in that a photomask feature may not include any blocking material. A photomask feature in a photomask does not transmit sufficient light for the corresponding area on the photore sist surface to be fully exposed, and the wafer surface beneath it will not be exposed after development of the photore sist. The area outside of the projection of the photomask feature is exposed, creating a photore sist feature.

A photore sist feature is a discrete feature formed by exposing and developing photore sist. Such a photore sist feature is surrounded by exposed wafer surface after development of photore sist. This term is broader than the term “projected photore sist feature” because it may be formed by projection, as is the case for a projected photore sist feature, or, as will be seen, by residual photore sist features formed at phase transition boundaries.

The simplest photomask is a binary photomask, shown in FIG. 1a. A plate of a transmitting material 12, for example quartz, makes up the bulk of the photomask. A blocking material 14, typically chromium, is formed in areas where light is to be obscured. FIG. 1b shows the electrical field in the plane of the photomask. The electrical field is either positive (1.0), non-existent (0.0), or negative (-1.0). Where light is transmitted it is in a first phase, here referred to as zero degree phase. Where light is blocked, there is no electrical field. (In FIGS. 1b and 1c, the X-axis is horizontal position, corresponding with horizontal position across the section of photomask shown in FIG. 1a.)

FIG. 1c shows the actual intensity distribution of light at the photore sist surface. It will be seen that, due to interference effects, the edges of lighter and darker areas are not perfectly defined, and even in the center of the obscured area, the light intensity at the photore sist surface is not zero. (A value of zero on the Y-axis of FIG. 1c indicates zero intensity. The value of 1.0 is unitless and arbitrarily assigned, and the other values assigned relative to it. This is a standard representation of image intensity, as will be known to those skilled in the art.)

This photomask is also made up of a plate of transmitting material 12, with regions of blocking material 14. In region 18, light is transmitted as in the binary mask. In region 20, however, the transmitting material 12 is etched such that light passing through it is shifted 180 degrees. An area of a photomask which inverts the phase of incident light, such as transmitting area 12, will be called a phase shifter.

FIG. 2a illustrates a conventional alternating phase shifting photomask 16. This photomask is also made up of a plate of transmitting material 12, with regions of blocking material 14. In region 18, light is transmitted as in the binary mask. In region 20, however, the transmitting material 12 is etched such that light passing through it is shifted 180 degrees. If either phase is assigned, a region of zero degree phase will be immediately adjacent a region of 180 degree phase. In the transition from zero degree phase to 180 degree phase, the electrical field must pass through zero, as shown in FIG. 5. (The X-axis on this figure again corresponds to horizontal position across the photomask.) Where the electrical field is zero, the light intensity at the photore sist surface will be zero, unintentionally creating a region of unexposed photore sist, leading to creation of a residual photore sist feature after development of the photore sist. In this case, the residual photore sist feature is unwanted.

The 436 application solved this problem by teaching masked features like the one shown in FIG. 6, each including an interior nonprinting window. For such a feature, light transmitted through the nonprinting window 22 is in a first phase, while light transmitted through a transmitting area 24 outside the masked feature is in a second phase opposite the first phase. For example, the window may be a phase shifter, such that light transmitted through the masked feature is in 180 degree phase, while the transmitting area 24 outside the window 22 is in zero degree phase. Alternatively, the window 22 may transmit light in zero degree phase, while the transmitting area outside the masked feature 24 is in 180 degree phase. The window is described as "nonprinting" because its dimensions are selected such that it will not print, i.e. such that light transmitted through it will not expose photore sist within the perimeter of the corresponding...
A photoresist feature enough for the wafer surface within the feature to be exposed after development of photoresist. In the '436 application, the interior nonprinting window 22 was substantially surrounded by blocking material 26. A blocking material is one that transmits 15 percent or less of incident light, for example chromium or molybdenum silicide.

Turning to FIG. 7, it will be seen that masked features with interior nonprinting windows, as taught in the '436 application, transmitting light in one phase inside the masked feature and light in an opposite phase outside the masked feature, can be printed with no phase conflict. Each masked feature F of FIG. 7 includes a window W, the window W comprising a phase shifter. Thus the windows W are assigned 180 degree phase. The transmitting area 34 commonly and substantially entirely surrounding the masked features F is assigned zero degree phase. Clearly the phases could be inverted if desired.

The techniques of the '436 application provide a powerful tool to pattern very fine features. The photomask used in the '436 application, however, is relatively complex and expensive to make. The present invention uses similar concepts, but omits the surrounding blocking material (blocking material 26, shown in FIG. 6) used in the '436 application, resulting in a photomask which is simpler and cheaper to make.

It has been described that when a nonshifting area and a shifting area are printed, the nonprinting area is immediately adjacent, at the transition from zero degree phase to 180 degree phase, the electrical field must pass through zero, as shown in FIG. 5. The light intensity at the corresponding point on the photoresist surface is also zero, so a residual photoresist feature remains on the wafer surface after the photoresist is developed.

Prior art chromeless phase edge techniques have made use of this residual photoresist feature. Turning to FIG. 8, a photomask is shown consisting of parallel lines, where the lines are alternately shifting regions 30 and nonshifting regions 32, each having a width 34. Such an arrangement is described in prior art which is mentioned in Lee et al., U.S. Pat. No. 5,240,796. Each of the shifting and nonshifting regions is printing, exposing a corresponding area on the photoresist surface. FIG. 8 further shows, below the photomask in cross-section, the wafer and photoresist surface after developing. It will be seen that residual photoresist lines 36 form on the wafer surface 38 at the phase transition boundaries, creating patterned lines after etch. (Dotted lines relate the phase transition in the photomask with the resulting residual photoresist feature.)

In other prior art, for example Chen et al., U.S. Pat. No. 6,482,555 (hereinafter the '555 patent), several phase shifting windows are placed in close proximity to each other, with no blocking material used, as in FIG. 50 or FIG. 7 of that patent, and as shown in FIG. 9. Turning to FIG. 9, the transmitting area 50 between the phase shifting areas 50 is the '555 patent has width 62. This width is so small that the residual photoresist features created by adjacent shifting areas 60 merge, forming a single, large photoresist feature. Thus the transmitting area between phase shifters is non-printing.

The present invention is a photomask comprising nonprinting transmitting windows transmitting light in a first phase entirely surrounded by a printing transmitting area transmitting light in a second phase opposite the first phase, with no blocking material intervening. The transition between the phases causes a closed residual photoresist feature to be formed at the perimeter of such a nonprinting transmitting window. The dimensions of the nonprinting transmitting window are chosen such that opposite sides of the residual photoresist feature merge, so that, during normal use of the photomask, photoresist in the interior of the feature is not exposed sufficiently to expose the wafer surface after the photoresist is developed. A patterned feature can be created from each residual photoresist feature.

To contrast with other photomasks mentioned herein: In the present invention, a photomask comprises shifting areas immediately adjacent to nonshifting areas, with no blocking material intervening (unlike the '436 application). In terms of the present invention, either the shifting area or the nonshifting area is nonprinting, so that opposite sides of a single closed residual photoresist feature formed by the nonprinting area merge (unlike the alternating shifting and non-shifting stripe photomask described in Lee et al.). The nonprinting area is entirely surrounded by an area that is printing (unlike the '555 patent) and creates a photoresist feature that does not merge with an adjacent photoresist feature. FIG. 10a, for example, shows a plan view of a section of a photomask formed according to the present invention. The areas labeled 180° are shifting, while the area labeled 0° is nonshifting. Clearly, the phases could be inverted if desired.

To summarize, an exemplary photomask according to the present invention comprises a transmitting nonprinting window transmitting light in a first phase; and a transmitting area substantially entirely surrounding and in contact with the transmitting window on all sides with no blocking material intervening, wherein the transmitting area transmits light in a second phase, the second phase substantially opposite the first phase, and wherein, when used to pattern photoresist, the transmitting area is printing on all sides of the transmitting window. A transmitting window is a continuous area of a photomask which transmits light and includes no blocking material which is surrounded on all sides by some other material.

FIG. 10b shows the same section of photomask in cross section, the cross section taken along line L-L'. Shifting areas are shifted to shift incident light by about 180 degrees, while nonshifting areas do not shift incident light. No blocking material separates shifting areas from nonshifting areas.

FIG. 10c shows the electric field in the plane of the photomask, and FIG. 10d shows the light intensity at the photoresist surface. FIG. 10e illustrates (in cross-section) residual photoresist features formed on the wafer surface after developing the photoresist. It will be seen that, if the dimensions of the shifting area and the nonshifting areas are chosen appropriately, the opposite sides of a single residual photoresist features formed at the phase transition perimeter are so close together that they are in contact, and merge.

Thus, using such a photomask, a patterned feature can be formed by transmitting light through a phase shifting photomask onto photoresist covering the wafer surface; forming an isolated first residual photoresist feature between a first wafer area exposed to light in a first phase and a
second wafer area exposed to light in a second phase, wherein the first phase is substantially opposite the second phase, and wherein the second wafer area entirely surrounds the first wafer area in the plane of the wafer; and forming the patterned feature from the photore sist feature. In this case the isolated first photore sist feature defines a closed shape having a perimeter, and, after the developing step and before the etching step, no portion of the wafer surface is exposed within the perimeter. The photore sist feature is described as isolated because it is not merged with an adjacent photore sist feature.

[0068] Such a photomask is relatively simple to make, compared to the photomask used in the '436 application.

[0069] The photomask of the '436 application requires two patterning steps, one to pattern the blocking material, the second to etch the quartz to form the shifting area. Turning to FIG. 11a, patterning begins with quartz layer 40 coated with a layer of blocking material (typically chromium) 42. Photore sist 44 is deposited on chromium layer 42, then patterned in the shape of the blocking material defining masked features with interior nonprinting windows, like the photomask shown in FIG. 7. FIG. 11b shows the photomask after etch of chromium layer 42 and removal of photore sist. (It will be noted that in cross-sectional figures representing patterning of a photomask, the photomask appears upside down relative to its presentation in figures representing the photomask in use; in FIG. 2a, for example, where the blocking material appears below the photomask, as opposed to FIG. 11a, where it is shown above. This represents the orientation of the photomask during these respective activities.)

[0070] For the second patterning step, photore sist is again applied and patterned, this time exposing only the interior windows of the masked features, as shown in FIG. 11c. An etch step thick quartz layer 40, creating shifting area 46, as shown in FIG. 11d, which shows the completed photomask.

[0071] Both photolithography steps performed to create the photomask of the '436 application present challenges; the first includes patterning of complex shapes, while the second requires precise alignment.

[0072] Forming the photomask according to the present invention, for example the photomask shown in FIG. 10a, is significantly simpler. Turning to FIG. 12a, patterning again begins with quartz layer 40 coated with chromium layer 42. Photore sist 44 is deposited on chromium layer 42, then patterned to excavate rectangular holes in the photore sist. FIG. 12b shows patterned chromium remaining after the etch step. In this example, a continuous area of chromium remains with rectangular holes etched in the chromium, the surface of quartz layer 40 exposed in the holes. A second etch step follows, in which the remaining chromium 42 serves as a hard mask while the quartz layer 40 is etched to produce rectangular shifting areas 46 shown in FIG. 12c. FIG. 12d shows the completed photomask after the chromium layer 42 has been stripped.

[0073] Only a single, relatively simple patterning step is required to make this photomask. It will be noted that none of chromium layer 42 remained in the completed photomask shown in FIG. 12d. It was included in this description because standard photomask blanks as purchased normally include this layer. If quartz or other suitable photomask substrate with no chromium layer was used as the starting point, then photore sist could be deposited directly on the quartz, the photore sist patterned and the quartz etched, without the intermediate chromium etch.

[0074] For clarity, an example has been provided of how to make a photomask according to the present invention. Many variations will be readily apparent to those skilled in the art. This example described a photomask having shifting transmitting areas surrounded by a nonshifting transmitting area; clearly the phases could be inverted. Negative photore sist is also well known in the art. When developed, the exposed areas of negative photore sist remain, while the obscured areas are removed. For brevity, the present application describes the use of positive photore sist. Those skilled in the art will appreciate that negative photore sist could be used either in formation of the photomask or when patterning the wafer surface. A regular rectangular pattern was described; clearly other patterns can be envisioned.

[0075] When describing dimensions in a photomask, it is usual to speak of those dimensions in terms of projected dimensions; i.e. rather than describing the actual size of a photomask feature in the photomask, one describes the size of the photomask feature multiplied by the stepper magnification. Stepper magnification is typically 4 or 5; i.e. a linear dimension of a chromium area in a photomask is typically about four or five times larger than the size of the same feature as projected onto the photore sist surface.

[0076] This description will follow this convention. A photomask feature dimension will be described as “xS”, or multiplied by a projection scaling factor S. The projection scaling factor S is the stepper magnification. Suppose, for example, the stepper magnification and the projection scaling factor is four. If the actual physical dimension of a photomask feature in a photomask is, for example, 1000 nm, the dimension of the photomask feature will be described as 250 nmxS. For a stepper magnification and projection scaling factor S of five, and an actual physical dimension of 1000 nm of a photomask feature will be described as 200 nmxS.

[0077] Stepper magnification is determined by optics, and is the single most important factor controlling the relationship between the size of a photomask feature and the size of the resulting projected photore sist feature. It is not, however, the only factor. As is well known in the art, varying exposure dose also changes the size of projected photore sist features. Longer exposure results in more light energy reaching the photore sist surface, and more thorough exposure of photore sist. When the photore sist is developed, more photore sist is removed; thus for larger exposure dose, dimensions of the eventual etched features are smaller. FIG. 13a and FIG. 13b, for example, show projected photore sist features 47 after development. The photore sist feature 47 of FIGS. 13a and 13b could be formed from the same photomask feature in the same photomask during separate photolithography operations. The narrower photore sist feature 47 of FIG. 13b is created when a higher exposure dose is applied.

[0078] It will be recalled that in aspects of the present invention, residual photore sist features are formed using a photomask fabricated according to the present invention, having first transmitting windows transmitting light in a first phase surrounded by a second transmitting area transmitting light in a second phase, the second phase substantially
opposite the first phase. The size of the first transmitting windows are selected such that the opposite sides of a single residual photoresist feature formed at the perimeter of a window merge. The residual photoresist feature forms at the transition between opposite phases; i.e. at the border between the projection of a first transmitting window and the second transmitting area surrounding it, and not entirely within the boundaries of the first transmitting window. Thus a linear dimension of a photoresist feature defined by merged residual photoresist lines may be larger than the linear dimension $s$ of the photomask feature used to create it.

[0079] Many interrelated factors determine the actual dimension a transmitting window must have in its shortest dimension to be nonprinting, according to the present invention. In general, the size of the window is proportional to the wavelength of incident light, and is inversely proportional to numerical aperture.

[0080] The shortest dimension for a transmitting window to be nonprinting is also affected by the aperture of the condensing lens. This value can either be a single aperture radius $\sigma_1$ or can be an inner aperture radius $\sigma_i$ and an outer aperture radius $\sigma_o$. FIG. 14a shows a conventional aperture which has a single aperture radius $\sigma$. An annular aperture is shown in FIG. 14b, in which the center area is obscured and light is transmitted through an annulus having an outer aperture radius $\sigma_o$ and an inner aperture radius $\sigma_i$.

[0081] A quadrupole aperture, shown in FIG. 14c, is obscured except for four holes arranged as shown. The outer aperture radius $\sigma_o$ is the distance to the outside edge of each hole, while the inner aperture radius $\sigma_i$ is the distance to the inside edge of each hole. A dipole aperture, shown in FIG. 14d, is obscured except for two holes arranged as shown. The outer aperture radius $\sigma_o$ is the distance to the outside edge of each hole, while the inner aperture radius $\sigma_i$ is the distance to the inside edge of each hole. Aperture radius is generally uniform, and is expressed as a proportion of the entire lens. For example, for a conventional aperture, $\sigma$ is typically about 0.7.

[0082] The relationship of all of these factors can be summarized as follows:

$$D' \propto \frac{\lambda}{\left(1 + \frac{\sigma_i^2 + \sigma_o^2}{2}\right) NA}$$

[0083] In this equation, $D'$ represents the size the shortest dimension of a transmitting window must have in order to be nonprinting; $\lambda$ is the wavelength of incident light, $\sigma_i$ is the inner aperture radius and $\sigma_o$ is the outer aperture radius; and NA is the numerical aperture. Thus $D$ is proportional to wavelength $\lambda$, and is inversely proportional to aperture radius $\sigma$ (or inner aperture radius $\sigma_i$ and outer aperture radius $\sigma_o$) and numerical aperture NA.

[0084] Conventionally, light of two wavelengths are in general use: 248 nm (KrF) and 193 nm (ArF).

[0085] In general, for 248 nm photolithography, at any point at which a transmitting window has a dimension no more than about 160 nm $\times$ 8S, it will be nonprinting. Similarly, for 193 nm photolithography, at any point at which a transmitting window has a dimension no more than about 120 nm $\times$ 8S, it will be nonprinting.

[0086] In practical terms a window having a shortest dimension smaller than about 50 nm will be ineffective; preferably the window of the present invention should have no dimension less than about 60 nm.

[0087] FIG. 15 is a plan view of an exemplary photomask formed according to the present invention. The photomask comprises a plurality of transmitting nonprinting windows 50 transmitting light in a first phase; a transmitting area 52 transmitting light in a second phase, each transmitting window 50 substantially entirely surrounded by and in contact with the transmitting area 52 with no blocking material intervening, wherein the second phase is substantially opposite the first phase, and wherein a first width of unbroken transmitting area 52 surrounds each transmitting window on all sides, the first width sufficient for the unbroken transmitting area 52 to print when the photomask is used to expose photoresist.

[0088] In this example, windows 50 have linear dimension $D_1$, and are separated by distance $D_2$. $D_2$ is chosen so that opposite sides of a single residual photoresist feature formed on the photoresist surface at the projected perimeter of a window merge, and the window 50 is nonprinting. $D_2$ is chosen so that the space between the windows is printed, that is, so that the photoresist in this area is exposed sufficiently that it is removed during developing. Because the window 50 is a closed feature, its interior receives less overall light than does the transmitting area 52 between windows. For this reason dimension $D_1$ can be larger than first dimension $D_2$ while window 50 is nonprinting and transmitting area 52 is printing. The exposure dose must be selected so that the windows 50 are nonprinting. (This example shows windows 50 square and uniformly spaced. Clearly many other arrangements are possible. Transmitting windows 50 can be rectangular but not square, or any other shape. Transmitting windows 50 can be randomly spaced.)

[0089] Describing the transmitting area surrounding a transmitting window on all sides as "unbroken" for a first width means that within that width only the transmitting area exists, and that no other material, such as blocking material or another transmitting window transmitting light in a different phase, is present. For example, turning to FIG. 16a, transmitting window 50, which transmits light in a first phase, is surrounded on all sides by a first width $D_2$ of unbroken transmitting area 52, which transmits light in a second phase substantially opposite the first phase. The boundary of first width $D_2$ is denoted by the dotted-line frame 51. No other material exists within first width $D_3$ of transmitting window 50. The first width $D_3$ is sufficient for the unbroken transmitting area 52 to print when the photomask is used to expose photoresist; in preferred embodiments, the first width $D_3$ is at least 100 nm.S.

[0090] In contrast, in FIG. 16b, transmitting window 54, which in this example transmits light in the first phase, is less than a first width $D_2$ from transmitting window 50, and thus transmitting window 50 in FIG. 16b is not surrounded on all sides by first width $D_3$ of unbroken transmitting area 52. An example of such a configuration appears in the '555 patent, in which the width of the transmitting area separating adjacent transmitting shifting windows is not sufficient for the transmitting area between those windows to print when the photomask is used to expose photoresist.
In other words, FIG. 15 shows a section of a photomask. The photomask comprises a plurality of spatially separate transmitting nonprinting windows 50 transmitting light in a first phase; a transmitting area 52 transmitting light in a second phase, the second phase substantially opposite the first, the transmitting area 52 entirely surrounding and in contact with each of the transmitting windows 50 of the first plurality; wherein each transmitting window is separated from its nearest neighbor in the plurality by an unbroken length of transmitting area having at least a first dimension D1, and wherein the smallest dimension D1 of each window is no more than about 160 percent of the first dimension D1. As described earlier, because the window 50 is a closed feature, its interior receives less overall light than does the transmitting area 52 between windows. For this reason dimension D1 can be larger than first dimension D1 while window 50 is nonprinting and transmitting area 52 is printing. It will be evident that the smallest dimension D1 of each window is measured in a plane parallel to the plane of the photomask.

Monolithic three dimensional memory arrays such as the one taught in Herner et al., U.S. patent application Ser. No. 10/326,470, “An Improved Method for Making High Density Nonvolatile Memory,” filed Dec. 19, 2002, hereby incorporated by reference, include a plurality of substantially evenly spaced pillars. These pillars can comprise polycrystalline silicon, called polysilicon. The pillars are portions of memory cells, and the memory cells formed in the same patterning steps generally form a portion of a memory level at a first height above a substrate. Such a monolithic three dimensional memory array further comprises at least a second memory level formed at a second height above the substrate, the second height different from the first height.

The substantially evenly spaced pillars of Herner et al. have a pitch of between about 220 nm and 280 nm, preferably about 260 nm, and are patterned, for example, using light having a wavelength of 248 nm. A photomask according to the present invention having a quadrupole aperture is highly effective for patterning regularly spaced pillars.

It will be recalled that light is projected through a photomask having a photomask feature to create a corresponding photoresist feature. The photoresist feature is then processed, typically by etching, to create a patterned feature. Pillars, for example the pillars of Herner et al., are the patterned features created from the photomask features 50 of FIG. 15.

The photomask of FIG. 15 can advantageously be used to pattern the pillars of Herner et al. When used for this purpose, referring to FIG. 15, photomask feature dimension D1 is between about 50 nm×S and about 160 nm×S, preferably between about 90 nm×S and about 140 nm×S, most preferably between about 130 and 140 nm×S. The shortest dimension of the transmitting window 50 is no more than about 160 nm×S. Photomask feature dimension D2, then, is between about 210 nm×S and about 100 nm×S, preferably between about 170 nm×S and about 120 nm×S, most preferably between about 130 nm×S and 120 nm×S. With proper exposure dose, these photomask dimensions should produce photoresist features having a width of about 130 nm and separated by a gap of about 130 nm. Dose varies from design to design, photomask to photomask, and machine to machine, and it is routine to for some experimentation to be required to identify optimum dose. Preferably the width of the photoresist features is no more than about 150 nm, and the gap is no less than about 110 nm. The photoresist features are then etched to form the patterned features, which will be pillars, as described in Herner et al. As noted in Herner et al., while the masked feature is rectangular, the cross-section of the patterned feature will tend to be substantially cylindrical. The dimensions given here assume that the light has a wavelength of 248 nm.

It will also be understood by those skilled in the art that, depending on the materials etched and the etch processes used, photoresist feature dimensions may not be the same as actual patterned feature dimensions. It is routine to adjust exposure dose and etch processes to achieve optimum results.


A monolithic three dimensional memory array is one in which multiple memory levels are formed above a single substrate, such as a wafer, with no intervening substrates. The layers forming one memory level are deposited or grown directly over the layers of an existing level or levels. In contrast, stacked memories have been constructed by forming memory levels on separate substrates and adhering the memory levels atop each other, as in Leedy, U.S. Pat. No. 5,915,167, “Three dimensional structure memory.” The substrates may be thinned or removed from the memory levels before bonding, but as the memory levels are initially formed over separate substrates, such memories are not true monolithic three dimensional memory arrays.

These monolithic three dimensional memory arrays are highly dense structures. Thus photomasks made according to the present invention can advantageously be
used to pattern any of the lines, pillars, or other tightly-packed structures formed at any level of these arrays.

[0100] It will be evident, however, that the techniques described herein are in no way limited to three dimensional memory arrays, and can be used to pattern any fine features, including conventional two-dimensional and non-memory devices.

[0101] The examples provided in this description of photomasks formed according to the present invention have largely depicted regular patterns, for example evenly spaced rectangles of uniform size. Regular patterns offer the advantage that it is simpler to determine exactly what size photomask feature and exposure dose are required to create a patterned feature of a desired size; once the determination is made, it applies to all of the elements in the pattern. Nonetheless, the methods and photomasks of the present invention can be applied to features of any shape, and the patterned features need not be uniformly sized or spaced.

[0102] An area of a photomask can be described as having a shifting degree. A shifting degree describes the shifting characteristics of a transmitting area of a photomask. If light transmitted through an area of a photomask is not phase shifted, for example, that area has a shifting degree of about zero degrees. If light transmitted through an area of a photomask is phase shifted to the opposite phase, that area has a shifting degree of about 180 degrees.

[0103] To summarize, FIG. 15 shows a phase shifting photomask comprising a transmitting nonprinting window 50 having a first shifting degree; a second transmitting area 52 having a second shifting degree, the second transmitting area 52 entirely surrounding and in contact with the first transmitting window 50, wherein the second transmitting area 52 is printing on all sides of the transmitting window 50, and wherein the second shifting degree is substantially opposite the first shifting degree.

[0104] Photomasks formed according to the present invention have been described which include first transmitting windows transmitting light in a first phase surrounded by a second transmitting area transmitting light in a second phase, the second phase substantially opposite the first phase. In the examples provided, no blocking material separated the first transmitting windows from the second transmitting area. It should be noted, however, that photomask features with no blocking material formed according to the present invention can be combined in a single photomask with conventional photomask features that include blocking material. More specifically, the presence of blocking material in any part of a photomask does not preclude it from falling within the scope of the invention.

[0105] The foregoing detailed description has described only a few of the many forms that this invention can take. For this reason, this detailed description is intended by way of illustration, and not by way of limitation. It is only the following claims, including all equivalents, which are intended to define the scope of this invention.

What is claimed is:

1. A phase shifting photomask comprising:
   a plurality of transmitting nonprinting windows transmitting light in a first phase;
   a transmitting area transmitting light in a second phase, each transmitting window substantially entirely surrounded by and in contact with the transmitting area with no blocking material intervening,
   wherein the second phase is substantially opposite the first phase, and
   wherein a first width of unbroken transmitting area surrounds each transmitting window on all sides, the first width sufficient for the unbroken transmitting area to print when the photomask is used to expose photoresist.

2. The photomask of claim 1 wherein the first phase is about 180 degrees and the second phase is about 0 degrees.

3. The photomask of claim 2 wherein the shortest dimension of any of the plurality of nonprinting transmitting windows parallel to the plane of the photomask is no more than about 160 nmS.

4. The photomask of claim 3 wherein the shortest dimension of any of the plurality of nonprinting transmitting windows parallel to the plane of the photomask is no more than about 120 nmS.

5. The photomask of claim 3 wherein at least one of the plurality of transmitting windows is rectangular.

6. The photomask of claim 5 wherein all of the transmitting windows are rectangular.

7. The photomask of claim 3 wherein the transmitting windows are uniformly spaced.

8. The photomask of claim 2 wherein the first width of unbroken transmitting area is at least 100 nmS.

9. The photomask of claim 3 wherein the second phase is about 0 degrees and the second phase is about 180 degrees.

10. The photomask of claim 9 wherein the shortest dimension of any of the plurality of nonprinting transmitting windows in the plane of the photomask is no more than about 160 nmS.

11. The photomask of claim 10 wherein the shortest dimension of any of the plurality of nonprinting transmitting windows in the plane of the photomask is no more than about 120 nmS.

12. The photomask of claim 9 wherein at least one of the plurality of transmitting windows is rectangular.

13. The photomask of claim 12 wherein all of the transmitting windows are rectangular.

14. The photomask of claim 9 wherein the transmitting windows are uniformly spaced.

15. The photomask of claim 9 wherein the first width of unbroken transmitting area is at least 100 nmS.

16. A phase shifting photomask comprising:
   a transmitting nonprinting window transmitting light in a first phase; and
   a transmitting area substantially entirely surrounding and in contact with the transmitting window on all sides with no blocking material intervening,
   wherein the transmitting area transmits light in a second phase, the second phase substantially opposite the first phase, and
   wherein, when used to pattern photoresist, the transmitting area is printing on all sides of the transmitting window.

17. The photomask of claim 16 wherein the first phase is about zero degrees and the second phase is about 180 degrees.
18. The photomask of claim 17 wherein a shortest dimension of the first nonprinting transmitting window parallel to the plane of the photomask is no more than about 160 nm x S.

19. The photomask of claim 17 wherein a shortest dimension of the nonprinting transmitting window parallel to the plane of the photomask is no more than about 120 nm x S.

20. The photomask of claim 17 wherein a width of the transmitting area on all sides of the transmitting window is at least 100 nm x S.

21. The photomask of claim 16 wherein the first phase is about 180 degrees and the second phase is about zero degrees.

22. The photomask of claim 21 wherein the shortest dimension of the nonprinting transmitting window parallel to the plane of the photomask is no more than about 160 nm x S.

23. The photomask of claim 21 wherein the shortest dimension of the nonprinting transmitting window parallel to the plane of the photomask is no more than about 120 nm x S.

24. The photomask of claim 21 wherein a width of the transmitting area on all sides of the transmitting window is at least 100 nm x S.

25. A phase shifting photomask comprising:
   a plurality of spatially separate transmitting nonprinting windows transmitting light in a first phase; and
   a transmitting area transmitting light in a second phase, the second phase substantially opposite the first, the transmitting area entirely surrounding and in contact with each of the transmitting windows of the first plurality;

wherein each transmitting window is separated from its nearest neighbor in the plurality by an unbroken length of transmitting area having at least a first dimension, and

wherein the smallest dimension of each window is no more than about 160 percent of the first dimension.

26. The photomask of claim 25 wherein the first phase is about zero degrees and the second phase is about 180 degrees.

27. The photomask of claim 26 wherein the first dimension is at least about 100 nm x S.

28. The photomask of claim 27 wherein the smallest dimension of each window is less than about 160 nm x S.

29. The photomask of claim 26 wherein the plurality of transmitting nonprinting windows are arranged in a grid pattern.

30. The photomask of claim 25 wherein the first phase is about 180 degrees and the second phase is about zero degrees.

31. The photomask of claim 30 wherein the first dimension is at least about 100 nm x S.

32. The photomask of claim 31 wherein the smallest dimension of each window is less than about 160 nm x S.

33. The photomask of claim 30 wherein the plurality of transmitting nonprinting windows are arranged in a grid pattern.

34. A phase shifting photomask comprising:
   a transmitting nonprinting window having a first shifting degree;

   a second transmitting area having a second shifting degree, the second transmitting area entirely surrounding and in contact with the first transmitting window, wherein the second transmitting area is printing on all sides of the transmitting window; and

   wherein the second shifting degree is substantially opposite the first shifting degree.

35. The photomask of claim 34 wherein the first shifting degree is about zero degrees and the second shifting degree is about 180 degrees.

36. The photomask of claim 35 wherein the shortest dimension of the second transmitting area in the plane of the photomask is no more than about 160 nm x S.

37. The photomask of claim 36 wherein the shortest dimension of the second transmitting area in the plane of the photomask is no more than about 120 nm x S.

38. The photomask of claim 35 wherein the second transmitting area has a rectangular shape.

39. The photomask of claim 34 wherein the first shifting degree is about 180 degrees and the second shifting degree is about zero degrees.

40. The photomask of claim 39 wherein the shortest dimension of the second transmitting area in the plane of the photomask is no more than about 160 nm x S.

41. The photomask of claim 40 wherein the shortest dimension of the second transmitting area in the plane of the photomask is no more than about 120 nm x S.

42. The photomask of claim 39 wherein the second transmitting area has a rectangular shape.

43. A method for forming a patterned feature on a wafer surface, the method comprising:
   transmitting light through a phase shifting photomask onto photoresist covering the wafer surface;

   forming an isolated first residual photoresist feature between a first wafer area exposed to light in a first phase and a second wafer area exposed to light in a second phase, wherein the first phase is substantially opposite the second phase, and

   wherein the second wafer area entirely surrounds the first wafer area in the plane of the wafer; and

   forming the patterned feature from the photoresist feature.

44. The method of claim 43 wherein the first phase is about zero degrees and the second phase is about 180 degrees.

45. The method of claim 44 wherein:
   the step of forming an isolated photoresist feature comprises developing photoresist, and

   the step of forming the patterned feature comprises etching, and

   the first residual photoresist feature defines a closed shape having a perimeter, and,

   after the developing step and before the etching step, no portion of the wafer surface is exposed within the perimeter.

46. The method of claim 45 wherein the shortest dimension of the photoresist feature measured in the plane of the wafer surface is no greater than about 150 nm.
47. The method of claim 44 further comprising forming a plurality of residual photoresist features, wherein each photoresist feature of the plurality is exposed to light in the first phase, and each photoresist feature of the plurality is entirely surrounded by the second wafer area.

48. The method of claim 47 wherein each of the plurality of photoresist features defines a closed shape having a perimeter, and, after the developing step and before the etching step, no portion of the wafer surface is exposed within the perimeter.

49. The method of claim 48 wherein the plurality of photoresist features is uniformly spaced.

50. The method of claim 48 wherein the plurality of photoresist features is randomly spaced.

51. The method of claim 48 wherein a patterned feature is formed on the wafer surface from each of the plurality of photoresist features, and wherein the patterned features are portions of memory cells forming a first memory level in a memory array, the first memory level formed at a first height above a substrate.

52. The method of claim 51 wherein the memory array is a monolithic three dimensional memory array, the array further comprising at least a second memory level formed at a second height above the substrate, the second height different from the first height.

53. The method of claim 43 wherein the first phase is about 180 degrees and the second phase is about zero degrees.

54. The method of claim 53 wherein:

- the step of forming an isolated photoresist feature comprises developing photoresist, and
- the step of forming the patterned feature comprises etching, and
- the first residual photoresist feature defines a closed shape having a perimeter, and, after the developing step and before the etching step, no portion of the wafer surface is exposed within the perimeter.

55. The method of claim 54 wherein the shortest dimension of the photoresist feature measured in the plane of the wafer surface is no greater than about 150 nm.

56. The method of claim 55 further comprising forming a plurality of residual photoresist features, wherein each photoresist feature of the plurality is exposed to light in the first phase, and each photoresist feature of the plurality is entirely surrounded by the second wafer area.

57. The method of claim 56 wherein each of the plurality of photoresist features defines a closed shape having a perimeter, and, after the developing step and before the etching step, no portion of the wafer surface is exposed within the perimeter.

58. The method of claim 57 wherein the plurality of photoresist features is uniformly spaced.

59. The method of claim 57 wherein the plurality of photoresist features is randomly spaced.

60. The method of claim 57 wherein a patterned feature is formed on the wafer surface from each of the plurality of photoresist features, and wherein the patterned features are portions of memory cells forming a first memory level in a memory array, the first memory level formed at a first height above a substrate.

61. The method of claim 60 wherein the memory array is a monolithic three dimensional memory array, the array further comprising at least a second memory level formed at a second height above the substrate, the second height different from the first height.

62. A method for forming photoresist features on a wafer surface using a photomask, the method comprising:

- transmitting light through a first mask area onto a first wafer area, the first mask area having a first shifting degree;
- transmitting light through a second mask area onto a second wafer area, the second mask area having a second shifting degree,

wherein the second mask area entirely surrounds and is on all sides in contact with the first mask area, and the first shifting degree is substantially opposite the second shifting degree; and developing photoresist, wherein, after the developing step, a closed residual photoresist feature remains between the first wafer area and the second wafer area, and wherein the closed residual photoresist feature is isolated and not merged with any adjacent photoresist feature.

63. The method of claim 62 wherein the first shifting degree is about zero degrees and the second shifting degree is about 180 degrees.

64. The method of claim 63 wherein, after the developing step, no wafer surface is exposed within the first wafer area.

65. The method of claim 62 wherein the first shifting degree is about 180 degrees and the second shifting degree is about zero degrees.

66. The method of claim 65 wherein, after the developing step, no wafer surface is exposed within the first wafer area.

67. A monolithic three dimensional memory array comprising:

- a plurality of patterned features, the plurality of patterned features patterned using a photomask comprising:
- a plurality of spatially separate first transmitting windows, wherein the transmitting windows transmit light in a first phase; and
- a transmitting area of the photomask, each transmitting window substantially surrounded by and in contact with the transmitting area,

wherein the transmitting area transmits light in a second phase, the second phase substantially opposite the first phase.

68. The monolithic three dimensional memory array of claim 67, wherein the patterned features comprise substantially coplanar pillars.

69. The monolithic three dimensional memory array of claim 68 wherein the pillars have a diameter no more than about 150 nm.