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(54) Title: METHOD AND APPARATUS FOR IMPROVED ESD PERFORMANCE

(57) Abstract: The present invention provides an integrated circuit for improved ESD protection and method of forming the same. The integrated circuit comprises a substrate and an insulating layer formed over the substrate. The circuit also comprises a field effect field effect transistor (FET) formed over the insulating layer. The FET includes a well region of a first conductivity type. The circuit also includes a well resistor coupled to the FET to provide ballasting to the circuit. The well resistor includes a well region also of the first conductivity type.

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US06/23142

| A. CLASSIFICATION OF SUBJECT MATTER IPC: H01L 29/76(2006.01) | | | | |
|---|--|--|---|--|
| USPC: 257/327 According to International Patent Classification (IPC) or to both national classification and IPC | | | | |
| B. FIELDS SEARCHED | | | | |
| Minimum documentation searched (classification system followed by classification symbols) U.S.: 257/327 | | | | |
| Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched | | | | |
| Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) | | | | |
| C. DOCT | JMENTS CONSIDERED TO BE RELEVANT | | | |
| Category * | Citation of document, with indication, where a | ppropriate, of the relevant passages | Relevant to claim No. | |
| Y | US 6,909,143 (JEON et al) 9 April 2004 (09.04.200 | 4) Fig. 3 | 1-10 | |
| Y | US 2003/0227058 (WANG et al) 26 November 200 | 02 (26.12.2002) Fig. 4,19 | 1-10 | |
| | | | | |
| Further | documents are listed in the continuation of Box C. | See patent family annex. | | |
| Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance | | date and not in conflict with the applic | later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention | |
| • | lication or patent published on or after the international filing date | | document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone | |
| 'L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) | | considered to involve an inventive step | document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being | |
| "O" document | referring to an oral disclosure, use, exhibition or other means | obvious to a person skilled in the art | | |
| priority date claimed | | "&" document member of the same patent family | | |
| Date of the actual completion of the international search | | Date of mailing of the international search report | | |
| 30 June 2007 (30.06.2007) | | Authorized officer | | |
| Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 | | Authorized officer Bradley Baumeister William Old The Telephone No. 571-272-1722 | | |
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Form PCT/ISA/210 (second sheet) (April 2005)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US06/23142

| Box No. II | Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet) | | |
|--|---|--|--|
| This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons: | | | |
| 1. | Claims Nos.: because they relate to subject matter not required to be searched by this Authority, namely: | | |
| 2. | Claims Nos.: because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically: | | |
| 3. | Claims Nos.: because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a). | | |
| Box No. II | Observations where unity of invention is lacking (Continuation of item 3 of first sheet) | | |
| | tional Searching Authority found multiple inventions in this international application, as follows: Continuation Sheet . | | |
| 1. | As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims. As all searchable claims could be searched without effort justifying additional fees, this Authority did not invite payment of any additional fees. As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.: | | |
| 4. 🔀 Remark on F | No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.: 1-10 Protest | | |

Form PCT/ISA/210 (continuation of first sheet(2)) (April 2005)

International application No. INTERNATIONAL SEARCH REPORT PCT/US06/23142 BOX III. OBSERVATIONS WHERE UNITY OF INVENTION IS LACKING This application contains the following inventions or groups of inventions which are not so linked as to form a single general inventive concept under PCT Rule 13.1. In order for all inventions to be examined, the appropriate additional examination fees must be paid. Claims 19-20, drawn to a method of improving ESD robustness of a FET, classified in class 438. I. Claims 1-18, drawn to an integrated circuit, classified in class 257. II. The inventions listed as Groups I and II do not relate to a single general inventive concept under PCT Rule 13.1 because, under PCT Rule 13.2, they lack the same or corresponding special technical features for the following reasons: the process claimed could make a materially different product. Specifically the FET could be placed directly on the substrate with no insulating layer. This application contains claims directed to more than one species of the generic invention. These species are deemed to lack unity of invention because they are not so linked as to form a single general inventive concept under PCT Rule 13.1. In order for more than one species to be examined, the appropriate additional examination fees must be paid. The species are as follows: The claims are deemed to correspond to the species listed above in the following manner: The device of invention II, claims 1-10, wherein a first well region is of a second conductivity type. i. The device of invention II, claims 11-18, wherein a first well region is of a first conductivity type. ii. The species listed above do not relate to a single general inventive concept under PCT Rule 13.1 because, under PCT Rule 13.2, the

species lack the same or corresponding special technical features for the following reasons: The common special technical features of

Inventions i and ii are disclosed in view of Jeon '143/Wang '058.