



(51) International Patent Classification:

G06F 21/73 (2013.01) H04L 9/32 (2006.01)
G06F 21/44 (2013.01)

(21) International Application Number:

PCT/EP2012/076212

(22) International Filing Date:

19 December 2012 (19.12.2012)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

12305058.5 16 January 2012 (16.01.2012) EP

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM,

DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

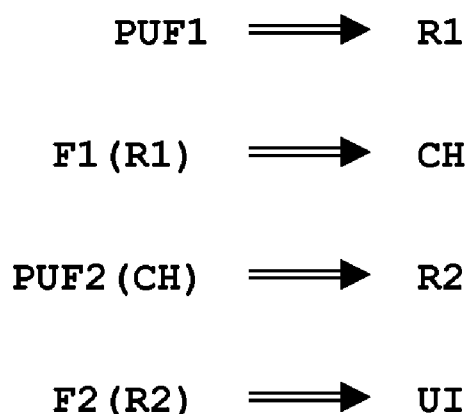
Declarations under Rule 4.17:

— of inventorship (Rule 4.17(iv))

Published:

— with international search report (Art. 21(3))

(54) Title: ELECTRONIC DEVICE GENERATING AN IDENTIFIER BASED ON PHYSICAL UNCLONABLE FUNCTIONS



(57) Abstract: The invention is an electronic device comprising first and second hardware components. The electronic device comprises a first means adapted to run a first physical unclonable function able to generate a first response representative of said first hardware component. The electronic device comprises a second means adapted to run a second physical unclonable function able to generate a second response representative of said second hardware component, said second physical unclonable function requiring a challenge as input parameter, said electronic device comprising a chaining means able to generate the challenge from said first response and able to provide second means with said challenge. The electronic device comprises a generating means adapted to generate an identifier of the electronic device from said second response.

FIG.3

ELECTRONIC DEVICE GENERATING AN IDENTIFIER BASED ON PHYSICAL UNCLONABLE FUNCTIONS

(Technical field)

The present invention relates to the methods of generating an identifier of an electronic device. It relates particularly to methods of generating an identifier of a device comprising a plurality of hardware components.

(Background)

A Physical Unclonable Function (PUF) is a function that is embodied in a physical structure and is easy to evaluate but extremely difficult to predict. An individual PUF component must be easy to make but practically impossible to duplicate, even given the exact manufacturing process that produced it. When a physical stimulus is applied to the structure, it reacts in an unpredictable way due to the complex interaction of the stimulus with the physical microstructure of the component. This exact microstructure depends on physical factors introduced during manufacture which are unpredictable. The applied stimulus is called the challenge, and the reaction of the PUF is called the response. A specific challenge and its corresponding response together form a challenge-response pair which is specific to a given component. Such a challenge-response pair is assumed to be steady. The identity of the component is established by the properties of the microstructure itself.

It is known to authenticate one component of a device by using the result of a PUF execution. For instance, the plastic body of a smart card may be authenticated via a PUF as explained in EP0583709-A1 publication. Such an authentication is limited to the identifying of a single component belonging to a device.

There is a need for increasing the authentication of an electronic device.

(Summary of the Invention)

An object of the invention is to solve the above mentioned technical problem.

The object of the present invention is a method for generating an identifier of an electronic device. The electronic device comprises first and second hardware components. The method comprises the following step:

- identifying a first physical unclonable function able to generate a first response representative of said first hardware component,
- identifying a second physical unclonable function able to generate a second response representative of said second hardware component, and
- computing the identifier of the electronic device by using said first and second responses.

Advantageously, the second physical unclonable function requires a challenge as input parameter and this challenge may be generated from said first response. The identifier may be generated from said
5 second response.

Advantageously, the identifier may be generated by a third function which takes said first and second responses as input parameters.

Said third function may be a hash of the
10 concatenation of said first and second responses.

Advantageously, the method may comprise the step of authenticating the electronic device by using said identifier.

Advantageously, the method may comprise the step
15 of using said identifier as cryptographic key.

The step of computing the identifier may be performed into the electronic device or into a machine distinct from the electronic device.

Another object of the invention is an electronic
20 device comprising first and second hardware components. The electronic device comprises a first means which is adapted to run a first physical unclonable function able to generate a first response representative of said first hardware component. The electronic device
25 comprises a second means adapted to run a second physical unclonable function able to generate a second response representative of said second hardware component. The electronic device comprises a computing means adapted to compute an identifier of the
30 electronic device by using said first and second responses.

Advantageously, said second physical unclonable function may require a challenge as input parameter and the electronic device may comprise a chaining means adapted to generate the challenge from said first response. The chaining means may be adapted to provide said second means with the challenge. The electronic device may comprise a generating means adapted to generate the identifier from said second response.

Advantageously, the computing means may be adapted to run a hash function which takes said first and second responses as input parameters and generates the identifier.

Another object of the invention is a system comprising a machine and an electronic device. The electronic device comprises first and second hardware components. The system comprises a first means adapted to run a first physical unclonable function able to generate a first response representative of said first hardware component. The system comprises a second means adapted to run a second physical unclonable function able to generate a second response representative of said second hardware component. The system comprises a computing means adapted to compute an identifier of the electronic device by using said first and second responses.

(Brief description of the drawings)

Other characteristics and advantages of the present invention will emerge more clearly from a reading of the following description of a number of

preferred embodiments of the invention with reference to the corresponding accompanying drawings in which:

- Figure 1 depicts schematically an example of a system comprising an electronic device according to a first embodiment of the invention;

- Figure 2 is an example of a step sequence for generating an identifier of the device according to a one embodiment of the invention;

- Figure 3 is another example of a step sequence for generating an identifier of the device according to another embodiment of the invention; and

- Figure 4 depicts schematically an example of a system comprising an electronic device according to a second embodiment of the invention.

(Detailed description of the preferred embodiments)

The invention may apply to any types of electronic devices comprising several hardware components which may be identified thanks to a PUF. In the following examples, the electronic device is a smart card but it could be any other kind of device. In particular, the electronic device may be a personal computer, a tablet, a mobile phone, a mass storage USB token or a secure element connected to a host machine.

The present invention relies on the combination of a plurality of PUF in order to identify a device thanks to a set of hardware components belonging to the device. The invention relies on the fact that a physical unclonable function generates a result which

is representative of a corresponding hardware component.

The invention allows guaranteeing that several hardware components which are assumed to belong to a single device have neither been dismantled nor replaced.

Figure 1 shows a first system SY1 comprising an electronic device D1 connected to a machine MA1 according to a first embodiment of the invention.

According to the system SY1, an identifier UI of the device D1 is computed into the device D1 itself.

The electronic device D1 is a contactless smart card and comprises a non volatile memory MEM, a microprocessor MP, a communication interface IN and two hardware components H1 and H2. The non volatile memory MEM comprises an operating system OS which may contain a Virtual Machine.

The hardware component H1 may be an antenna allowing the device D1 to communicate through a contactless protocol. The hardware component H2 may be a working memory of RAM (Random-Access memory) type.

The device D1 is intended to exchange data with the machine MA1 through the communication interface IN.

The memory MEM may be implemented as any combinations of one, two or more memories. These memories may be any kind of non volatile memory (for example: NAND flash or EEPROM memory).

The non volatile memory MEM comprises five means M1 to M5.

The means M1 is adapted to run a first physical unclonable function (called PUF1) able to generate a

response R1 which is representative of the hardware component H1. The means M2 is adapted to run a second physical unclonable function (called PUF2) able to generate a response R2 that is representative of the hardware component H2.

The means M3 (also called computing means) is adapted to compute an identifier UI of the electronic device D1 by using the responses R1 and R2.

The means M4 (also called chaining means) is adapted to generate a challenge CH from the response R1 and is adapted to provide the means M2 with the generated challenge CH.

The means M5 (also called generating means) is adapted to generate the identifier UI of the electronic device D1 from the response R2.

The machine MA1 is a personal computer having a smart card reader (not drawn). The machine MA1 comprises a reference RE and a means M6. The means M6 is adapted to compute cryptographic treatment. For example, the means M6 may be able to authenticate the device D1 by using both the generated identifier UI and the reference RE. Alternatively, the means M6 may be able to use the generated identifier UI as a key for secured treatments. For instance, the means M6 may be able to establish a secure session with the device D1 by using the reference RE while the device D1 is able to complete the secure session establishment by using the generated identifier UI.

Figure 2 shows a sequence of steps for generating an identifier UI of the device according to a first example of the invention.

At a first step, the means M1 runs the PUF1 in order to generate the response R1. An example of PUF1 is described later in this document. The means M2 runs the PUF2 in order to generate the response R2. Note that the execution of PUF1 and PUF2 may be sequentially performed. If needed, preset values may be used as challenge for PUF1 and PUF2. These preset values may be provided either by the machine MA1 or by stored into the device D1.

At a second step, the identifier UI of the device D1 is generated by a computing means M3 which run the function F3 that takes R1 and R2 as input parameters. For instance, the function F3 may concatenate R2 and R1 and compute a hash of this concatenated string. Alternatively, the function F3 may compute the encryption of R2 under R1 (e.g. R1 is used as a key) or reversely. The encryption may be run with any relevant algorithm, like DES (Data Encryption Standard) for instance.

Figure 3 shows a sequence of steps for generating an identifier UI of the device according to a second example of the invention.

At a first step, the means M1 runs the PUF1 in order to generate the response R1.

At a second step, the chaining means M4 generates a challenge CH from the response R1. For example, the chaining means M4 may just set the value of the challenge CH with the value of the response R1. The

chaining means M4 may also generate CH thanks to a function F1 which may be a hash function for example. Alternatively the function F1 may encrypt a preset pattern with the response R1 used as a key and vice versa.

At a third step, the means M2 runs the PUF2 in order to generate the response R2 by using the challenge CH as input parameter.

At a fourth step, the identifier UI of the device D1 is generated by a function F2 which takes R2 as input parameter. For instance, the function F2 may compute a hash of the response R2. Alternatively, the function F2 may compute the encryption of R2 under a predefined pattern (which is used as a key).

Note that the functions F1 and F2 may be any suitable functions which generate a value from an input parameter in a predictable manner.

The invention is not limited to the use of two hardware components. The device identifier UI may be generated by using three or more hardware components and as many related physical unclonable functions.

For a smart card, the hardware components may be the body (which carries the chip), the resin used for coating the chip onto the body, the RAM memory, the non volatile memory or any suitable physical components. For contactless smart cards, one of the hardware components may be the antenna.

The PUF of a chip may be computed as a silicon PUF. In particular, this kind of PUF may be computed as a delay-PUF (such as an Arbiter-PUF) that exploits the variation in delay of wires and gates of a chip. For

instance, a silicon PUF delay circuit may be based on several multiplexers and an arbiter where two pulses race on two delay paths. The arbiter detects the faster pulse.

5 The PUF of the resin may be computed as a coating PUF wherein several locations of resistive points are to be measured.

 The PUF of the antenna may be computed by measuring the capacitance or inductive behavior of the
10 antenna under certain frequency conditions. The PUF may also be computed by measuring the resonance frequency while transmitting a specific signal pattern for example.

 The PUF of the body may be computed as a surface
15 authentication, by measuring objects randomly (or not) spread out.

 The PUF of a SRAM (Static Random-Access memory) body may be computed according to the default state of the memory when the SRAM component is started. Such a
20 PUF is based on the uncertainty of the value of each bit of a SRAM memory when the SRAM component is powered. It has been shown that a number of memory points, among all the memory, have the same value for each start of a same component while being different
25 from one component to another within the same family. Additionally, a PUF memory-based may exploit this feature within the values of some or all memory points of the SRAM component and select a set of memory points depending on a challenge.

For instance the identifier UI of a smart card device may be computed according to the following sequence. First a PUF of the chip is computed. Then the result of said first PUF is used for identifying a set of resistive points to be measured on the resin. Accordingly, the PUF of the resin is computed and generates a second result. Said second result is used for generating a signal pattern to be transmitted in contactless mode to the device D1 by the connected machine. (Assuming that the second result is previously sent to the machine by the device D1). Then the PUF of the antenna is computed and generates a third result. Said third result is used for identifying a set of surface points to be measured on the body of the card. The PUF of the body is then measures and a fourth result is generated. The identifier is then computed as the result of a hash function of said fourth result.

Thus each PUF to be run may be performed by a means located either in the device or in the connected machine, provided that the challenges or the responses are exchanged between the device and the machine when needed.

It is to be noted that depending on the order of the PUF, several identifier values may be generated for a unique device. In other words, the chaining of PUF generates different identifiers whose value depends on the order of the used PUF.

Once the identifier UI has been generated into the device D1, the identifier UI may be sent to the machine MA1 for an authentication step. Advantageously, the identifier UI may be ciphered before sending or

sent through a secured channel previously established. The machine MA1 may use a reference RE in order to authenticate the device D1. For example, the means M6 may compare the values of the reference RE and the value of the received identifier UI. In case of
5 successful comparison, the device D1 is considered as authenticated.

In another scheme, the identifier UI may be kept by the device D1 without sending to the machine MA1. In
10 this case, the machine MA1 may try to establish a secured session with the device D1 by using the reference RE as a secret key. The device D1 is assumed to complete the establishment of the secured session by using the computed identifier UI. Alternatively, the
15 machine MA1 may cipher data by using the reference RE as a secret key and the device D1 may decipher the received data by using the computed identifier UI as a secret key.

Figure 4 shows a second system SY2 comprising an
20 electronic device D2 connected to a machine MA2 according to a second embodiment of the invention.

According to the system SY2, the identifier of the device D2 is computed outside the device D2.

The electronic device D2 of Figure 4 is similar
25 to the electronic device D1 of Figure 1 with the following differences: the electronic device D2 does not comprise the means M1 to M5.

The machine MA2 of Figure 4 is similar to the machine MA1 of Figure 1 with the following differences:
30 the machine MA2 comprises the means M7 to M11 and a reference RE2 corresponding to the device D2.

The means M7 is similar to the means M1 of Figure 1. The means M7 is adapted to run PUF1 to generate a response R1 which is representative of the hardware component H1.

5 The means M8 is similar to the means M2 of Figure 1. The means M8 is adapted to run PUF2 to generate a response R2 that is representative of the hardware component H2.

10 The means M9 (also called computing means) is similar to the means M3 of Figure 1. The means M9 is adapted to compute an identifier of the electronic device D1 by using the responses R1 and R2.

15 The means M10 (also called chaining means) is similar to the means M4 of Figure 1. The means M10 is adapted to generate a challenge CH from the response R1 and is adapted to provide the means M8 with the generated challenge CH.

20 The means M11 (also called generating means) is similar to the means M5 of Figure 1. The means M11 is adapted to generate the identifier of the electronic device D1 from the response R2.

25 The machine MA2 also comprises a means M6 similar to the corresponding means of Figure 1. The means M6 is adapted to compute cryptographic treatment. For example, the means M6 may be able to authenticate the device D2 by using both the reference RE2 and the identifier generated for the device D2.

30 The invention may apply to any kinds of electronic devices made of several hardware components, like an ePassport, a USB token, a smartphone or a laptop. For example, the invention may apply to

personal computer and the identifier generated according to the invention may be the combination of PUF related to the screen, the keyboard, the CPU, the RAM and the Hard Drive. According to another example,
5 the invention may apply to tablet computer and the identifier which is generated according to the invention may be the combination of PUF related to the main processor, the graphic processor, the Wifi chip, the Wifi antenna, the Bluetooth ® chip, the keyboard,
10 and the non volatile memory.

The invention allows authenticating the genuineness of an electronic device made of several hardware components which must be preserved in its entirety. The identifier of the device may be generated
15 from a combination of a subset of hardware components which have an available PUF (at least two components). Advantageously, the identifier of the device may be generated from a combination of all hardware components having an available PUF.

20 The invention allows authenticating the genuineness of an electronic device made of hardware components and of a removable hardware component, like a smart card or a SD ® card. For instance, the invention may apply to a mobile phone and the
25 identifier of the mobile phone may be generated as a combination of PUF related to the screen, the Telecom antenna and a plugged SIM or UICC (Universal Integrated Circuit Card).

CLAIMS

1. An electronic device (D1) comprising first and second hardware components (H1, H2), said electronic device (D1) comprising a first means (M1) adapted to run a first physical unclonable function (PUF1) able to generate a first response (R1) representative of said first hardware component (H1),

characterized in that said electronic device (DE) comprises a second means (M2) adapted to run a second physical unclonable function (PUF2) able to generate a second response (R2) representative of said second hardware component (H2), in that in that said second physical unclonable function (PUF2) requires a challenge (CH) as input parameter, in that said electronic device (D1) comprises a chaining means (M4) able to generate the challenge (CH) from said first response (R1) and able to provide second means (M2) with said challenge (CH) and in that the electronic device (D1) comprises a generating means (M5) adapted to generate an identifier (UI) of the electronic device (DE) from said second response (R2).

2. An electronic device (D1) according to claim 1, wherein one of said first and second hardware components (H1, H2) is an electronic component while the other is a non-electronic component.

3. An electronic device (D1) according to claim 2, wherein the electronic component belongs to a group

comprising memory, antenna and chip and wherein the non-electronic component belongs to a set comprising smart card body and resin.

- 5 4. A system (SY1, SY2) comprising a machine (MA1, MA2) and an electronic device (D1, D2),
characterized in that said electronic device (D1, D2) is the electronic device (D1, D2) of claim 1, in
that the electronic device (D1, D2) is configured to
10 send said first response (R1) to the machine (MA1, MA2)
in that the machine (MA1, MA2) is configured to
generate a signal pattern from said first response (R1)
and to send the signal pattern to the electronic device
(D1, D2), and in that the second means (M2) is
15 configured to run said second physical unclonable
function (PUF2) by using the signal pattern.

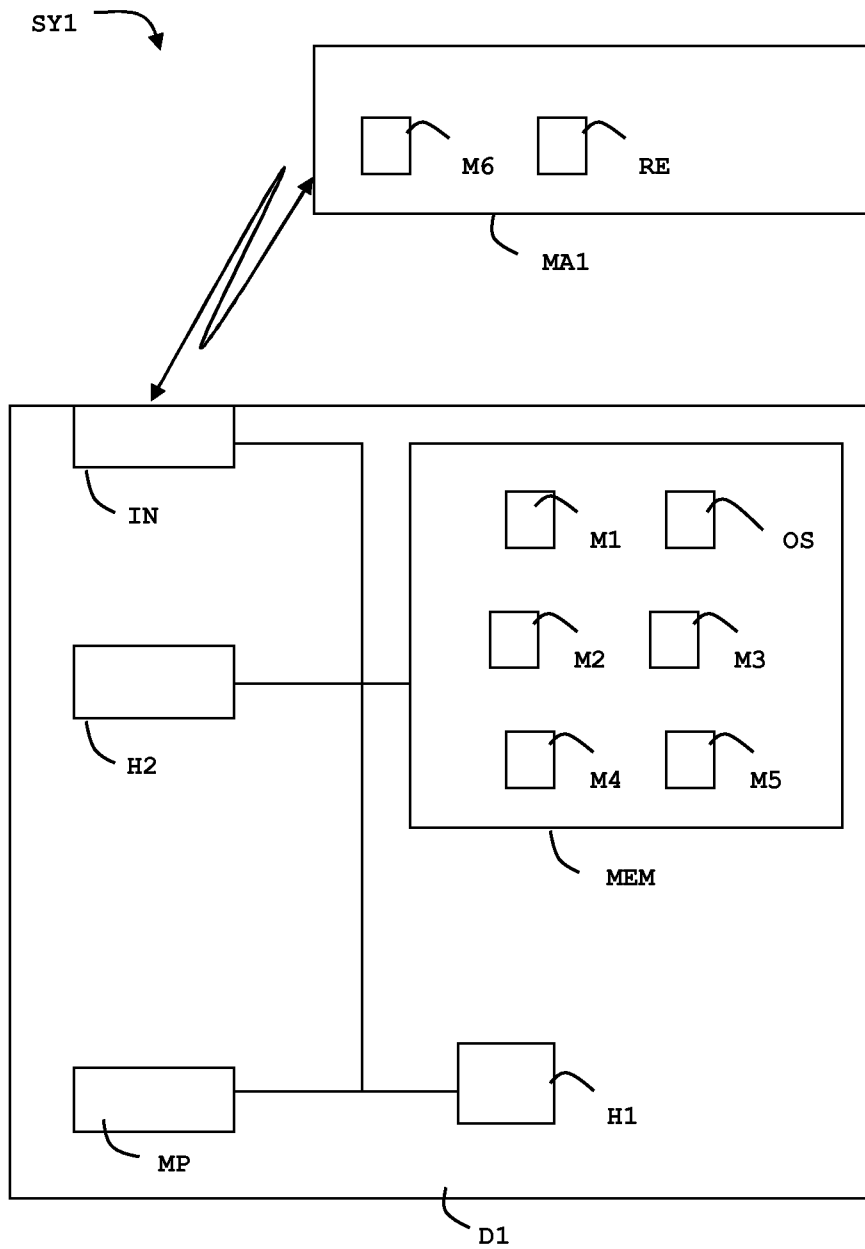


FIG.1

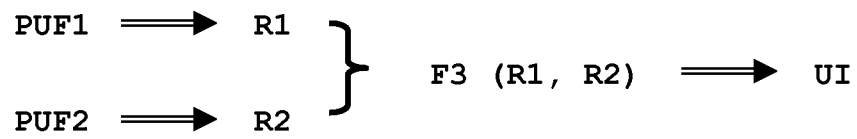


FIG.2

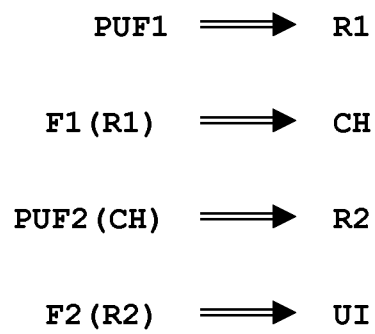


FIG.3

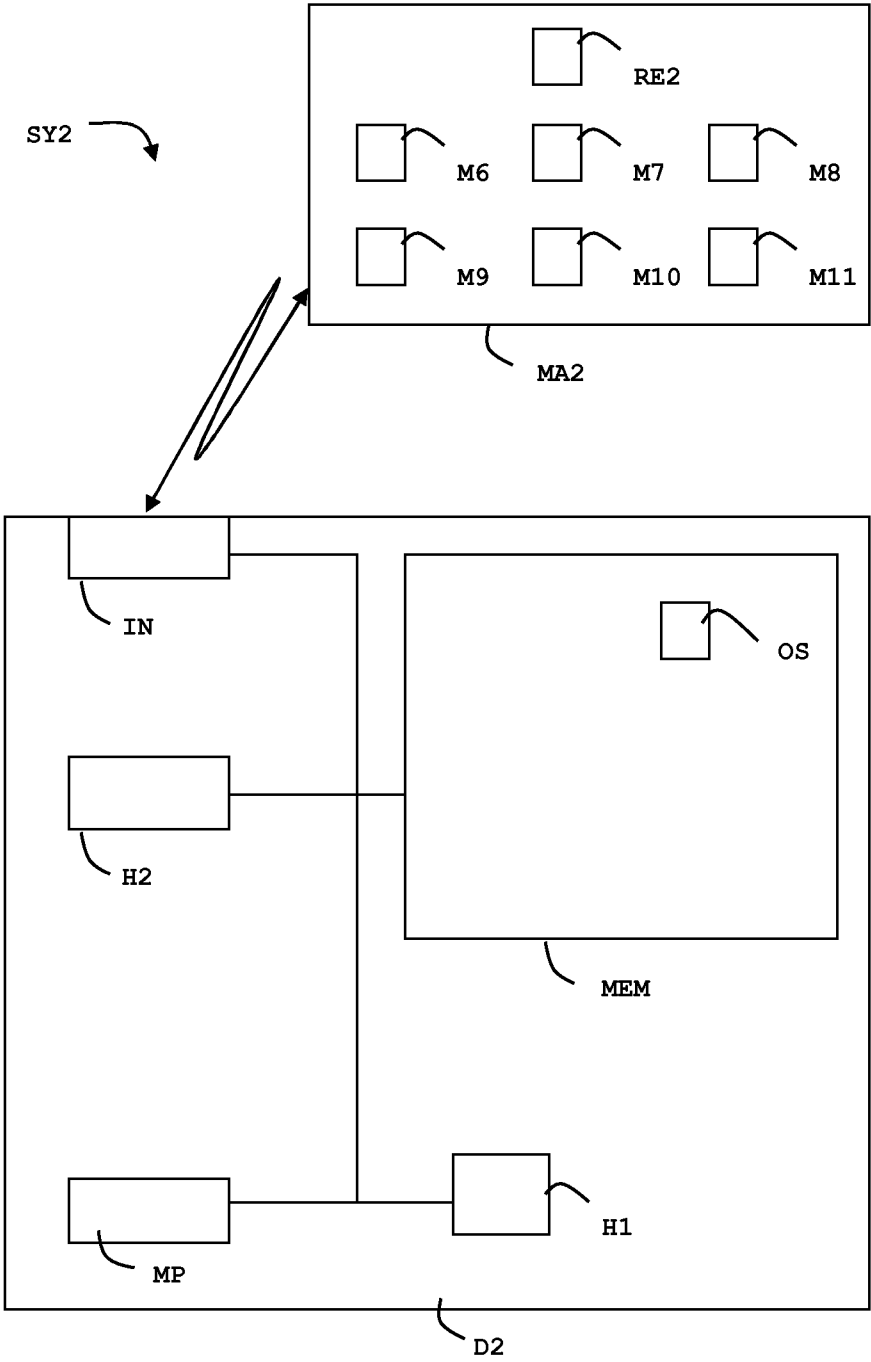


FIG.4

INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2012/076212

A. CLASSIFICATION OF SUBJECT MATTER

INV. G06F21/73 G06F21/44 H04L9/32
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G06F H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2007/063473 A1 (KONINKL PHILIPS ELECTRONICS NV [NL]; SKORIC BORIS [NL]; BRUEKERS ALPHO) 7 June 2007 (2007-06-07)	1-3
A	page 2, line 27 - page 3, line 6 page 4, line 9 - page 4, line 25 page 5, line 15 - page 5, line 28 page 6, line 17 - page 6, line 34 figure 1	4
X	US 7 898 283 B1 (KOUSHANFAR FARINAZ [US] ET AL) 1 March 2011 (2011-03-01)	1-3
A	column 3, line 18 - column 3, line 29 column 4, line 10 - column 4, line 29 column 6, line 1 - column 6, line 34 figures 1, 3	4
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Further documents are listed in the continuation of Box C.



See patent family annex.

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Date of the actual completion of the international search

6 March 2013

Date of mailing of the international search report

14/03/2013

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
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Volpato, Gian Luca

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2012/076212

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2003/204743 A1 (DEVADAS SRINIVAS [US] ET AL CLARKE DWAIN [BB] ET AL) 30 October 2003 (2003-10-30) paragraphs [0007], [0056] - [0058], [0169], [0291] -----	1-4
A	US 2009/083833 A1 (ZIOLA THOMAS [US] ET AL) 26 March 2009 (2009-03-26) paragraphs [0005], [0006], [0020], [0032], [0034], [0045], [0064] figures 4, 5 -----	1-4

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2012/076212

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