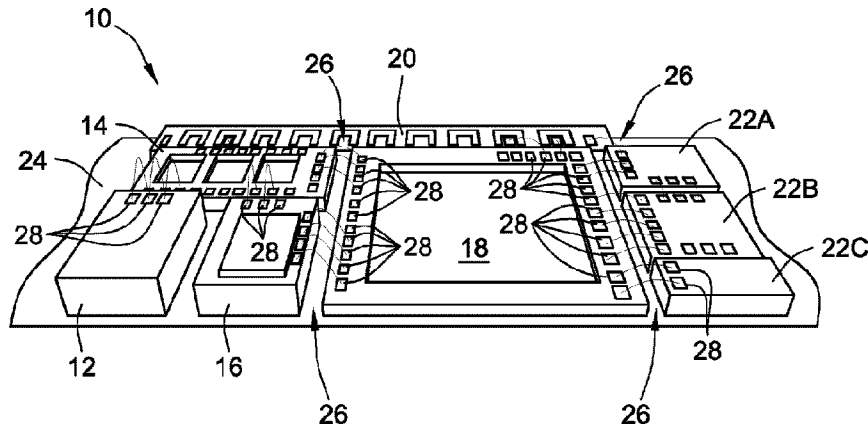




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(54) **Titre : CIRCUITS ELECTRONIQUES SOUPLES A PUCE DE CIRCUIT INTEGRE INCORPOREE, ET LEURS PROCEDES DE FABRICATION ET D'UTILISATION**  
(54) **Title: FLEXIBLE ELECTRONIC CIRCUITS WITH EMBEDDED INTEGRATED CIRCUIT DIE AND METHODS OF MAKING AND USING THE SAME**



(57) **Abrégé/Abstract:**

Flexible integrated circuit (IC) modules, flexible IC devices, and methods of making and using flexible IC modules are presented herein. A flexible integrated circuit module is disclosed which includes a flexible substrate and a semiconductor die attached to the flexible substrate. An encapsulating layer, which is attached to the flexible substrate, includes a thermoplastic resin and/or a polyimide adhesive encasing therein the semiconductor die. The encapsulating layer may be an acrylic-based thermally conductive and electrically isolating polyimide adhesive. Optionally, the encapsulating layer may be a B-stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend. The die may be embedded between two flexible substrates, each of which includes a layer of flexible polymer, such as a polyimide sheet, with two layers of conductive material, such as copper cladding, disposed on opposing sides of the layer of flexible polymer.



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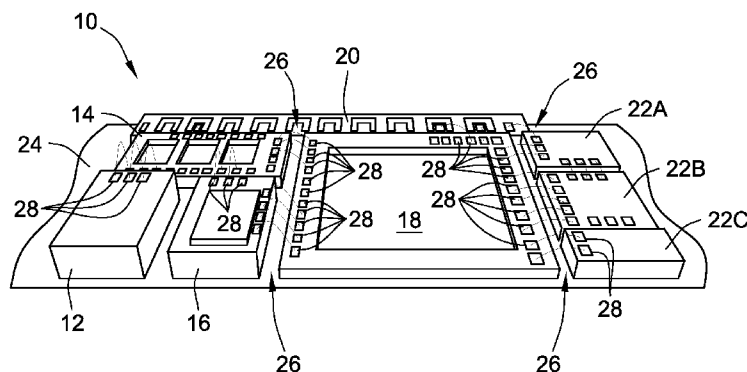


FIG. 1

(57) Abstract: Flexible integrated circuit (IC) modules, flexible IC devices, and methods of making and using flexible IC modules are presented herein. A flexible integrated circuit module is disclosed which includes a flexible substrate and a semiconductor die attached to the flexible substrate. An encapsulating layer, which is attached to the flexible substrate, includes a thermoplastic resin and/or a polyimide adhesive encasing therein the semiconductor die. The encapsulating layer may be an acrylic -based thermally conductive and electrically isolating polyimide adhesive. Optionally, the encapsulating layer may be a B- stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend. The die may be embedded between two flexible substrates, each of which includes a layer of flexible polymer, such as a polyimide sheet, with two layers of conductive material, such as copper cladding, disposed on opposing sides of the layer of flexible polymer.

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## **FLEXIBLE ELECTRONIC CIRCUITS WITH EMBEDDED INTEGRATED CIRCUIT DIE AND METHODS OF MAKING AND USING THE SAME**

### **TECHNICAL FIELD**

5           The present disclosure relates generally to printed circuit boards (PCB) and integrated circuits (IC). More particularly, aspects of this disclosure relate to flexible integrated circuitry with embedded IC die.

### **BACKGROUND**

10           Integrated circuits (IC) are the cornerstone of the information age and the foundation of today's information technology industries. The integrated circuit, a.k.a. "chip" or "microchip," is a set of interconnected electronic components, such as transistors, capacitors, and resistors, which are etched or imprinted onto a tiny wafer of semiconducting material, such as silicon or germanium. Integrated circuits take on various forms including, as some  
15 non-limiting examples, microprocessors, amplifiers, Flash memories, application specific integrated circuits (ASICs), static random access memories (SRAMs), digital signal processors (DSPs), dynamic random access memories (DRAMs), erasable programmable read only memories (EPROMs), and programmable logic. Integrated circuits are used in innumerable products, including personal computers, laptop and tablet computers,  
20 smartphones, flat-screen televisions, medical instruments, telecommunication and networking equipment, airplanes, watercraft and automobiles.

Advances in integrated circuit technology and microchip manufacturing have led to a steady decrease in chip size and an increase in circuit density and circuit performance. The scale of semiconductor integration has advanced to the point where a single semiconductor  
25 chip can hold tens of millions to over a billion devices in a space smaller than a U.S. penny. Moreover, the width of each conducting line in a modern microchip can be made as small as a fraction of a nanometer. The operating speed and overall performance of a semiconductor chip (e.g., clock speed and signal net switching speeds) has concomitantly increased with the level of integration. To keep pace with increases in on-chip circuit switching frequency and  
30 circuit density, semiconductor packages currently offer higher pin counts, greater power dissipation, more protection, and higher speeds than packages of just a few years ago.

Conventional microchips are generally rigid structures that are not designed to be bent or stretched during normal operating conditions. In addition, most IC's are typically mounted on a printed circuit board (PCB) that is as thick or thicker than the IC and similarly rigid.

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Processes using thick and rigid printed circuit boards are generally incompatible for applications requiring stretchable or bendable circuitry. Consequently, many schemes have been proposed for embedding microchips on or in a flexible polymeric substrate. This, in turn, enables many useful device configurations not otherwise possible with rigid silicon-based electronic devices. However, many of these schemes are based on the assumption that the embedded chips are considerably thicker than the individual layers of flexible polymer that make up the PCBs. Such schemes are not compatible for “thin chip” configurations. In addition, available processes for making flexible circuits oftentimes require multiple layers of expensive materials, which not only increases material and manufacturing costs but also results in a composite structure that is undesirably thick.

### SUMMARY

Disclosed herein are flexible electronic circuits with an embedded semiconductor die, including methods of making and methods of using the same. Embodiments of this disclosure are directed to embedding a silicon (Si) die (or other semiconductor dies, including those fabricated from gallium arsenide (GaAs) and those intended for photovoltaic applications) of an integrated circuit in a layer of thermoplastic polymer, polyimide adhesive, or other flexible polymeric adhesives. Aspects of this disclosure describe a stack of flexible substrate materials used in embedding a silicon IC chip directly into the substrate. Some configurations, for example, entrench the die in a layer of polyimide (PI) adhesive. Other configurations entrench the die in a layer of thermoplastic resin. For either of the foregoing examples, the substrate with embedded die can be sandwiched between multiple layers of thermoset polymer sheets with electrically conductive metallic coatings. Two sheets of double-sided copper clad polyimide film, for example, can surround the embedding substrate material. The result is a four-metal-layer flexible printed circuit board.

Aspects of the present disclosure are directed to a flexible integrated circuit (IC) module. The flexible IC module includes a flexible substrate with a semiconductor die attached to the flexible substrate. The flexible IC module also includes an encapsulating layer that is attached or coupled to the flexible substrate. The encapsulating layer includes a thermoplastic resin or a polyimide adhesive, or both, encasing therein the semiconductor die. The encapsulating layer may be an acrylic-based thermally conductive and electrically isolating polyimide adhesive. Optionally, the encapsulating layer may be a B-stage FR-4 glass-reinforced epoxy thermoplastic polymer, copolymer or blend. The die may be

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embedded between two flexible substrates, each of which includes a layer of flexible polymer, such as a polyimide sheet, with two layers of conductive material, such as copper cladding, on opposing sides of each layer of flexible polymer. Modules with greater or fewer layers are also envisioned as being within the scope and spirit of the present disclosure.

5           According to other aspects of the present disclosure, a flexible integrated circuit (IC) package for an extremely flexible electronic device is presented. The flexible IC module includes a first flexible substrate with a first layer of flexible polymer and a first pair of layers of conductive material. Each layer of conductive material is disposed on a respective side of the layer of flexible polymer. A silicon die is attached to the first flexible substrate. The  
10   silicon die includes a wafer of electronic-grade silicon with an integrated circuit formed thereon. The flexible IC module also includes a second flexible substrate with a second layer of flexible polymer and a second pair of layers of conductive material. Each layer of conductive material is disposed on a respective side of the flexible polymer. An encapsulating layer is disposed between and laminated to both the first and second flexible substrates. The  
15   encapsulating layer includes a thermoplastic resin or a polyimide adhesive, or both, encasing therein the silicon die.

Other aspects of the present disclosure are directed to methods for making and methods for using flexible integrated circuits. In one aspect, a method for assembling a flexible integrated circuit module is disclosed. The method includes: providing first and  
20   second flexible substrates, each of the flexible substrates including a respective layer of flexible polymer with two layers of conductive material each disposed on a respective side of the layer of flexible polymer; attaching a semiconductor die to the second flexible substrate; laminating an encapsulating layer to the first flexible substrate, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both; and, laminating the  
25   encapsulating layer and the first flexible substrate to the second flexible substrate such that the thermoplastic resin or the polyimide adhesive, or both, flow around and encase therein the semiconductor die. By using a thermoplastic resin or a polyimide adhesive, the encapsulating layer can be heat-set laminated to one substrate and subsequently heat-set laminated to the another substrate without requiring an additional layer of adhesive material. This, in turn,  
30   reduces manufacturing and material costs, and helps to minimize the module thickness and overall volume.

The above summary is not intended to represent each embodiment or every aspect of the present disclosure. Rather, the foregoing summary merely provides an exemplification of some of the novel aspects and features set forth herein. The above features and advantages, and

other features and advantages of the present disclosure, will be readily apparent from the following detailed description of representative embodiments and modes for carrying out the present invention when taken in connection with the accompanying drawings and the appended claims.

5           In another aspect, this document discloses a flexible integrated circuit (IC) module comprising: a flexible substrate; a semiconductor die attached to the flexible substrate; and an encapsulating layer attached to the flexible substrate and encasing the semiconductor die between the flexible substrate and the encapsulating layer, the encapsulating layer including a polyimide adhesive.

10           In another aspect, this document discloses a flexible integrated circuit (IC) module comprising: a flexible substrate; a semiconductor die attached to the flexible substrate; and an encapsulating layer attached to the flexible substrate, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both, encasing the semiconductor die between the flexible substrate and the encapsulating layer, wherein the thermoplastic resin comprises a B-  
15 stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend.

          In another aspect, this document discloses a flexible integrated circuit (IC) module comprising: a flexible substrate including a layer of flexible polymer and two layers of conductive material, each of the layers of conductive material being disposed on a respective side of the layer of flexible polymer; a semiconductor die attached to the flexible substrate; and an  
20 encapsulating layer attached to the flexible substrate, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both, encasing the semiconductor die between the flexible substrate and the encapsulating layer.

          In another aspect, this document discloses a flexible integrated circuit (IC) package for an extremely flexible electronic device, the flexible IC package comprising: a first flexible  
25 substrate including a first layer of flexible polymer and a first pair of layers of conductive material, each of the first layers of conductive material being disposed on a respective side of the first layer of flexible polymer; a silicon die attached to the first flexible substrate, the silicon die including a wafer of electronic-grade silicon with an integrated circuit formed thereon; a second flexible substrate including a second layer of flexible polymer and a second pair of layers of  
30 conductive material, each of the second layers of conductive material being disposed on a respective side of the second layer of flexible polymer; and an encapsulating layer disposed between and laminated to the first and second flexible substrates, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both, encasing the silicon die.

In another aspect, this document discloses a flexible integrated circuit (IC) module comprising: a flexible substrate; a semiconductor die attached to the flexible substrate; and an encapsulating layer attached to the flexible substrate and encasing the semiconductor die between the flexible substrate and the encapsulating layer, the encapsulating layer including a polyimide adhesive, wherein the flexible substrate comprises a layer of flexible polymer and two layers of conductive material, each of the layers of conductive material being disposed on a respective side of the layer of flexible polymer, and further comprising a second flexible substrate attached to the encapsulating layer on the opposite side of the flexible substrate, the second flexible substrate including one or more vias extending therethrough to the semiconductor die.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

FIG. 1 is a perspective-view illustration of an example of a flexible electronic circuit system with integrated circuit (IC) packages connected by pliant wirebonded interconnects in accord with aspects of the present disclosure.

FIG. 2 is a cross-sectional side-view illustration of a representative flexible electronic circuit with a multi-layer IC module in accord with aspects of the present disclosure.

FIG. 3 is a cross-sectional side-view illustration of another representative flexible electronic circuit system with a multi-layer IC module in accord with aspects of the present disclosure.

FIG. 4 is a workflow diagram illustrating a representative method for assembling a flexible circuit (IC) module in accord with aspects of the present disclosure.

The present disclosure is susceptible to various modifications and alternative forms, and some representative embodiments have been shown by way of example in the drawings and will be described in detail herein. It should be understood, however, that the invention is not intended to be limited to the particular forms disclosed. Rather, the disclosure is to cover all modifications, equivalents, combinations, subcombinations, and alternatives falling within the spirit and scope of the invention as defined by the appended claims.

#### **DETAILED DESCRIPTION**

This disclosure is susceptible of embodiment in many different forms. There are shown in the drawings, and will herein be described in detail, representative embodiments with the understanding that the present disclosure is to be considered as an exemplification of the

principles of the present disclosure and is not intended to limit the broad aspects of the disclosure to the embodiments illustrated. To that extent, elements and limitations that are disclosed, for example, in the Abstract, Summary, and Detailed Description sections, but not explicitly set forth in the claims, should not be incorporated into the claims, singly or  
5 collectively, by implication, inference or otherwise. For purposes of the present detailed description, unless specifically disclaimed or logically prohibited: the singular includes the

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plural and vice versa; and the word “including” or “comprising” or “having” means “including without limitation.” Moreover, words of approximation, such as “about,” “almost,” “substantially,” “approximately,” and the like, can be used herein in the sense of “at, near, or nearly at,” or “within 3-5% of,” or “within acceptable manufacturing tolerances,”

5 or any logical combination thereof, for example.

The terms “flexible” and “stretchable” and “bendable,” including roots and derivatives thereof, when used as an adjective to modify electrical circuitry, electrical systems, and electrical devices or apparatuses, are meant to encompass electronics that comprise at least some components having pliant or elastic properties such that the circuit is  
10 capable of being flexed, stretched and/or bent, respectively, without tearing or breaking or compromising their electrical characteristics. These terms are also meant to encompass circuitry having components (whether or not the components themselves are individually stretchable, flexible or bendable) that are configured in such a way so as to accommodate and remain functional when applied to a stretchable, bendable, inflatable, or otherwise pliant  
15 surface. In configurations deemed “extremely stretchable,” the circuitry is capable of stretching and/or compressing and/or bending while withstanding high translational strains, such as in the range of -100% to 100% and, in some embodiments, up to -100,000% to +100,000%, and/or high rotational strains, such as to an extent of 180° or greater, without fracturing or breaking and while substantially maintaining electrical performance found in an  
20 unstrained state.

The discrete “islands” or “packages” mentioned herein are discrete operative devices, e.g., arranged in a “device island” arrangement, and are themselves capable of performing the functionality described herein, or portions thereof. Such functionality of the operative devices can include, for example, integrated circuits, physical sensors (e.g. temperature, pH,  
25 light, radiation, etc.), biological sensors, chemical sensors, amplifiers, A/D and D/A converters, optical collectors, electro-mechanical transducers, piezoelectric actuators, light emitting electronics (e.g., LEDs), and any combination thereof. A purpose and an advantage of using one or more standard ICs (e.g., CMOS on single crystal silicon) is to use high-quality, high-performance, and high-functioning circuit components that are readily  
30 accessible and mass-produced with well-known processes, and which provide a range of functionality and generation of data far superior to that produced by passive means. The discrete islands may range from about, but not limited to, 10-100 micrometers ( $\mu\text{m}$ ) in size measured on an edge or by diameter.

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Referring now to the drawings, wherein like reference numerals refer to like components throughout the several views, FIG. 1 illustrates an example of a flexible integrated circuit (IC) system, designated generally as 10, which may be adapted as or integrated into an “extremely stretchable” IC apparatus. Many of the disclosed concepts are discussed with reference to the representative systems depicted in the drawings; the systems illustrated herein, however, are provided merely as exemplary applications by which the various inventive aspects and features of this disclosure can be applied. Thus, the novel aspects and features of the present disclosure are not per se limited to the particular arrangements and components presented in the drawings. Moreover, only selected components of the system(s) have been shown and will be described in additional detail hereinbelow. Nevertheless, the systems and devices discussed herein can include numerous additional and alternative features, and other well-known peripheral components, for example, for carrying out the various methods and functions disclosed herein. Some of the illustrated components are optional and, thus, can be removed.

The flexible IC system 10 of FIG. 1 comprises various electronic components (collectively referred to as “circuitry”), such as a laminated battery 12, a set of microchips 14, a sensor 16, a sensor hub 18, antenna 20, and an assortment of integrated passive devices (IPD) 22A, 22B and 22C. The circuitry is applied, secured, embedded or otherwise affixed to substrate 24, which is flexible – e.g., stretchable, bendable and/or compressible - as described herein. As such, the substrate 24 can be made of a plastic material or an elastomeric material, or combinations thereof. Examples of suitable flexible elastomers for the IC substrate material include polymeric organosilicon compounds (commonly referred to as “silicones”), including Polydimethylsiloxane (PDMS). Other non-limiting examples of materials suitable for the substrate 24 include polyimide, photopatternable silicon, SU8 polymer, PDS polydustrene, parylene and its derivatives and copolymers (parylene-N), ultrahigh molecular weight polyethylene, polyether ether ketones (PEEK), polyurethanes, polylactic acid, polyglycolic acid, polymer composites, silicones/siloxanes, polytetrafluoroethylene, polyamic acid, polymethyl acrylate, and combinations thereof. The substrate 24 can take on any possible number of shapes, sizes, and configurations. In the illustrated example, the substrate is substantially flat and, in some embodiments, configured to be an elongated sheet or strip.

The circuitry of FIG. 1 comprises one or more sensors 16 (also termed “sensor devices”) to detect any of various parameters. These parameters can include, in any combination, thermal parameters (e.g., temperature), optical parameters (e.g., infrared energy), electrochemical and biochemical parameters, such as pH, enzymatic activity,

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blood components (e.g., glucose), ion concentrations, and protein concentrations, electrical parameters (e.g., resistance, conductivity, impedance, etc.), acoustic parameters, tactile parameters (e.g., pressure, surface characteristics, or other topographic features), etc. In this regard, one or more of the sensors 16 may be a thermocouple, a silicon band gap  
 5 temperature sensor, a thin-film resistance temperature device, an LED emitter, a photodetector, a piezoelectric sensor, an ultrasonic sensor, an ion sensitive field effect transistor, etc. For some implementations, one or more of the sensors 16 can be coupled to a differential amplifier and/or a buffer and/or an analog to digital converter. The sensor hub 18, which may be in the nature of a microcontroller or digital signal processor (DSP),  
 10 operates to integrate data signals from the sensor(s) 16 and process such signals. Signals from the sensor(s) 16 can be processed using multiplexing techniques, and can be switched into and processed by one or a few amplifier/logic circuits, including one or more of the microchips 14.

Battery 12 acts as a power source to supply power to the circuitry in the flexible IC  
 15 system 10 of FIG. 1. Any suitable battery which is small in size and has a sufficiently long life with a suitable amp-hour capacity may be employed. It is also within the scope of this disclosure to employ alternative means for powering the system 10, including external power supplies. According to some embodiments, the flexible IC system 10 also includes a data transmission facility with an RF antenna 20 to wirelessly communicate with external devices.  
 20 The antenna 20 can take on various forms, including a printed trace antenna coil with vias, which may be operable as a low frequency, high frequency or ultra-high frequency antenna. Other forms of wired and wireless signal transmission are also within the scope of this disclosure. Each integrated passive device (IPD) 22A-22C may comprise, as some non-limiting examples, a filter, a transformer, a photodiode, LED, TUFT, electrode,  
 25 semiconductor, duplexer, coupler, phase shifter, thin-film device, circuit element, control elements, microprocessor, capacitors, resistors, inductors, buffer or other passive component.

For embodiments where the substrate 24 is stretchable or compressible, the illustrated circuitry is configured in applicable manners, such as those described herein, to be stretchable or compressible and/or to accommodate such stretching/compressing of the substrate 24.  
 30 Similarly, for embodiments where the substrate 24 is bendable, but not necessarily stretchable, the illustrated circuitry is configured in applicable manners, such as those described herein, to be bendable and/or accommodate such bending of the substrate. For example, each of the illustrated modules or "islands" is connected to one or more adjacent modules with flexible wirebonded interconnects, some of which are designated generally as

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26 in FIG. 1. The connection point of the individual interconnects to a device island may be anywhere along the device island edge, or may be at a point on the top surface of the device island (i.e., the surface opposite the substrate 24). The bond wires 26 are attached to externally mounted bond pads 28 on the modules and extend to a corresponding externally mounted bond pad 28 on an adjacent module. The bond wires can be attached through any known wirebonding technique, such as: ultrasonic bonding which uses a combination of pressure and ultrasonic vibration bursts to form a metallurgical cold weld; thermocompression bonding which uses a combination of pressure and elevated temperature to form a weld; and thermosonic bonding which uses a combination of pressure, elevated temperature, and ultrasonic vibration bursts to form a weld joint.

Turning next to FIG. 2, there is shown a cross-sectional illustration of a representative flexible electronic circuit system, designated generally as 100, with one or more multi-layer IC modules. While differing in appearance, the flexible IC system 100 of FIG. 2 can take on any of the various forms, optional configurations, and functional alternatives described herein with respect to the examples shown in FIGS. 1 and 3, and thus can include any of the corresponding options and features. Like the system 10 of FIG. 1, for example, the system 100 of FIG. 2 may be configured as an ultrathin, extremely stretchable integrated circuit system. Moreover, system 100 may comprise an assortment of discrete devices – one of which is represented in FIG. 2 by a flexible IC module 102 – that are arranged in a “device island” arrangement and electrically coupled, for example, by pliant electrical interconnects. It is contemplated that the system 100 comprise greater or fewer than the number of discrete devices shown in the drawings, each of which may take on alternative forms and configurations.

In the embodiment of FIG. 2, the IC module 102 includes, but is not necessarily limited to, a flexible multi-layer integrated circuit (IC) package or “stack” capable of performing one or more of the functions described herein. The module 102 includes at least one semiconductor die 104 that is seated between at least two flexible substrates 106A and 106B. As shown, the semiconductor die 104 (also referred to herein as “silicon die”) comprises a wafer of electronic-grade silicon 103 with an integrated circuit (or microchip) 105 formed thereon (e.g., via photolithography or any other known and industry accepted techniques). For some embodiments, the semiconductor die 104 is adhered directly to the second flexible substrate 106B. The microchip 105 may be a “thin chip” configuration with a thickness of about 2-7  $\mu\text{m}$  or, in some embodiments, a thickness of about 5-7  $\mu\text{m}$  or, in some embodiments, a thickness of about 3-5  $\mu\text{m}$  or, in some embodiments, a thickness of about 2-3

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μm. By comparison, the semiconductor die 104 has a thickness of approximately 10-50 μm or, in some embodiments, a thickness of approximately 35-50 μm or, in some embodiments, a thickness of approximately 15-25 μm or, in some embodiments, a thickness of approximately 10-15 μm, for example. In the representative systems, methods and devices described herein,  
 5 each thin chip can be one or more passive electronic devices and/or one or more active electronic devices. Non-limiting examples of devices that can be embedded according to any of the principles described herein include an amplifier, a transistor, a photodiode array, a photodetector, a sensor, a light-emitting device, a photovoltaic device, a semiconductor laser array, an optical imaging device, a logic gate array, a microprocessor, an opto-electronic  
 10 device, a microelectromechanical device, a microfluidic device, a nanoelectromechanical device, a thermal device, or other device structures.

Silicon die 104 is shown in FIG. 2 sandwiched between first and second flexible substrates 106A, 106B, each of which comprises one or more flexible polymeric layers interposed with one or more flexible electrically conductive layers. As shown, the first  
 15 flexible substrate 106A includes two (first) layers of conductive material 110A, each disposed on a respective side of a (first) layer of flexible polymer 112A. Likewise, the second flexible substrate 106B includes two (second) layers of conductive material 110B, each disposed on a respective side of a (second) layer of flexible polymer 112B. The layers of flexible polymer 112A, 112B may be fabricated as sheets of thermoset polyimide polymer,  
 20 while the layers of conductive material 110A, 110B may be fabricated as metallic sheets or coatings. In one specific implementation, the flexible polymer layers 112A, 112B are fabricated from a liquid crystal polymer or a polyimide polymer, such as KAPTON® film available from DuPont™. Alternatively, the flexible polymer layers 112A, 112B may be fabricated from any of the materials described above with respect to the substrate 24 of FIG.  
 25 1 or other materials suitable for flexible electronic circuitry. The polymeric layers 112A, 112B can each have a thickness of about 7 μm to about 85 μm or, in some embodiments, about 60 μm to about 85 μm or, in some embodiments, about 25 μm to about 50 μm or, in some embodiments, about 7 μm to about 10 μm. It is envisioned that the module 102 comprise greater or fewer layers than that shown in FIG. 2 of the drawings.

30 First and second electrically conductive (polymeric or metallic) layers 110A, 110B are disposed on opposing sides of the flexible polymeric layers 112A, 112B, as seen in FIG. 2. In an example, layers of copper cladding are applied, e.g., via electroplating, bonding, or other known cladding techniques, to opposing sides of elongated and flat polyimide sheets. The sheets of double-sided copper clad polyimide can subsequently be patterned with

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circuitry using ablation, etching or other similar patterning processes. Each electrically conductive layer 110A, 110B can have a thickness of about 5  $\mu\text{m}$  to about 20  $\mu\text{m}$  or, in some embodiments, a thickness of about 15  $\mu\text{m}$  to about 20  $\mu\text{m}$  or, in some embodiments, a thickness of about 10  $\mu\text{m}$  to about 12  $\mu\text{m}$  or, in some embodiments, a thickness of about 5  $\mu\text{m}$  to about 8  $\mu\text{m}$ . Electrically conductive layers can also be fabricated, for example, from other metallic materials, including aluminum or a combination of copper and aluminum, as well as non-metallic materials.

An encapsulating layer 108 is disposed between and attached or coupled to the two flexible substrates 106A, 106B of FIG. 2. Encapsulating layer 108 may be a polyimide (PI) adhesive which covers the semiconductor die 104 such that the die 104 is encased between the flexible substrate 106B and the encapsulating layer 108. For some embodiments, the encapsulating layer 108 is an acrylic-based thermally conductive and electrically isolating polyimide adhesive that is first laminated onto one flexible substrate 106B, flowing over and surrounding die 104, and subsequently laminated to another substrate 106A to form the multi-layer stack. The encapsulating layer 108 can have a thickness of about 15  $\mu\text{m}$  to about 65  $\mu\text{m}$  or, in some embodiments, about 20  $\mu\text{m}$  to about 55  $\mu\text{m}$  or, in some embodiments, about 25  $\mu\text{m}$  to about 50  $\mu\text{m}$ . The thickness of the encapsulating layer 108, for at least some embodiments, is 16  $\mu\text{m}$ . Encapsulating layer 108 can be a conductive adhesive or a non-conductive (dielectric) adhesive that is configured to withstand the temperatures of assembly and processing. In some optional and alternative configurations, encapsulating layer 108 can be a fluropolymer adhesive, a polyimide blend adhesive, an epoxy adhesive, or an acrylic adhesive, such as PYRALUX® Bond-Ply available from DuPont™, or any combination thereof.

Polyimide adhesive is a non-metallic organic adhesive capable of bonding composite laminates and a wide variety of high temperature metallic substrates, such as copper, stainless steel and titanium, while maintaining thermal oxidative stability, high service temperature performance (e.g., 575°F+), moisture resistance and environmental durability. Unlike many other available adhesive compositions suitable for integrated circuit applications, PI adhesives can be laminated and re-laminated without compromising the structural integrity of the resultant bond. By using a polyimide adhesive or a thermoplastic resin (discussed below in FIG. 3), the encapsulating layer can be heat-set laminated to one substrate and subsequently heat-set laminated to another substrate without requiring any additional layers of adhesive material. This, in turn, reduces manufacturing and material costs, and also helps minimize the module's thickness and overall volume.

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One or more vias can be generated as channels, e.g., with a laser drill, extending through outer layers of the flexible IC package to allow for conductive connections between different layers of the multi-layer stack. In FIG. 2, for example, the flexible IC module 102 includes a pair of vias 116 that extend through the top layers of the module 102 (e.g., the three-layer substrate 106A) to the microchip 105. Once these vias 116 have been created, the vias 116 can be electroplated or filled through sputtering or other known technique to create electrical connections from the top conductive layer 110A to one or more electrical contact pads of the die. The conductive layers can then be patterned and an overlay can be applied to the outer surface of each conductive layer. In some implementations, the overlay is a non-conductive polymer.

It is contemplated that the illustrated multi-layer IC package comprises additional or fewer layers than the sandwich constructions shown in FIG. 2. It should also be noted that the use of the term “layer” in the description and claims does not necessarily require that particular segment of the sandwich construction be continuous or span the entirety of (i.e., be coextensive with) all remaining layers unless otherwise explicitly stated in the claims. While preferable in some applications, it is not necessary in practice that the layers of one flexible substrate be fabricated from the same materials as the layers of the other flexible substrate. It may be desirable, for some implementations, that the multi-layer package be vacuum laminated as a discrete, unitary structure prior to electrical coupling with one or more adjacent devices.

FIG. 3 illustrates another representative flexible electronic circuit system, designated generally as 200, with one or more multi-layer IC modules. Like reference numerals are used in FIG. 3 to indicate similar structure from FIG. 2. For example, the system 200 of FIG. 3 may comprise an assortment of discrete devices, including a flexible IC module 202, which are arranged in a “device island” arrangement and electrically coupled, for example, by pliant electrical interconnects. Moreover, the flexible IC system 200 can take on any of the various forms, optional configurations, and functional alternatives described herein with respect to the other examples shown in the figures, and vice versa, unless explicitly or logically prohibited.

Similar to the example illustrated in FIG. 2, the IC module 202 of FIG. 3 includes a flexible multi-layer integrated circuit (IC) package with at least one semiconductor die 204 that is seated between at least two flexible substrates 206A and 206B. For some embodiments, the semiconductor die 204 is adhered directly to the second flexible substrate 206B. Each of the first and second flexible substrates 206A, 206B comprises one or more

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flexible polymeric layers interposed with one or more flexible electrically conductive layers. While not per se required to practice the inventive aspects disclosed herein, the semiconductor die 204 and flexible substrates 206A, 206B of FIG. 3 may be structurally and functionally identical to their counterparts illustrated in FIG. 2; as such, for brevity and  
 5 conciseness, a duplicated description of these elements will be omitted.

An encapsulating layer 208 is disposed between and attached to the two flexible substrates 206A, 206B of FIG. 3. In addition to or in lieu of the polyimide adhesive described in FIG. 2, the encapsulating layer 208 of FIG. 3 may be a thermoplastic polymer, copolymer or polymer blend (collectively referred to therein as “thermoplastic resin”) which  
 10 covers the semiconductor die 204 such that the die 204 is encased between the flexible substrate 206B and the encapsulating layer 208. For some embodiments, the encapsulating layer 208 is a B-stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend that is first laminated onto one flexible substrate 206B, flowing over and surrounding die 204, and subsequently laminated to another substrate 206A to form the multi-layer stack.  
 15 In some non-limiting examples, the thermoplastic based resins include polycarbonate (PC), polyethylene (PET), and polyurethane (PU), and any composites or copolymer blends of these materials. A copolymer blend can include a polyimide layer on one of these thermoplastic polymers. The encapsulating layer 208 can have a thickness of about 15  $\mu\text{m}$  to about 65  $\mu\text{m}$  or, in some embodiments, about 20  $\mu\text{m}$  to about 55  $\mu\text{m}$  or, in some  
 20 embodiments, about 25  $\mu\text{m}$  to about 50  $\mu\text{m}$ .

A functional advantage of using a thermoplastic core over a thermoset core to encase the die(s) is that thermoplastic polymers can be melted and re-melted back into a plasticized or liquid state, whereas thermoset plastics remain in a permanent solid state. Thermoplastics soften when heated and become more fluid as additional heat is applied; the curing process is  
 25 reversible as no chemical bonding takes place. This characteristic allows thermoplastics to be reheated and remolded without negatively affecting the material’s physical properties. There are multiple thermoplastic resins that offer various performance benefits, but most materials commonly offer high strength, shrink-resistance and easy bendability.

Also disclosed herein are methods for manufacturing flexible integrated circuits.  
 30 These methods will be described with reference to the various configurations and features shown in FIGS. 1 through 3 of the drawings; such reference is being provided purely by way of explanation and clarification. In an example, a method 300 is illustrated in FIG. 4 as comprising, first, providing first and second flexible substrates (e.g., substrates 106A, B of FIG. 2 or substrates 206A, B of FIG. 3) at step 301. As indicated above, each substrate may

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include a layer of flexible polymer (e.g., flexible polymer layers 112A, B of FIG. 2) with a layer of conductive material (e.g., conductive material layers 110A, B of FIG. 2) disposed on each side of the flexible polymer layer. As indicated above, the flexible substrates may comprise sheets of double sided copper-clad polyimide film. The method 300 may then  
5 require, at step 303, patterning circuitry on both substrates.

At step 305, a silicon-based semiconductor die (e.g., semiconductor dies 104 and 204 of FIGS. 2 and 3) is then placed directly on one flexible substrate (e.g., the second flexible substrate 106B or 206B of FIGS. 2 and 3) and attached or coupled thereto, e.g., by non-conditioned epoxy, directly to an outer metal layer thereof. As seen in FIG. 4, the method  
10 300 thereafter includes at step 307 laminating an encapsulating layer (e.g., a PI adhesive or a thermoplastic resin) to the other flexible substrate (e.g., the first flexible substrate 106A or 206A of FIGS. 2 and 3). At step 309, the flexible substrate with encapsulating layer are then laminated to the other flexible substrate with silicon die such that the thermoplastic resin and/or polyimide adhesive of the encapsulating layer flow around and encase the  
15 semiconductor die. At step 311, one or more vias may then be drilled or otherwise formed through the second flexible substrate and the encapsulating layer to contacts on the semiconductor die. The method 300 may then include electroplating the vias to connect the first flexible substrate to the semiconductor die. Additional circuit patterning may then be performed, and a protective solder mask applied to the outer surfaces of the stack.

20 In some embodiments, the aforementioned method includes at least those steps enumerated above. It is also within the scope and spirit of the present disclosure to omit steps, include additional steps, and/or modify the order presented herein. It should be further noted that each of the foregoing methods can be representative of a single sequence of related steps; however, it is expected that each of these method will be practiced in a systematic and  
25 repetitive manner.

The present disclosure is not limited to the precise construction and compositions disclosed herein; any and all modifications, changes, and variations apparent from the foregoing descriptions are within the spirit and scope of the disclosure as defined in the appended claims. Moreover, the present concepts expressly include any and all combinations  
30 and subcombinations of the preceding elements and aspects.

What is claimed is:

1. A flexible integrated circuit (IC) module comprising:  
a flexible substrate;  
a semiconductor die attached to the flexible substrate; and  
an encapsulating layer attached to the flexible substrate, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both, encasing the semiconductor die between the flexible substrate and the encapsulating layer,  
wherein the thermoplastic resin comprises a B-stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend.
2. A flexible integrated circuit (IC) module comprising:  
a flexible substrate including a layer of flexible polymer and two layers of conductive material, each of the layers of conductive material being disposed on a respective side of the layer of flexible polymer;  
a semiconductor die attached to the flexible substrate; and  
an encapsulating layer attached to the flexible substrate, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both, encasing the semiconductor die between the flexible substrate and the encapsulating layer.
3. The flexible integrated circuit module of claim 2, wherein the layer of flexible polymer comprises a sheet of thermoset polyimide polymer, and the layers of conductive material each comprises a copper coating.
4. The flexible integrated circuit module of claim 3, wherein the layers of copper coating are each patterned on a respective side of the sheet of thermoset polyimide polymer.
5. The flexible integrated circuit module of claim 2, further comprising a second flexible substrate attached to the encapsulating layer on the opposite side of the flexible substrate.

6. The flexible integrated circuit module of claim 5, wherein the second flexible substrate comprises a second layer of flexible polymer and two second layers of conductive material, each of the second layers of conductive material being disposed on a respective side of the second layer of flexible polymer.

7. The flexible integrated circuit module of claim 6, wherein the second layer of flexible polymer comprises a sheet of thermoset polyimide polymer, and the second layers of conductive material each comprises a copper coating.

8. The flexible integrated circuit module of claim 5, wherein the second flexible substrate further comprises one or more vias extending therethrough to the semiconductor die.

9. A flexible integrated circuit (IC) package for an extremely flexible electronic device, the flexible IC package comprising:

a first flexible substrate including a first layer of flexible polymer and a first pair of layers of conductive material, each of the first layers of conductive material being disposed on a respective side of the first layer of flexible polymer;

a silicon die attached to the first flexible substrate, the silicon die including a wafer of electronic-grade silicon with an integrated circuit formed thereon;

a second flexible substrate including a second layer of flexible polymer and a second pair of layers of conductive material, each of the second layers of conductive material being disposed on a respective side of the second layer of flexible polymer; and

an encapsulating layer disposed between and laminated to the first and second flexible substrates, the encapsulating layer including a thermoplastic resin or a polyimide adhesive, or both, encasing the silicon die.

10. The flexible integrated circuit package of claim 9, wherein the polyimide adhesive comprises an acrylic-based thermally conductive and electrically isolating polyimide adhesive.

11. The flexible integrated circuit package of claim 9, wherein the thermoplastic resin comprises a B-stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend.

12. The flexible integrated circuit package of claim 9, wherein the second flexible substrate further comprises one or more vias extending therethrough to the silicon die.

13. A flexible integrated circuit (IC) module comprising:  
a flexible substrate;  
a semiconductor die attached to the flexible substrate; and  
an encapsulating layer attached to the flexible substrate and encasing the semiconductor die between the flexible substrate and the encapsulating layer, the encapsulating layer including a polyimide adhesive,

wherein the flexible substrate comprises a layer of flexible polymer and two layers of conductive material, each of the layers of conductive material being disposed on a respective side of the layer of flexible polymer, and further comprising a second flexible substrate attached to the encapsulating layer on the opposite side of the flexible substrate, the second flexible substrate including one or more vias extending therethrough to the semiconductor die.

14. The flexible integrated circuit module of claim 1, wherein the polyimide adhesive comprises an acrylic-based thermally conductive and electrically isolating polyimide adhesive.

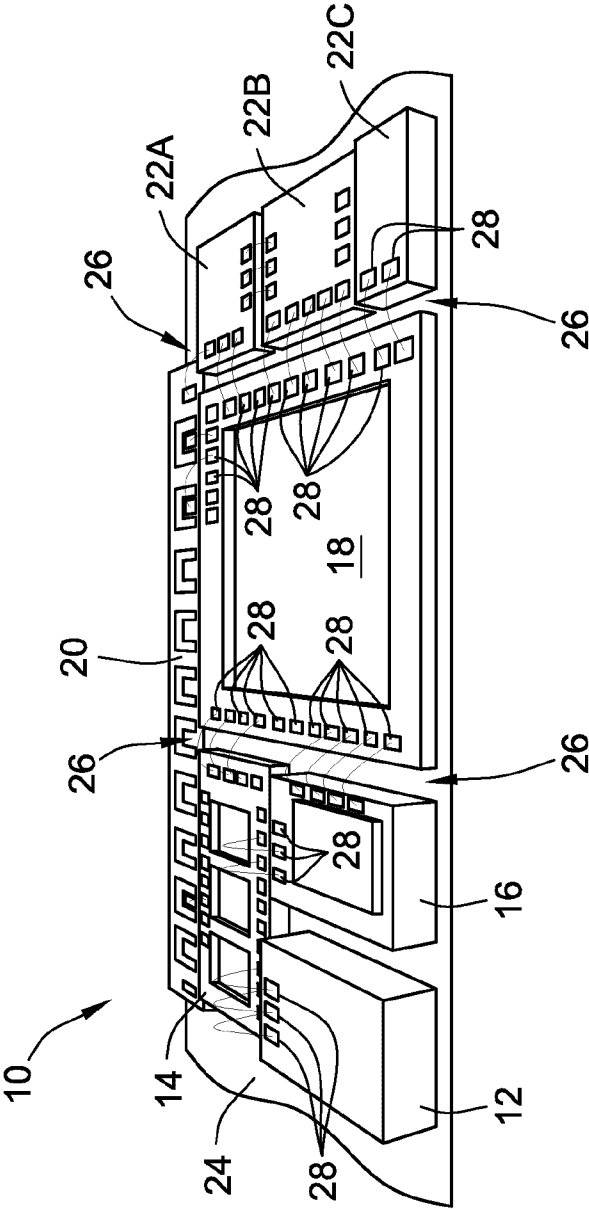
15. The flexible integrated circuit module of claim 1, wherein the semiconductor die comprises a wafer of electronic-grade silicon with an integrated circuit formed thereon.

16. The flexible integrated circuit module of claim 1, wherein the flexible substrate comprises a layer of flexible polymer and two layers of conductive material, each of the layers of conductive material being disposed on a respective side of the layer of flexible polymer.

17. The flexible integrated circuit module of claim 16, further comprising a second flexible substrate attached to the encapsulating layer on the opposite side of the flexible substrate, the second flexible substrate including one or more vias extending therethrough to the semiconductor die.

18. The flexible integrated circuit module of claim 2, wherein the thermoplastic resin comprises a B-stage FR-4 glass-reinforced epoxy thermoplastic polymer or copolymer or blend and wherein the polyimide adhesive comprises an acrylic-based thermally conductive and electrically isolating polyimide adhesive.

19. The flexible integrated circuit module of claim 2, wherein the semiconductor die comprises a wafer of electronic-grade silicon with an integrated circuit formed thereon.



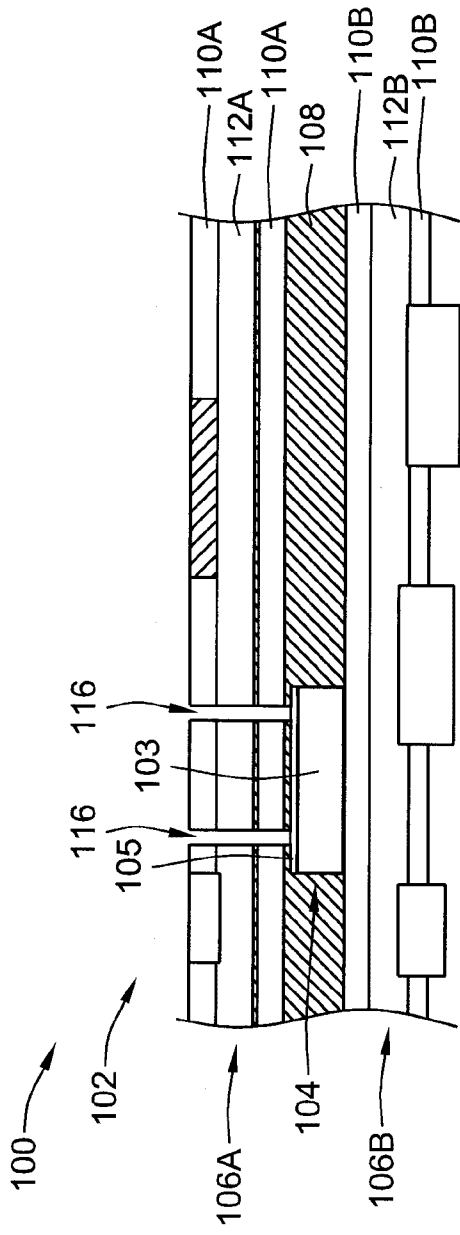


FIG. 2

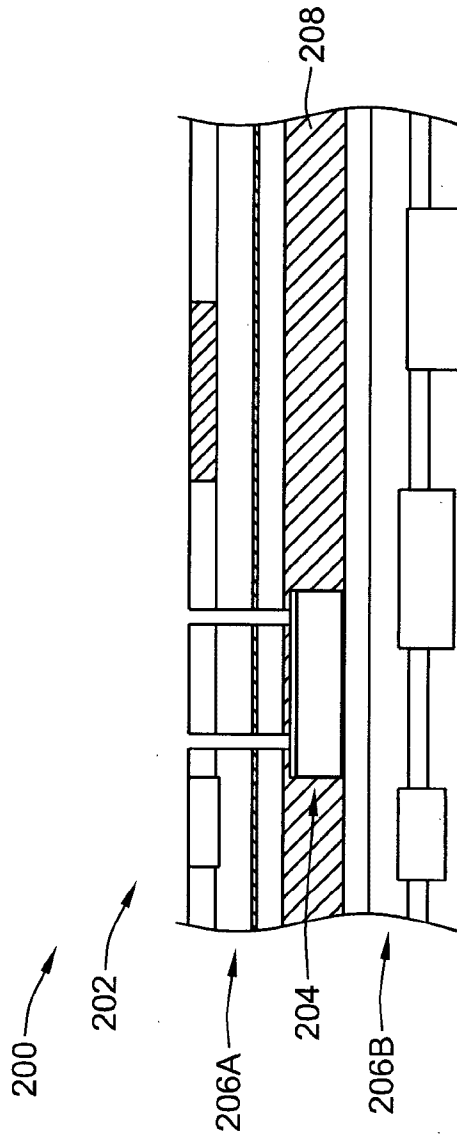


FIG. 3

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