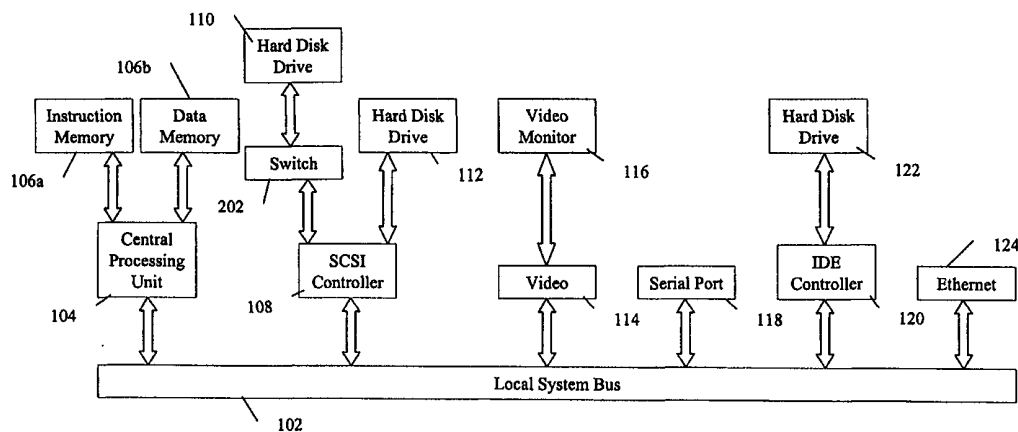




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(54) Title: SECURE COMPUTER SYSTEM AND METHOD OF PROVIDING SECURE ACCESS TO A COMPUTER SYSTEM



## (57) Abstract

A computer system includes hardware for selectively disabling alteration of data residing on a mass storage device which is subject to remote access. In one embodiment, a hard disk drive is operated in a conventional manner including both read and write modes when the system is being operated in a non-secure mode of operation, such as when remote access is not allowed. In a secure mode of operation, a locally operated switch is used to disable writing to the hard disk drive to maintain data integrity on the drive. The system may also include first and second electrically isolated buses and corresponding processors. In this configuration, the hard disk drive may be selectively connected to the first bus and processor for the storage of data, or to the second bus and processor when in a secure mode to provide for read-only remote access to the information stored on the hard drive. A write-only hard drive may also be included for storage of confidential information provided by remote users so that other remote users cannot access that information. In a master/slave processor configuration, all system programming is resident in an isolated portion of the system inaccessible to remote users. The slave processor receives instructions only from the master processor so that the operation of the slave processor cannot be compromised by viruses uploaded by remote users.

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## SECURE COMPUTER SYSTEM AND METHOD OF PROVIDING SECURE ACCESS TO A COMPUTER SYSTEM

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

5           The present invention relates to computer system architecture and more particularly to an architecture for and method of limiting remote access to programs and data.

#### 2. Description of the Related Technology

10           The role of computers is rapidly changing from computational machines to communication devices. The increasing use of the Internet by the general public increases the potential for hackers to break into sensitive computers. Computer hackers have successfully entered systems believed to be secure, gained unauthorized access, corrupted data, and infected systems with viruses that continue to cause havoc. While specialized software in the form of, for example, firewalls, is often provided to prevent unauthorized system access and to limit access so that unauthorized personnel cannot easily corrupt data and program files or otherwise cause damage to a computer system and loss of data, hackers are continually finding ways around the software. For example, viruses can be used to infect a computer system through infected software, causing the system to perform unauthorized functions and execute "rogue" code jeopardizing the integrity of the system. Because all functions performed by the computer system are controlled by instructions stored in the computer's memory, providing any remote access to the system provides an avenue for hackers to gain unauthorized access and do damage.

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25           A representative computer system according to the prior art is shown in block diagram form in FIG. 1. A prior art computer system 100 includes a local system bus 102 connecting major elements of the computer system. Thus, local system bus 102 handles the transfer of instructions, data, address and control signals, etc. between the elements of the computer system. As shown in the figure, central processing unit 104 has a direct connection to bus 102 and to a dedicated main memory 106. Main memory 106 is typically a high speed, high bandwidth random access memory storing data and instructions. Non-

volatile mass storage is provided by hard disk drives 110 and 112 interfacing via SCSI (small computer systems interface) device 108 to local system bus 102 and hard disk drive 122 interfacing through IDE (intelligent drive electronics) controller 120. Central processing unit 104 also has provisions for displaying data to a system operator by providing appropriate address, data and control signals to video interface 114 whereby data is displayed on video monitor 116. Finally, remote access to peripheral devices and buses is provided by serial port 118 and Ethernet interface 124, again over local system bus 102. Although not shown, other devices providing input and output to the system may be included, such as a keyboard, etc., which may include a dedicated interface to local system bus 102 or might be supported by serial port 118. Similarly, other output devices may be included, such as a printer interfacing through serial port 118 or an equivalent parallel port type data connection (not shown).

In operation, computer programs consisting of executable code and data and other information on which the code operates, are stored in main memory 106. Typically, this includes an operating system, such as Windows NT or Windows 98, together with various utilities and application programs. At startup or initialization, central processing unit 104 executes "boot" code, identifies system assets, such as IDE controller 120 and hard disk drive 122, and locates the appropriate operating system. The operating system software from hard disk drive 122 is then transferred through IDE controller 120 via local bus 102 to main memory 106. Central processing unit 104 then executes the operating system, transferring instructions as needed from main memory 106 into a "cache" or other local memory and registers that are a part of the central processing unit 104. While this is happening, dedicated hardware and firmware resident in video board 114 provide a visual display on video monitor 116 of system status and provide a video output for the operating system, utilities, and application programs. In addition to the online data storage provided by hard disk drive 122, multiple hard disk drives are supported by SCSI controller 108. As depicted, both hard disk drives 110 and 112 are interfaced to local system bus 102 through the SCSI controller 108 providing additional non-volatile storage capabilities.

In addition to local access to computer system 100, remote access is provided by serial port 118 and Ethernet card 124. For example, a modem (not shown) may be attached to serial port 118 to interface computer system 100 to other media such as the public switched telephone network (PSTN), radio and fiber optic systems, etc., thereby providing connectivity to remote users and systems. An appropriate communications utility or application running on central processing unit 104 together with serial port 118 supports exchange of data with the remote users and systems. Similarly, Ethernet 124 is a specific embodiment of a network connectivity supporting, for example, a local area network (LAN), a wide area network (WAN), etc., with multiple remote computer systems and other resources attached. Using these remote access facilities, computer system 100 becomes accessible to authorized, and in many cases, unauthorized users.

Although not shown, other peripherals may be included, such as CD-ROMS (compact disk- read only memories), CD-WORM (compact disk - write once read many) or CD-WO (compact disk - write once), CD-RW (compact disk - re-writeable), DVD-RAM (digital versatile disk - RAM), DVD-ROM (digital versatile disk - ROM), various tape drives and traditional 3-1/2 inch floppy disk drives. These devices are particularly useful for the transport of data between systems and backup purposes using removable media. Conventionally, because of access speed and storage space limitations, these devices are generally not relied upon as substitutes for hard disk drives which continue to be used as the primary media for non-volatile program and data mass storage. However, as computer systems have been made available to greater numbers of users, both locally and remotely, maintaining the integrity of programs and data stored on computer systems has become an increasing concern.

Prior art systems implement various physical and software systems to control access to the system and provide security. For example, computer systems handling classified information may require TEMPEST approval to avoid unintended radiation of information, be located in a secure facility such as a limited access area to provide physical security, and be operated in a stand alone configuration without provision for remote access to avoid remote hacker access. Physical security, however, cannot address remote access users so

that a variety of software is used to establish varying authorization levels for remote system use and access. For example, remote users may be required to interface via a secure access or "firewall" system which requires a user to establish authorization to access a computer system prior to providing a connection. A firewall may further monitor use of facilities, limiting access and use according to the user's authorization. Software on the computer system itself further monitors access using, for example, passwords, personal identification numbers (pins), etc. to control access and use. Other software may be implemented to protect, for example, certain area of memory such as the operating system from being altered or overwritten. Some operating systems, for example, further limit write operations to particular areas of memory containing data used by a particular application and limit access to other areas of memory or alteration of instructions stored in memory. However, such software protections have often proved inadequate to stop a determined hacker from gaining unauthorized access and bypassing such safeguards. For example, a hacker might use another program to generate and try thousands or millions of access code combinations to break into a system. Alternatively, using a more conventional approach, a hacker might rummage through discarded company documents to obtain access code information, unlisted maintenance telephone numbers, etc. Access may also be obtained by "back doors" into the system otherwise used for maintenance, billing, and other non-remote access purposes. Hackers may also obtain access by implanting computer viruses into the system, often embedded in innocent appearing host software. Once implanted, the virus can damage the system directly or provide other methods of access for the hacker.

In addition to remote covert action, computer systems are also subject to local attacks by, for example, disgruntled employees, etc. On a less sinister basis, computer systems are further subject to unintentional damage by human operator error inadvertently deleting or modifying files and by program bugs in the system and applications having similar effects and results as that of intentional attacks on the system.

For the foregoing reasons, there exists a need for a secure computer system architecture and method for providing computer security which cannot be easily bypassed by innocent or surreptitious means, either remotely or local to the computer system. A

further need exists for a computer system and method of operating a computer system which preserves data and program integrity while providing for remote access to users having only read access. A still further need exists for a computer system and method of operating a computer system which prevents data and instruction corruption, modification  
5 and deletion by improper operation of host applications or due to the intentional actions of software viruses and other rogue executable code.

### SUMMARY OF THE INVENTION

The present invention is directed to a computer system and method of operating a computer system which provides enhanced data and program security. A system and  
10 method according to the invention limit access to computer system storage media by providing a locally operable switch which selectively prevents alteration to the local storage media. The switch may be a manually operable mechanical device or may be electronic, so long as its operation is isolated from the system being protected, and may be entirely self-contained. For example, the appropriate control lines between a hard disk controller  
15 and the hard disk drive are routed through a manually operable electrical switch which can only be manipulated locally and cannot be operated or bypassed under computer control. In one configuration, the appropriate write enabling conductor of the cable is physically interrupted by the mechanical switch when in a secure mode and, instead, the appropriate write disabling signal is applied to the hard disk drive. This basic configuration and method  
20 can be applied to various computer system architectures to support stand alone, multiuser and remote access capable computer systems.

According to another aspect of the invention, a computer system includes dual processor elements, one isolated from remote access and having facilities for writing information to a storage device. The other processor element, while handling  
25 communications with remote devices, is connected so as to positively inhibit writing or altering data contained in the storage device. To further protect system integrity, another aspect of the invention configures the communications processing element as a slave,

receiving and executing instructions from the isolated processing element. The invention further divides data storing and retrieval functions between a pair of hard disk drives used to provide remote access. Using this division, remote users may read from one hard disk drive, but are incapable of altering the contents of the read only drive. Similarly, remote  
5 users can write to the other hard drive, but cannot read information stored by other users and cannot target information for alteration or destruction.

According to an aspect of the invention, a digital computer system includes a processor, a storage device and a manually operative switch. The storage device is responsive to the processor for selectively operating in a read mode of operation for reading  
10 previously stored data and in a write mode of operation for storing data. The manually operative switch selectively disables the processor from causing the storage device to operate in the write mode of operation. According to a feature of the invention, the manually operative switch is connected to interrupt the control signal required to cause the storage device to operate in the write mode of operation. The manually operative switch  
15 may be in direct electrical contact with the storage device and may be in the form of a mechanical switch or may be an electronic switch including control software and hardware components.

According to another feature of the invention, the processor includes a central processing unit, a controller which is in direct electrical contact with the manually operative  
20 switch, and a bus which connects the central processing unit and the controller.

According to another aspect of the invention, a digital computer system includes a storage device, first and second central processing units, and a first manually operative switch. The storage device is responsive to a control signal for selectively operating in a read mode of operation for reading previously stored data and in a write mode of operation  
25 for storing data. The first and second central processing units are each capable of providing this control signal. The switch then alternatively provides the control signal from either the first or second central processing unit to the storage device. According to a feature of the invention, the system further includes a second manually operative switch selectively disabling the storage device from operating in the write mode of operation.



According to another aspect of the invention, a digital computer system includes a processor, a secure data storage device and a manually operative switch. The secure data device is responsive to a write control signal from the data processor for selectively storing data. The switch is manually selectable to enable and disable receipt by the secure data storage device of the write control signal.

According to a feature of the invention, the manually operative switch selectively applies a predetermined fixed control signal to the secure data storage device instead of the write control signal. The secure data device may be a non-volatile memory including a hard disk drive.

According to another feature of the invention, a bus connects the processor to the secure storage device for transmission of the control signal so that the manually operative switch selectively enables and disables a transmission of the control signal along the bus.

According to another feature of the invention, the processor includes a central processing unit and a disk controller connected to each other by a system bus. The secure data device includes a disk drive electrically connected through the manually operative switch to the disk controller for receiving the control signal so that the manually operative switch selectively enables and disables transmission of the control signal. Another disk drive may be included together with another disk controller connected to the system bus for selectively writing data to and reading data from the additional disk drive in the form of, for example, an array of multiple hard disk drives (e.g., redundant array of independent disks, or "RAID"). These additional disk drives may be connected independent of the manually operative switch or may be connected with a second manually operative switch to prevent writing to the additional disks.

According to another feature of the invention, the digital computer system further includes first and second disk controllers connected to respective master and slave central processing units by a system bus. The secure data storage device includes a first disk drive electrically connected through the manually operative switch to the first disk controller for receiving a control signal from the master central processing unit whereby the manually operative switch selectively enables and disables transmission of the control signal to the

first disk drive. The second disk drive is connected to the second disk controller and is accessible by the master and slave central processing units over the system bus. Alternatively, the first and second disk controllers may be included on separate buses accessible only by the respective master and slave central processing units.

5           According to another feature of the invention, a second manually operative switch is interposed between the second disk drive and the second disk controller to selectively disable reading from or, in an alternate configuration, writing to the second disk drive.

          According to another feature in the invention, the computer includes a third disk controller and disk drive with the disk drive operative to mirror data stored in the second  
10   disk drive.

          According to another feature in the invention, the computer system includes a first program memory connected to and storing instructions executable by the master central processing unit. A second program memory is connected to and stores instructions executable by the slave processing unit with a processor bus connecting the master and  
15   slave central processing units. A communications controller may be connected to the system bus to provide for remote access.

          According to another aspect of the invention, a computer system includes a processor, a manual switch and a data storage device. The switch is connected to selectively transmit a control signal received from the processor and, alternatively, a write  
20   inhibiting control signal. In response to the signal received from the switch, the data storage device selectively stores data or is inhibited from doing so.

          According to a feature of the invention, the storage device is responsive to the control signal transmitted by the manual switch for selectively operating in read and write modes of operation so that the write-inhibiting control signal causes the data storage device  
25   to operate only in the read mode of operation and/or other modes protecting the integrity of the data (e.g., internal refresh only).

          According to another feature of the invention, the processor includes a first disk controller and the data storage device is a first disk drive. According to another feature of

the invention, a second disk drive may also be connected to the first disk controller or may be connected to its own, second disk controller.

According to another aspect of the invention a digital computer system includes a processor, a storage device and a switch. The storage device is responsive to the processor for selectively operating in a plurality of operating modes including a read mode of operation for retrieving previously stored data and a write mode of operation for storing data. The switch is operable to selectively enable and disable at least one of the operating modes, the switch being controllable by means distinct and separate from the processor so that the processor is inhibited from controlling the operation of the switch. According to a feature of the invention, the switch may be manually operated to selectively make and break an electrical conducting path connecting the processor with the storage device. Alternatively, the switch may include a controller, an operation of which is independent of the processor for selectively enabling and disabling at least one of the operating modes. At least one of the operating modes may be a read mode of operation and, alternatively, may be a write mode of operation. According to a feature of the invention, a second "master" processor is isolated from the first processor and both (i) controls the switch and (ii) reads and writes to the storage device.

According to another feature of the invention, the storage device may include a magnetic media and comprise a disk drive or a magnetic tape. The storage device may alternatively include a non-volatile electronic memory device, such as an EEPROM. According to still a further feature, the storage device may include an optical storage device such as a CD-ROM or an electro-optical source device such as a CD-RW.

According to still another feature of the invention, the digital computer includes a processor with a first memory storing program instructions and a distinct and separate memory storing data. The first memory may be operable in the read only mode of operation in which the program instructions are protected from alteration and erasure by the central processing unit.

According to another aspect of the invention, a method of operating a digital computer system includes the steps of supplying a variable control signal to a disk drive and

writing data to the disk drive in response to the variable control signal. A manual electrical switch is operated so as to disconnect the variable control signal from the disk drive and instead, connect a fixed control signal to the disk drive. The disk drive is then operated in a mode other than a write mode of operation in response to the fixed control signal.

5 According to a feature of the method, remote access to the disk drive is provided only when operating in the mode other than the write mode of operation, i.e., in the secure mode inhibiting changes to the hard disk drive.

10 These and other features, aspects and advantages of the present invention will become better understood with regard to the following description, claims and accompanying drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of a computer system according to the prior art.

15 FIG. 2 is a block diagram of a computer system according to the invention including a switch for inhibiting a hard disk drive from operating in a write mode of operation and segmented main memory.

FIG. 3 is a pin-out diagram and table for an IDE connector.

FIGS. 4a and 4b are front and rear views of a stand alone switch device for insertion between a SCSI controller and one or more SCSI devices.

20 FIG. 5 is a flow diagram for a software implemented switch for restricting operation of designated peripheral devices to programmed modes of operation.

FIG. 6 is a block diagram of a computer system according to another embodiment of the invention including a switch for connecting a storage unit to a stand alone processing unit or to a processor providing for remote access.

25 FIG. 7 is a block diagram of a computer system according to another embodiment including isolated (i) secure local and (ii) remote processing systems sharing common hard disk facilities under the exclusive control of the secure local processor.

FIGS. 8a and 8b are front and rear views of a switching device for selectively connecting one of two SCSI controllers to a plurality of SCSI devices and for limiting operation of those SCSI devices to programmed modes of operation when connected to the second of the SCSI controllers.

5 FIG. 9 is a block diagram of a computer system according to the invention including a master/slave architecture.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring to FIG. 2 of the drawings, a computer system 200 includes conventional devices 102-124 as discussed in connection with the prior art with the (i) addition of switch  
10 202 interposed between SCSI controller 108 and hard disk drive 110 and (ii) partitioning of main memory into separate instruction memory 106a and data memory 106b. Instruction memory 106a may include various forms and levels of protection. For example, instruction memory 106a may be implemented in the form of an EEPROM with a manual erase and programming feature. Thus, CPU 104 would have read-only access to instruction memory  
15 106a unless and until the associated EEPROM was manually provided with the proper control signals to allow its programming. This feature prevents unauthorized modification of programming and provides security against viruses attacking the program code. In contrast, data memory 106b is a conventional RAM for the temporary storage of data, including system and application program parameters and variables.

20 Switch 202 may be configured as a part of SCSI controller 108, hard disk drive 110, or as a separate auxiliary device. Switch 202 may be exclusively manually operable to inhibit a hard disk drive from altering or erasing data. Alternatively, switch 202 may be an electronic switch controlled by a control signal physically inaccessible to or by CPU 104. Typically, hard disk drive 110 is responsive to read and write requests from SCSI controller  
25 108. Switch 202 is effective to selectively inhibit operation in the write (or read) mode so that, effectively, hard disk drive 110 can be operated in either a read/write mode, or in a read only or write only mode of operation.

If switch 202 is included as part of SCSI controller 108, then it is connected to inhibit write requests from CPU 104 (or other devices) from being sent to hard drive 110. If switch 202 is instead incorporated into hard drive 110, it can be connected to inhibit operation of hardware used to operate the disk drive's write heads. For example, the switch  
5 202 can be configured to cut power to a write head's output circuitry. Preferably, hard disk drive 110 and/or SCSI controller 108 provide the appropriate status and/or error messages to CPU 104 when operating in a write inhibited or read only mode of operation or when a write operation is requested and the write mode has been disabled.

Switch 202 may also be configured as an auxiliary, stand alone device mounted in  
10 a switch box enclosure with appropriate terminals for connecting controller 108 to hard disk drive 110. In this configuration, switch 202 is operative in a first read/write position to pass signals from controller 108 to hard disk drive 110 without change. In a write inhibit or read only mode of operation, switch 202 will not pass signals from controller 108 to hard disk drive 110 which would cause hard disk drive 110 to be placed in a write mode of operation.  
15 For example, pin 50 of a SCSI interface may be set to the appropriate logic level when a selected device is accessed so as to limit operation of the selected device to either a read or write mode as appropriate. Alternatively, switch 202 may be connected between IDE controller 120 and hard disk drive 122 to selectively restrict access and control of the latter. Using an IDE interface, a pin-out diagram for which is shown in FIG. 3 of the drawings,  
20 write strobes from the controller are transmitted to the hard drive on pin 23. That is, the controller signals the hard drive that data supplied on pins 3-18 is ready to be written by driving a control signal applied at pin 23 to a "low" logic level. Thus, in a secure mode of operation wherein writing to a hard drive is to be inhibited, pin 23 is connected to a high level logic signal source so that the hard disk drive does not receive the write strobe signal  
25 necessary to cause it to perform a write operation.

Alternatively, switch 202 may include appropriate hardware and software to monitor signals transmitted by controller 108 to hard disk drive 110. Write (or other inhibited actions such as read, erase, etc.) commands to one or more designated devices would be recognized and intercepted, switch 202 generating an appropriate error message back to

controller 108. Permissible operations would be transmitted through to disk drive 110 without impediment. In this software implementation of switch 202, predetermined portions of disk drive 202 may be designated as secure so that write commands are selectively inhibited only to designated tracks, sectors, clusters, etc.

5           FIGS. 4a and 4b show a stand alone, programmable embodiment of switch 202 which can accommodate eight peripheral devices on a SCSI interface. Switch 202 is mounted in enclosure 210 and includes panel mounted programming switches 212a-212h associated with respective SCSI devices 0-7. Each of the programming switches is selectable to designate a read only, read/write, or write only mode of operation for the  
10           respective device. Once programmed, the status of each device is indicated by a tricolor LED 214 associated with each switch, green, for example, indicating read/write capabilities, yellow that the corresponding device can be operated in a read only mode of operation (write-inhibited), and red indicating that the corresponding device is operable in a write only mode of operation (i.e., read operations are inhibited). As shown in FIG. 4a, devices  
15           0 and 1 are being operated in write only modes (i.e., a "secure" mode), devices 2 and 4 in read only modes (another "secure" mode), and devices 3, 5, 6, and 7 in read/write modes (i.e., are not being operated as "secure" devices).

          A key switch 216 may be included to control the operation of switch 202. In the  
20           "OUT" mode, the switch is functionally inoperative so that the operations of all devices are unrestricted as would be indicated by green status lights 214. In the "SECURE" mode, the programmed mode limits would be effective to limit read and write modes of operations. The "SET" mode is used to program switch 202 according to switches 212a-212h. A corresponding key (not shown) is removable from key switch 216 in the "OUT" and  
25           "SECURE" positions so that switch 202 can be left locked and unattended. Preferably, the "SET" position of key switch 216 is a temporary position with a spring returned to the "SECURE" position upon completion of programming. When switch 216 is in the "SET" mode, the position of switches 212a-212h are read and the corresponding mode limitations are stored in memory as would be indicated by status indicators 214.

A rear view of switch 202 is presented in FIG. 4b including panel mounted SCSI connectors 220 and 222 for connecting the switch to a SCSI controller and to SCSI devices being controlled, respectively.

The operation of switch 202 is shown in the flow diagram of FIG. 5. The program starts at entry point 300 with an initial decision box 302 handling the set mode of operation for programming the device. If switch 202 is in the "SET" mode, then the positions of switches 212a-212h are read at process 304 and the corresponding limitations are stored in memory at process 306. If the SET operation has not been activated, or upon completion of the programming, processing continues at step 308 where the numbers of the secure devices are read from memory together with the corresponding allowed modes or inhibited modes of operations, as appropriate. In response to receipt of a control signal at decision 10 310, the program decides if the control signal is directed to a secure device, i.e., a device number previously stored as being operated in a "SECURE" mode with either read or write operations inhibited. If the control signal is directed to a device which is not subject to read or write limitations, such as devices 3, 5, 6, and 7 according to FIG. 4a, then the control signal is transmitted to that device at process 316. However, if the control signal is directed to a device which is being operated in a secure mode of operation (devices 0, 1, 2, and 4 in this example), then the process determines at decision box 312 if the requested operation has been inhibited. For example, device numbers 0 and 1 are being operated in a read-inhibited mode while devices 2 and 4 are being operated in a write-inhibited mode. Accordingly, read requests directed to devices 0 or 1 and write requests directed to devices 2 and 4 would result in the left branch being taken out of decision point 312 where the appropriate control signal would be inhibited and an error message generated back to the requesting controller. Conversely, if the operation requested has not been inhibited, the right branch is taken out of decision box 312 and the request is transmitted to the device address. In either case, process flow continues down to terminal 318. At this point, the process would conventionally loop back to Start 300 to continuously detect and process programming requests and SCSI interface commands.



Another embodiment of the invention is shown in FIG. 6 depicting a dual processor system, with both read/write and read only hard drives, each having a dedicated bus, local memory and storage. A hard drive storage system is switchable between the processors. The hard drive storage system includes two disk drives, operable in a non-secure normal mode of operation in which both drives area read/write enabled, and in a protected mode wherein one drive is operated in a read only mode and the other in a write only mode of operation. In this configuration, the two processors are isolated from each other, one of the processors providing for local system operation, the other providing remote access to the mass storage devices including hard disk drives. In effect, the system is equivalent to two separate independent systems on one motherboard when configured as a personal computer (PC). Both systems require software to be loaded, and some system configuration to be performed. Communications between the processors is provided by the common hard drive storage system.

Operator monitoring of the system performance and downloading of data acquired by the system is performed by a primary CPU 104a connected to a first local system bus 102a. The second local data bus 102b supports a communications CPU 104b. Connected to both buses 102a and 102b, switch 204 physically switches SCSI controller 108b between the two buses. Hard disk drives 110a and 110b are connected and controlled by SCSI controller 108b through write mode disabling switch 202a and read mode disability switch 202b, respectively. Switchable SCSI controller 108b would be switched to main local system bus 102a for loading and configuration of software under control of main CPU 104a. After loading and testing of software, SCSI controller 108b would be switched to local system bus 102b supporting communications with remote users over serial port 118b and Ethernet 124. Hard disk drive 110a would be then operated in a read only mode of operation by switch 202a. Conversely, hard disk drive 110b would be operated in a “write only” mode of operation so that, for example, any uploaded data could be checked for viruses prior to that data becoming available to the system. Further, by placing hard disk drive 110b in a “write only” mode of operation using switch 202b, data uploaded to the drive by remote users of the system cannot be accessed by other remote users thereby

enhancing system security. This feature is particularly useful for e-commerce applications where confidential data received from remote user must be protected from unauthorized dissemination (e.g., credit card information, etc.).

In the configuration of FIG. 6, the primary CPU 104a and associated first bus 102a are inaccessible to remote users. Accordingly, switch 204 and switches 202a and 202b may be electronically controlled by primary CPU 104a without jeopardizing the security of the system. This feature is incorporated into the configuration shown in FIG. 7 wherein SCSI controllers 108c and 108d are connected to respective first and second buses 102a and 102b. Switch 206 is controlled by CPU 104a via serial port 118b connected to first bus 102a. Switch 206 selectively connects either SCSI controller 108c or 108d to SCSI hard disk drives 110a and 110b.

In a local mode of operation, switch 206 provides unlimited access by local SCSI controller 108c to hard disk drives 110a and 110b. Thus, CPU 104a can both read from and write to the drives. Upon being commanded to connect the drives to second bus 102b to support remote access, switch 206 disconnects SCSI controller 108c and connects SCSI controller 108d to the drives subject to preprogrammed operating mode limitations. For example, when being accessed by SCSI controller 108d, hard disk drive 110a may be write inhibited while hard disk drive 110b may be read inhibited as described in connection with the configuration of FIG. 6.

FIGS. 8a and 8b show an alternate implementation of a stand alone switch 210 suited to the dual processor system shown in FIG. 7. The output of SCSI controller 108c, which is connected to local system bus 102a, is provided to connector 230 while SCSI controller 108d, which is connected to local system bus 102b, is connected to connector 232. A serial connector 236 provides an interface for optional computer control of the switch.

In this configuration, switch 210 both switches hard disk drives 110a and 110b between the appropriate SCSI controller and selectively operates the hard disk drives in the pre-programmed restricted modes of operation. As shown, key switch 218 has five positions including "EXTERNAL", "OFF", "LOCAL", "REMOTE", and "SET". In the

“OFF” mode, neither of the SCSI controllers have access to peripheral devices including the hard disk drives. In the “LOCAL” position, signals from and to connector 230 are passed through without alteration to SCSI devices connected to connector 234. This mode is applicable to unrestricted operation of the peripheral devices when under control of primary CPU 104a which is inaccessible by remote users.

When key switch 218 is placed in the “REMOTE” position, connector 232 provides access to SCSI devices connected at connector 234 under the control and supervision of switch 210 to selectively inhibit predetermined modes of operation according to stored programming and as indicated by status indicator lights 214. As previously described, a temporary, spring loaded “SET” position is provided for programming switch 210 according to the positions of switches 212a-212h.

The “EXTERNAL” position allows a secure device, such as primary CPU 104a, to program and control switch 204 via a serial RS-232 interface. Thus, so long as the security of primary CPU 104a is not breached, the operating integrity of switch 202 is maintained.

Another embodiment in the invention including dual processors in a master/slave relationship is shown in the block diagram of FIG. 9. According to this embodiment, one processor manages communications including, for example, responding to requests from the Internet. However, the slave processor only accepts program instructions from the primary processor. This can be accomplished by appropriate programming of the system firmware (e.g., BIOS) of the slave processor. Thus, the slave processor is controlled only by the master processor and would not be accessible by a remote computer hacker.

Referring to FIG. 9, a master central processing unit 104a is connected to dedicated main memory 106a including an operating system. Master central processing unit 104a is connected via local system bus 102a to various devices including (1) hard disk drive 110a through SCSI controller 108a; (2) video control board 114 and video monitor 116; (3) serial port 118a; and (4) hard disk drive 122 through IDE controller 120. Slave central processing unit 104b provides remote access functions and is connected to a local main memory 106b. Central processing unit 104b connects to SCSI controller 108b, serial port 118b and Ethernet 124 through local system bus 102b. In turn, SCSI controller 108b connects to hard

disk drive 110b and, via selectable "read only" switch 202, to hard disk drive 110c. As previously mentioned, slave central processing unit 104b obtains operating instructions exclusively from master central processing unit 104a so that viruses or other changes cannot be remotely made to its operating instructions or programming. Critical data that is to be  
5 protected from change or deletion by remote users is stored in hard disk drive 110c operated in a read only mode of operation. Hard disk drive 110b supports storage of data by remote users, such as required for e-commerce, etc.

According to the invention as illustrated by the embodiments described, the capability of writing to and altering data is disabled for remote users by disabling hard disk  
10 write capabilities, limiting remote user access to a dedicated and segregated data processor and associated bus and data storage, and by isolating control of a communications processor so that instructions are only executed as received from a secured master processor. The invention further enhances security by segregating read and write functions to different hard drives so that remote users cannot alter information previously stored on the system nor can  
15 they read information stored by other remote users.

Although the present invention has been described in considerable detail with reference to certain preferred embodiments thereof, other embodiments or configurations are possible. For example, the mode limiting switch is applicable to other storage devices and media and to other devices where selection and control of operating modes must be  
20 restricted. For example, a restricted user may be limited by the switch to monitoring the output of a device such as a video camera, while a local user may additionally control the camera. Similarly, the switch may be used in-line with a printer to allow limited printing capabilities for certain users while providing full capabilities to local users of the system. Accordingly, the spirit and scope of the appended claims should not be limited to the  
25 description of the preferred embodiments contained herein.

What is claimed is:

- 1 1. A digital computer system comprising:  
2 a processor;  
3 a device responsive to said processor for selectively operating in a plurality of  
4 modes; and  
5 a manually operative switch selectively disabling said processor from causing said  
6 device to operate in one of said modes.
  
- 1 2. The digital computer system according to claim 1 wherein said device comprises  
2 a storage device operable in (i) a read mode of operation for reading previously stored data  
3 and (ii) a write mode of operation for storing data
  
- 1 3. The digital computer system according to claim 2 wherein said manually operative  
2 switch is connected to interrupt a control signal required to cause said storage device to  
3 operate in said write mode of operation.
  
- 1 4. The digital computer system according to claim 3 wherein said processor includes:  
2 a central processing unit;  
3 a controller connected to said manually operative switch; and  
4 a bus connecting said central processing unit and said controller.

1 5. The digital computer system according to claim 1 wherein said processor includes  
2 an interprocessor bus and master and slave central processing units connected to each other  
3 by said interprocessor bus and connecting to respective first and second buses, said first bus  
4 electrically isolated from said second bus.

1 6. A digital computer system comprising:  
2 a storage device responsive to a control signal for selectively operating in (i) a read  
3 mode of operation for reading previously stored data and (ii) a write mode of operation for  
4 storing data;  
5 first and second central processing units each capable of providing said control  
6 signal;  
7 a first manually operative switch alternatively providing said control signal from  
8 said first and second central processing units to said storage device.

1 7. The digital computer system according to claim 6 further comprising a second  
2 manually operative switch selectively disabling said storage device from operating in said  
3 write mode of operation.

1 8. The digital computer system according to claim 6 further comprising second and  
2 third switches wherein said storage device includes first and second disk drives, said second  
3 switch selectively inhibiting said first disk drive from operating in said write mode of

4 operation, said third switch selectively inhibiting said second disk drive from operating in  
5 said read mode of operation.

1 9. The digital computer system according to claim 8 further comprising first and  
2 second buses respectively connected to said first and second processing units and a  
3 communications interface providing remote access to said second bus.

1 10. The digital computer system according to claim 6 further comprising switching  
2 means and wherein said storage device includes first and second disk drives, said switching  
3 means having a first state wherein said first and second disk drives are operable in both said  
4 read and write modes of operation and a second state inhibiting operation of said first drive  
5 in said write mode and of said second drive in said read mode.

1 11. The digital computer system according to claim 10 further comprising a  
2 communications interface providing remote access to said second central processing unit.

1 12. The digital computer system according to claim 6 further comprising switching  
2 means wherein said storage device includes first and second disk drives, said switching  
3 means having a first state wherein said first and second disk drives are operable in both said  
4 read and write modes and a second state causing said first disk drive to be operated only in  
5 said read mode of operation and said second drive only in said write mode of operation.

1 13. The digital computer system according to claim 12 further comprising a  
2 communications interface providing remote access to said second central procession unit.

1 14. A digital computer system comprising:  
2 a processor;  
3 a secure data storage device responsive to a write control signal from said processor  
4 for selectively storing data; and  
5 a manually operative switch selectively enabling and disabling receipt by said  
6 secure data storage device of said write control signal.

1 15. The digital computer system according to claim 14 wherein said manually operative  
2 switch selectively applies a predetermined fixed control signal to said secure data storage  
3 device instead of said write control signal.

1 16. The digital computer system according to claim 14 wherein said secure data storage  
2 device comprises a non-volatile memory.

1 17. The digital computer system according to claim 14 wherein said secure data storage  
2 device comprises a hard disk drive.

1 18. The digital computer system according to claim 14 further comprising a bus  
2 connecting said processor to said secure data storage device for transmission of said control



3 signal wherein said manually operative switch selectively enables and disables a  
4 transmission of said control signal along said bus.

1 19. The digital computer system according to claim 18 wherein said secure data storage  
2 device comprises a hard disk drive.

1 20. The digital computer system according to claim 14 wherein said processor further  
2 includes a central processing unit and a disk controller connected to each other by a system  
3 bus and said secure data storage device comprises a disk drive electrically connected  
4 through said manually operative switch to said disk controller for receiving said control  
5 signal whereby said manually operative switch selectively enables and disables a  
6 transmission of said control signal.

1 21. The digital computer system according to claim 20 further comprising another disk  
2 drive and wherein said processor further includes another disk controller connected to said  
3 system bus and to said another disk drive for selectively writing data to and reading data  
4 from said another disk drive.

1 22. The digital computer system according to claim 21 wherein said another disk drive  
2 comprises an array of hard disk drives.

1 23. The digital computer system according to claim 20 further comprising another disk  
2 drive connected to said disk controller independent of said manually operative switch.

1 24. The digital computer system according to claim 14 wherein said processor further  
2 includes master and slave central processing units,

3 said digital computer system further comprising

4 (i) a system bus;

5 (ii) first and second disk controllers connected to said master and slave  
6 central processing units by said system bus, said secure data storage device  
7 including a first disk drive electrically connected through said manually  
8 operative switch to said first disk controller for receiving said control signal  
9 from said master central processing unit whereby said manually operative  
10 switch selectively enables and disables transmission of said control signal;

11 and

12 (iii) a second disk drive connected to said second disk controller.

1 25. The digital computer system according to claim 24 further comprising a third disk  
2 controller and a third disk drive connected to said third disk controller, said third disk drive  
3 operative to mirror data stored on said second disk drive.

1 26. The digital computer system according to claim 24 further comprising

2           a first program memory connected to and storing instructions executable by said  
3 master central processing unit,  
4           a second program memory connected to and storing instructions executable by said  
5 slave central processing unit, and  
6           a processor bus connecting said master and slave central processing units.

1   27.   The digital computer system according to claim 20 further comprising a  
2 communications controller connected to said system bus.

1   28.   A digital computer system comprising:  
2           a first central processing unit;  
3           a first system bus connected to said first central processing unit;  
4           a second central processing unit;  
5           a second bus connected to said second central processing unit and electrically  
6 isolated from said first system bus;  
7           a disk controller;  
8           a manual switch selectively providing a conductive path between the disk controller  
9 and, in a first position, said first system bus and, in a second position, said second system  
10 bus; and  
11           a hard disk drive connected to said disk controller and responsive to a write control  
12 signal from said disk controller for selectively storing information.

1 29. The digital computer system according to claim 28 further comprising another  
2 manual switch interposed between said disk controller and said hard disk drive for  
3 selectively transmitting said write control signal from said disk controller to said hard disk  
4 drive.

1 30. A digital computer system comprising:  
2 a processor;  
3 a device responsive to said processor for selectively operating in a plurality of  
4 operating modes; and  
5 a switch operable to selectively enable and disable at least one of said operating  
6 modes, said switch controllable by means distinct and separate from said processor whereby  
7 said processor is inhibited from controlling said operation of said switch.

1 31. The digital computer system according to claim 30 wherein said switch comprises  
2 a manually operated switch connected to selectively make and break an electrically  
3 conducting path connecting said processor and said device.

1 32. The digital computer system according to claim 30 wherein said switch comprises  
2 a controller, an operation of which is independent of said processor for selectively enabling  
3 and disabling said at least one of said operating modes.

1 33. The digital computer system according to claim 30 wherein said device comprises  
2 a data storage device operable in (i) a read mode of operation for retrieving previously  
3 stored data and (ii) a write mode of operation for storing data.

1 34. The digital computer system according to claim 33 wherein said at least one of said  
2 operating modes is said read mode of operation.

1 35. The digital computer system according to claim 33 wherein said at least one of said  
2 operating modes is said write mode of operation.

1 36. The digital computer according to claim 33 wherein said storage device comprises  
2 a magnetic media.

1 37. The digital computer according to claim 33 wherein said storage device comprises  
2 a disk drive.

1 38. The digital computer according to claim 33 wherein said storage device comprises  
2 a magnetic tape.

1 39. The digital computer according to claim 33 wherein said storage device comprises  
2 a non-volatile electronic memory device.

1 40. The digital computer according to claim 39 wherein said electronic non-volatile  
2 electronic memory device comprises an EEPROM.

1 41. The digital computer according to claim 33 wherein said storage device comprises  
2 an optical storage device.

1 42. The digital computer according to claim 33 wherein said storage device comprises  
2 an electro-optical storage device.

1 43. The digital computer according to claim 33 wherein said processor includes a  
2 central processing unit, a first memory storing program instructions and a second memory,  
3 separate and distinct from said first memory, storing data.

1 44. The digital computer according to claim 43 wherein said first memory is operable  
2 in a read-only mode of operation in which said program instructions are protected from  
3 alteration and erasure by said central processing unit.

1 45. A method of operating a digital computer system comprising the steps of:  
2 supplying a first control signal to a disk drive;  
3 selectively operating a disk drive in a write mode of operation in response to said  
4 first control signal;  
5 storing data on said disk drive operating in said write mode;

6           operating a manual switch to isolate said disk drive from circuitry capable of  
7 causing said disk drive to operate in said write mode;

8           operating said disk drive in a mode other than said write mode of operation in  
9 response to said operating step.

1    46.    A method of operating a digital computer system comprising the steps of:

2           operating a mass storage device in a write mode of operation;

3           storing data on said mass storage device;

4           operating a manual control to inhibit operation of said mass storage device in said  
5 write mode of operation;

6           providing remote access to said mass storage device only when said mass storage  
7 device is inhibited from operating in said write mode of operation.

1    47.    The method of claim 46 further comprising the steps of:

2           connecting said mass storage device to a first processor for storing data;

3           disconnecting said mass storage device from said first processor and, instead,

4           connecting said mass storage device to a second processor; and

5           controlling a transmission of data to a remote device using said second processor.

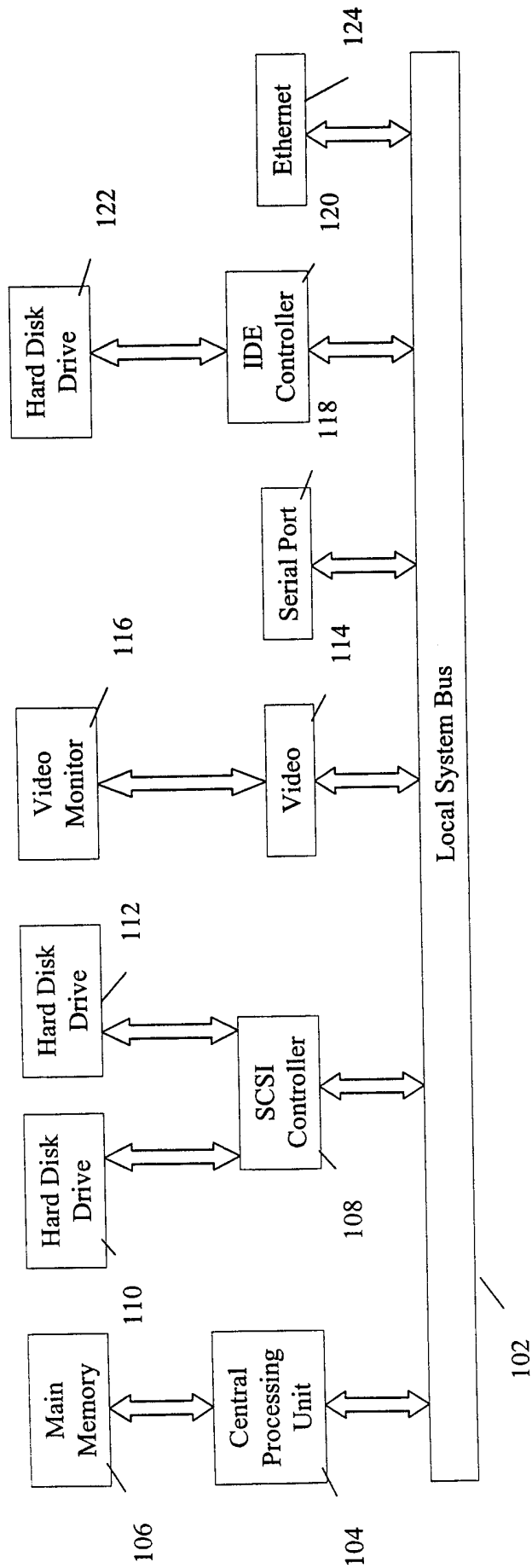
1    48.    The method of claim 46 further comprising the steps of:

2           storing instructions in a memory;

3           accessing said instructions using a first processor;

- 4 transmitting said instructions to a second processor;
- 5 wherein said step of providing remote access is performed by said second processor.





PRIOR ART

Figure 1

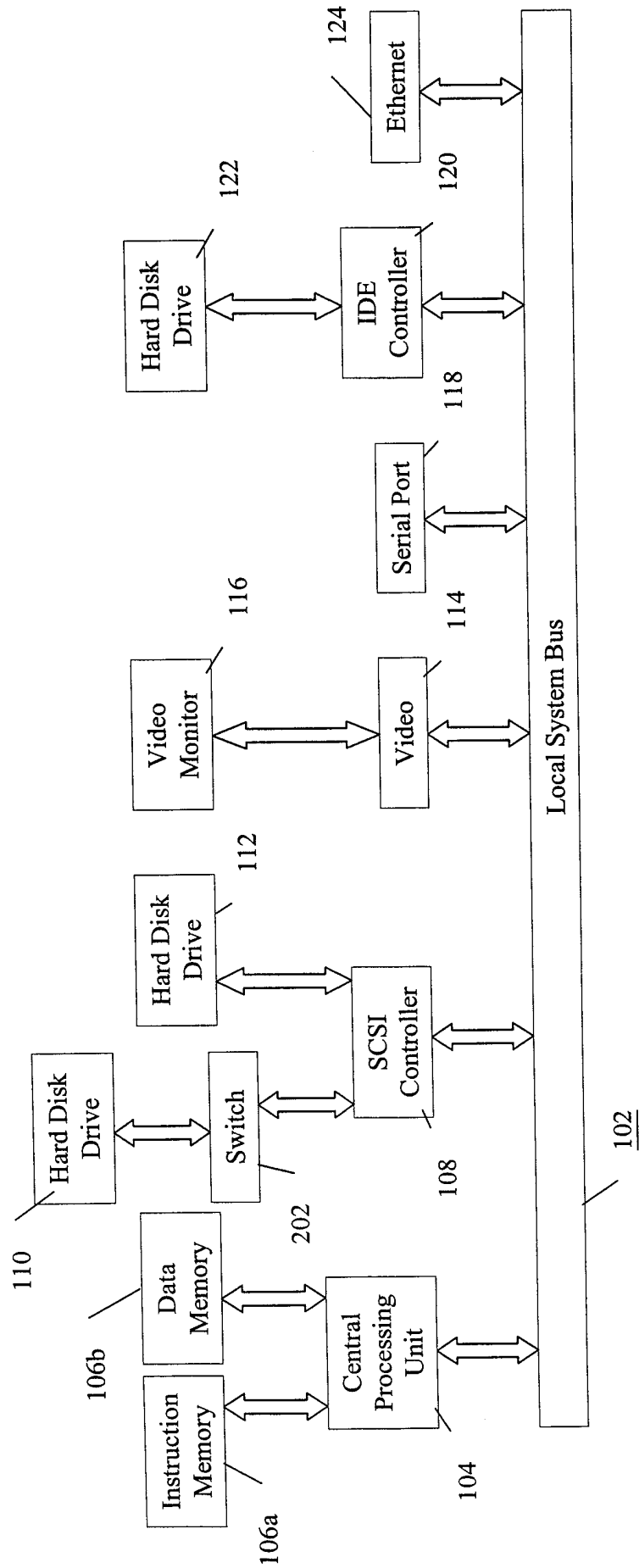
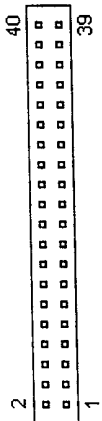
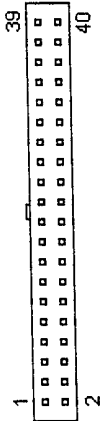


Figure 2

IDE Pinout at controller and peripheral



IDE Pinout at cable



Pin	Name	Dir.	Description
31	IRQR	←	Interrupt Request
32	/IOCS16		I/O Chip Select
33	DA1		Address
34	N/C		Not Connected
35	DA0	↑	Address 0
36	DA2	↑	Address 2
37	/IDE_CS0	↑	(1F0-1F7)
38	/IDE_CS1	↑	(3F6-3F1)
39	/ACTIVE	↑	Led Driver
40	GND		Ground

Pin	Name	Dir.	Description
16	DD14	↑	Data 14
17	DD0	↑	Data 0
18	DD15	↑	Data 15
19	GND		Ground
20	KEY		Key
21	N/C		Not Connected
22	GND		Ground
23	/IOW	↑	Write Strobe
24	GND		Ground
25	/IOR	↑	Read Strobe
26	GND		Ground
27	IO_RDY	←	I/O Channel
28	ALE	↑	Address Latch
29	N/C		Not Connected
30	GND		Ground

Pin	Name	Dir.	Description
1	/RESET		Reset
2	GND	↑	Ground
3	DD7	↑	Data 7
4	DD8	↑	Data 8
5	DD6	↑	Data 6
6	DD9	↑	Data 9
7	DD5	↑	Data 5
8	DD10	↑	Data 10
9	DD4	↑	Data 4
10	DD11	↑	Data 11
11	DD3	↑	Data 3
12	DD12	↑	Data 12
13	DD2	↑	Data 2
14	DD13	↑	Data 13
15	DD1	↑	Data 1

Figure 3

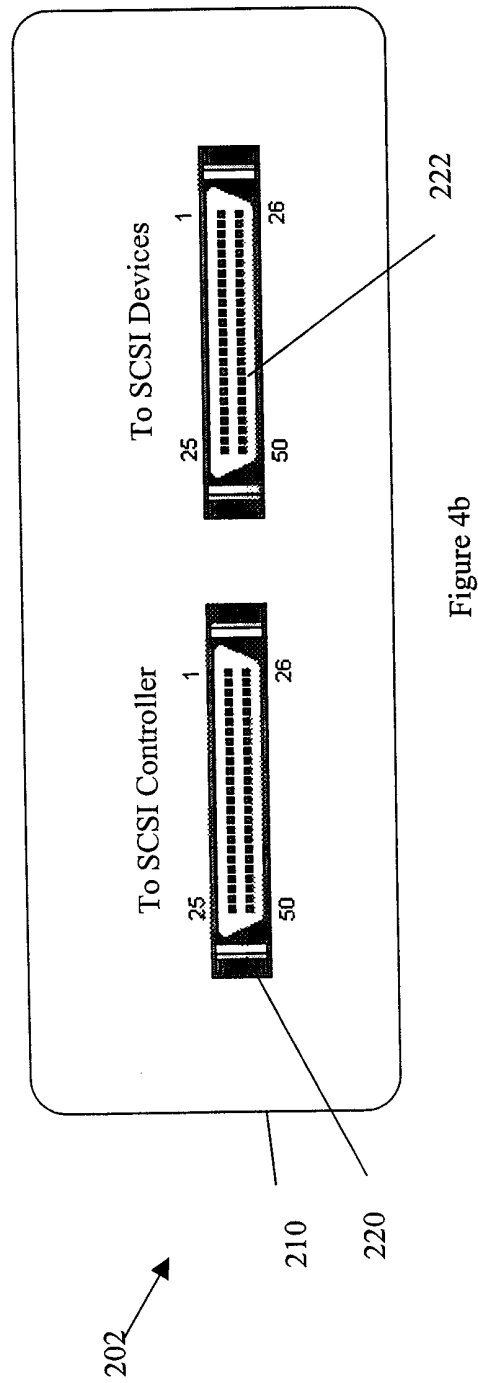
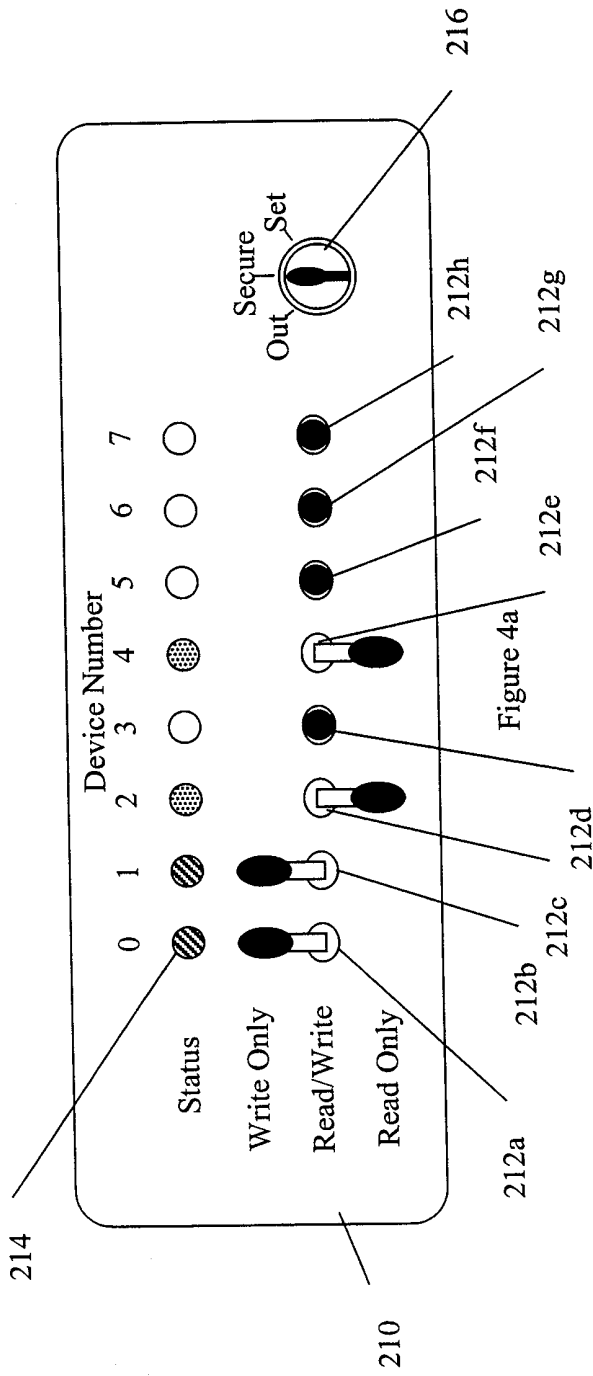


Figure 4b

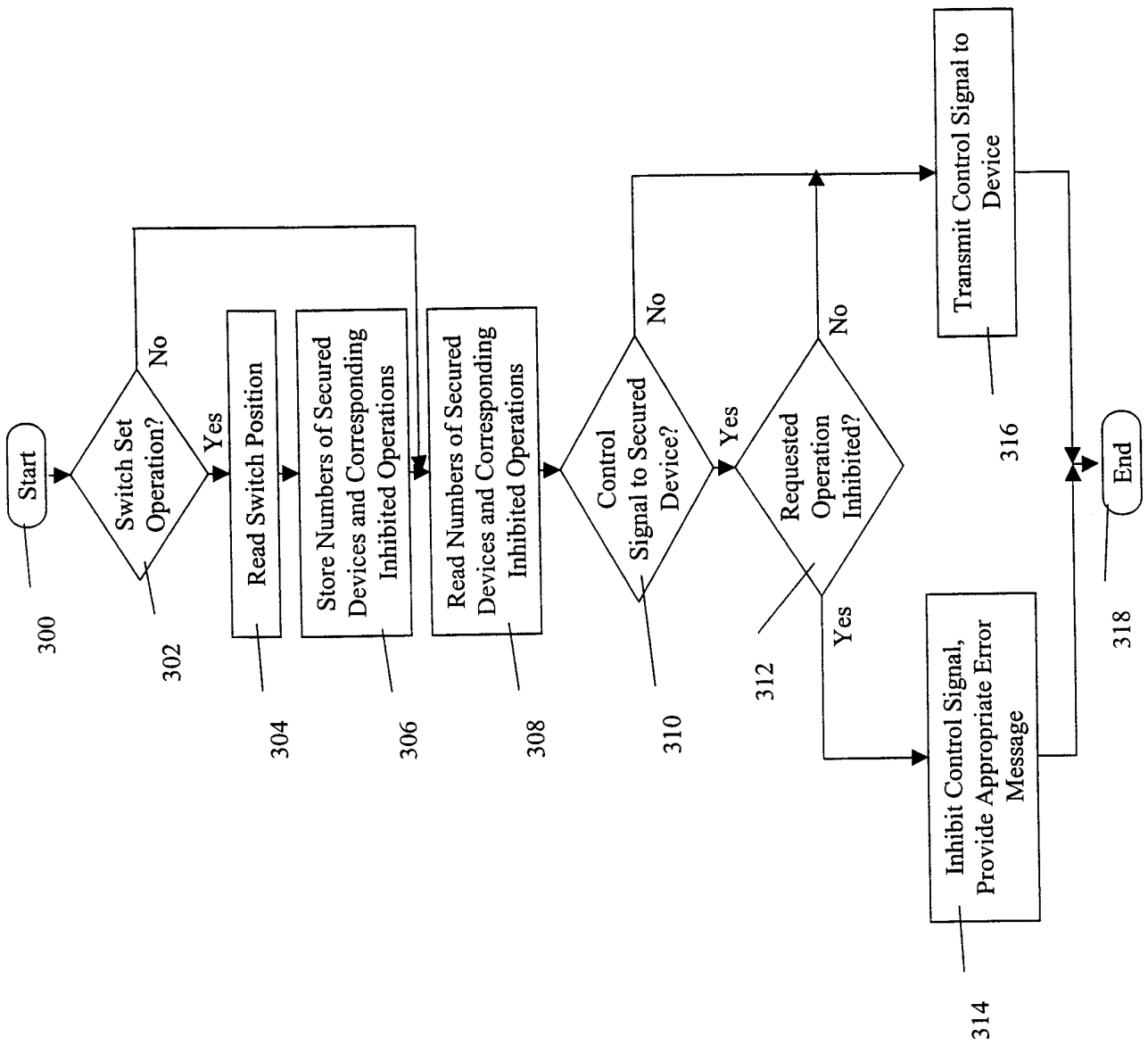


Figure 5

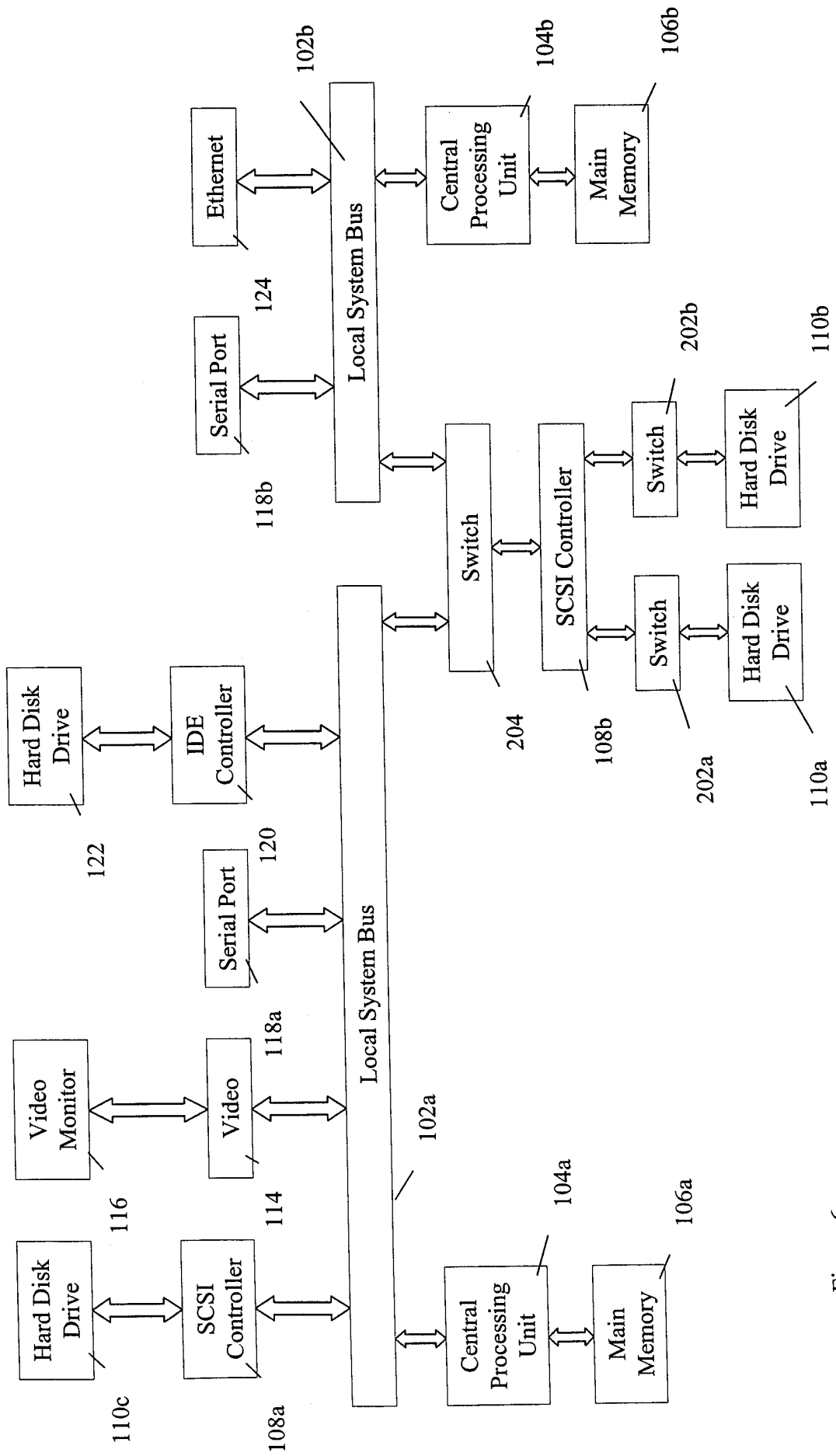


Figure 6

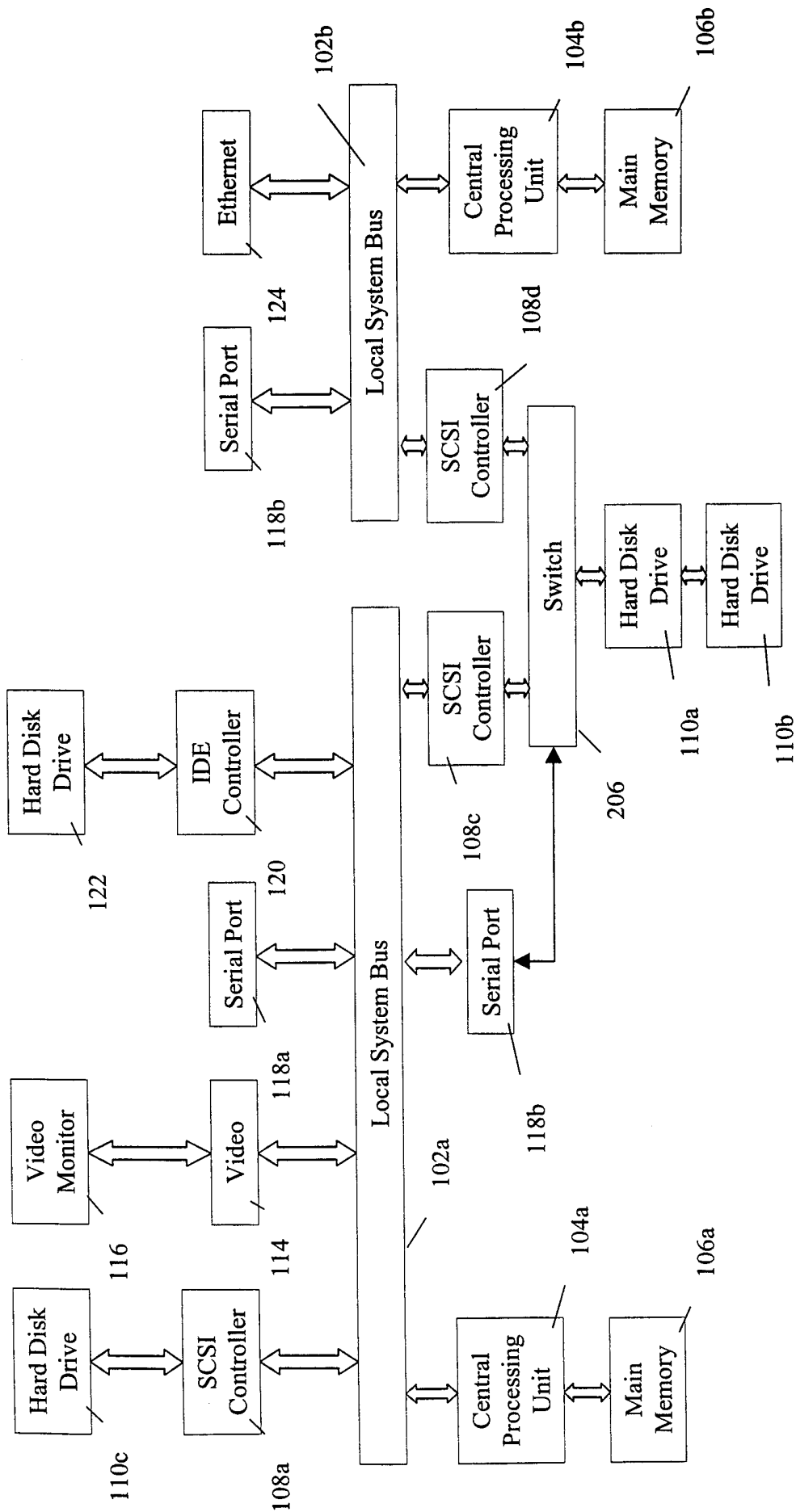


Figure 7

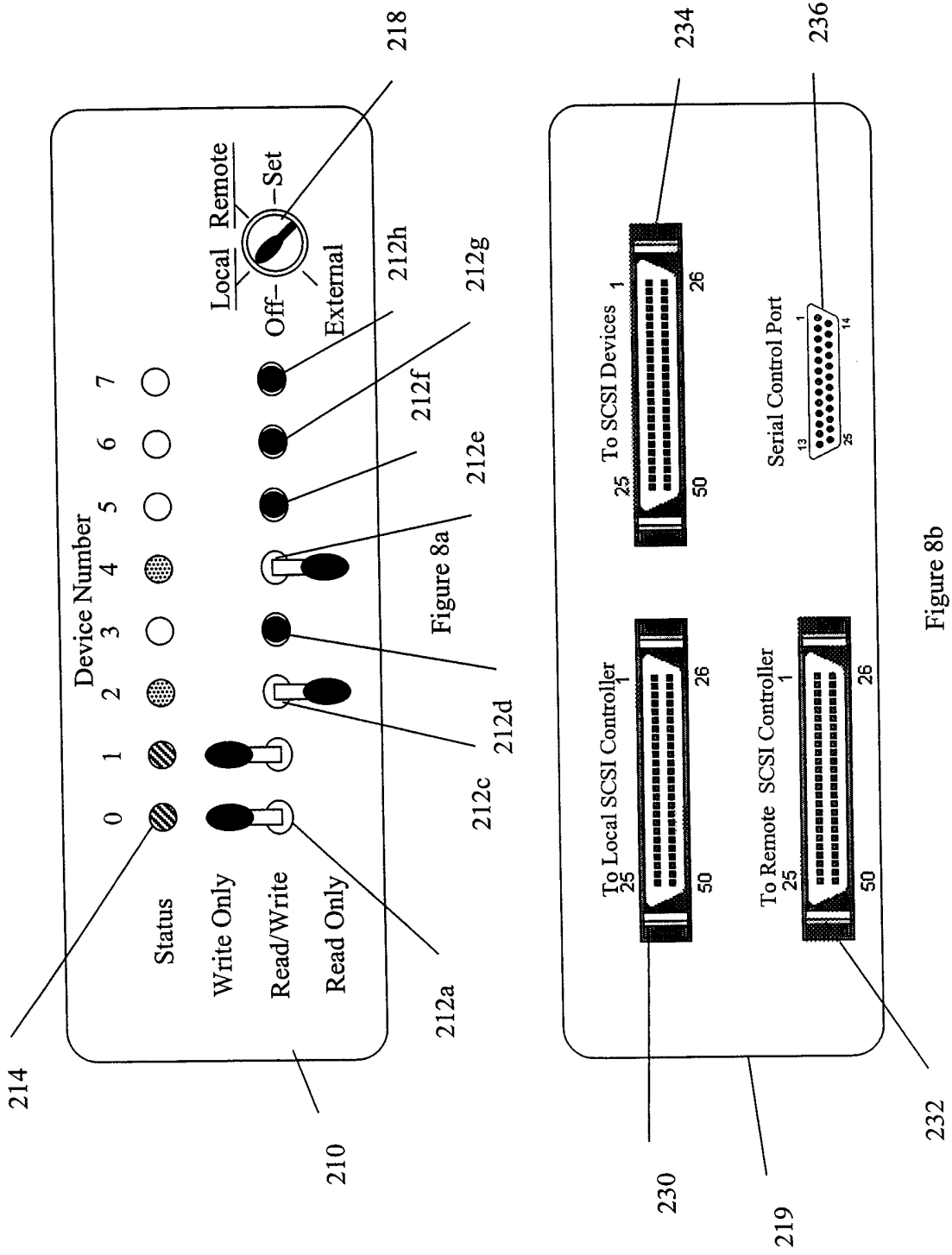


Figure 8b



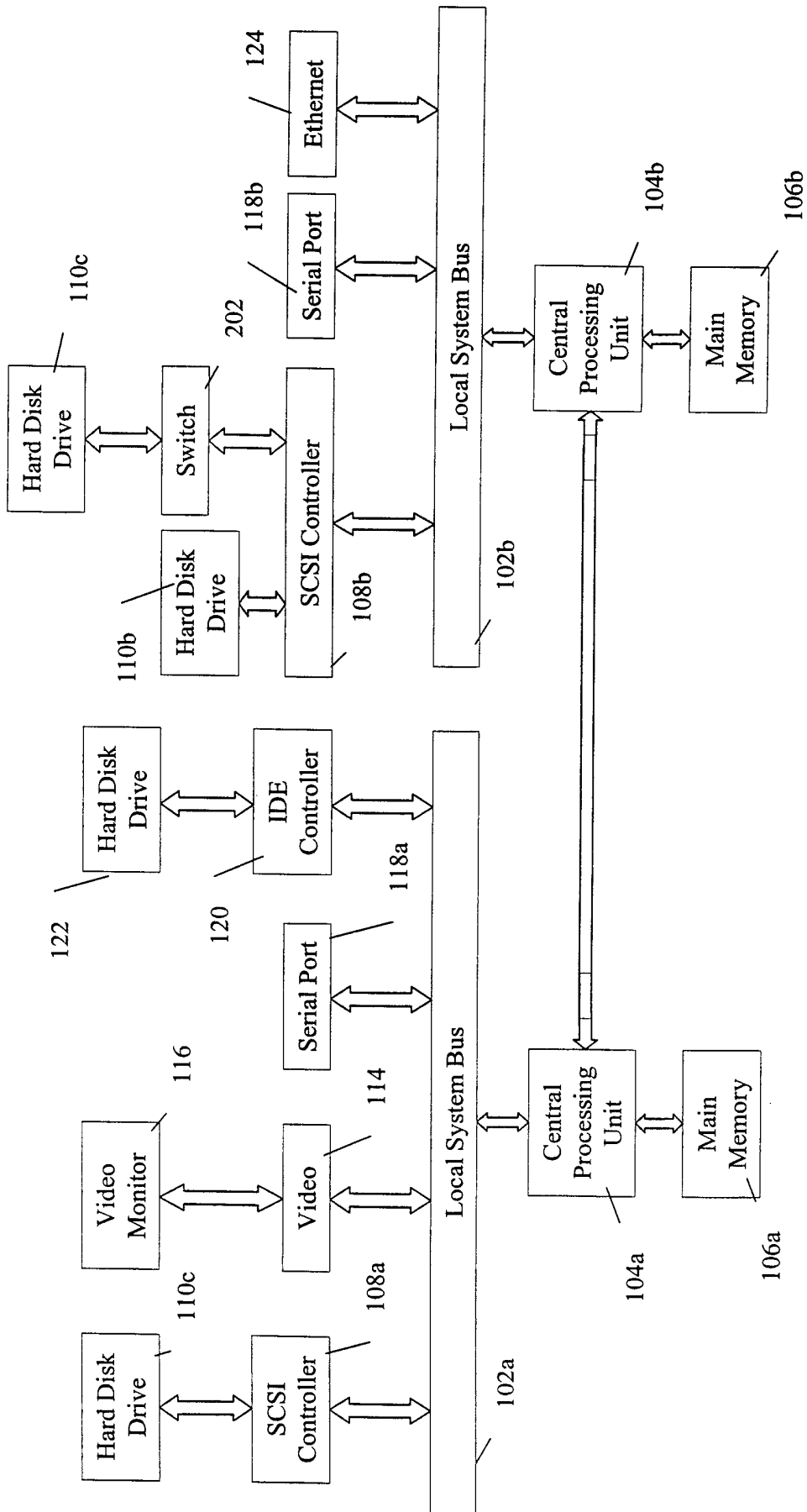


Figure 9