

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
18 December 2003 (18.12.2003)

PCT

(10) International Publication Number
WO 2003/104991 A3

(51) International Patent Classification⁷: **G06F 11/00**, 1/24

(21) International Application Number:
PCT/IB2003/002113

(22) International Filing Date: 5 June 2003 (05.06.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
102 25 471.0 10 June 2002 (10.06.2002) DE

(71) Applicant (for DE only): **PHILIPS INTELLECTUAL
PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-
damm 94, 20099 Hamburg (DE).

(71) Applicant (for AE, AG, AL, AM, AT, AU, AZ, BA, BB, BE,
BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CY, CZ, DK,
DM, DZ, EC, EE, ES, FI, FR, GB, GD, GE, GH, GM, GR,
HR, HU, ID, IE, IL, IN, IS, IT, JP, KE, KG, KP, KR, KZ,
LC, LK, LR, LS, LT, LU, LV, MA, MC, MD, MG, MK, MN,
MW, MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD,
SE, SG, SK, SL, SZ, TJ, TM, TN, TR, TT, TZ, UA, UG,

UZ, VC, VN, YU, ZA, ZM, ZW only): **KONINKLIJKE
PHILIPS ELECTRONICS N.V.** [NL/NL]; Groenewoud-
seweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **WAGNER, Martin**
[DE/DE]; c/o Philips Intellectual Property & Standards
GmbH, Weissshausstr. 2, 52066 Aachen (DE). **MUTH,
Matthias** [DE/DE]; c/o Philips Intellectual Property &
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

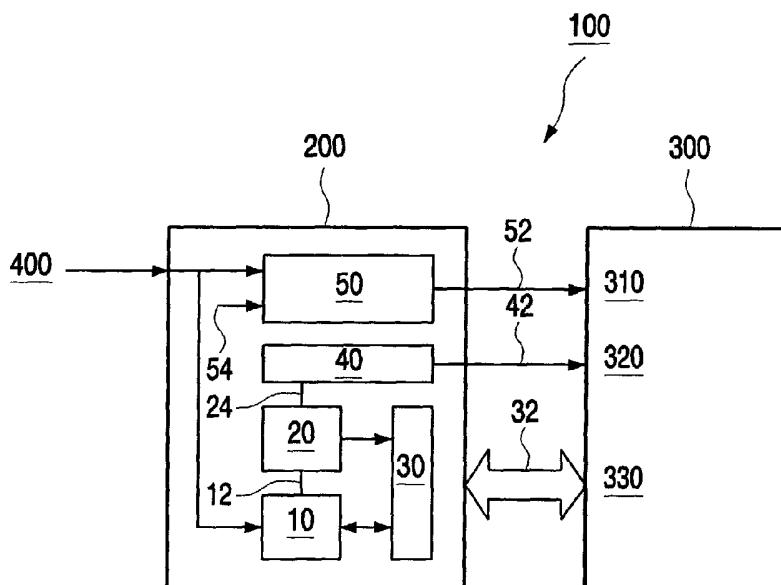
(74) Agent: **VOLMER, Georg**; Philips Intellectual Property &
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

(81) Designated States (national): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD,
SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US,
UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),

[Continued on next page]

(54) Title: METHOD AND BASE CHIP FOR MONITORING THE OPERATION OF A MICROCONTROLLER UNIT



(57) Abstract: To further develop a method and a base chip (200) for monitoring the operation of at least one microcontroller that is intended for at least one application and is associated with a system (100) in such a way that a failure in the reset function can be reliably detected and the conclusions that need to be drawn for system-related reasons can be drawn, it is proposed that: the microcontroller unit (300) has at least one monitoring module (10) associated with it and that; the fact that a reset of the microcontroller unit (300) has taken place is acknowledged to the monitoring module (10) by means of at least one confirming signal.



European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

(88) Date of publication of the international search report:
4 March 2004

Published:

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

 Internati Application No
 PCT/IB 03/02113

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G06F11/00 G06F1/24

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 307 480 B1 (SHELDON PETER ET AL) 23 October 2001 (2001-10-23) figure 1 column 1, line 38-62 column 2, line 44 -column 3, line 5 column 4, line 17-60 column 5, line 11-19 ----	1-10
X A	US 6 038 671 A (TRAN ROBIN T ET AL) 14 March 2000 (2000-03-14) figure 4 column 1, line 64 -column 2, line 34 column 3, line 45-67 ----- -/--	1,2,4, 7-9 3,5,6,10



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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& document member of the same patent family

Date of the actual completion of the international search

4 December 2003

Date of mailing of the international search report

05/01/2004

Name and mailing address of the ISA

 European Patent Office, P.B. 5818 Patentlaan 2
 NL - 2280 HV Rijswijk
 Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
 Fax: (+31-70) 340-3016

Authorized officer

Hanrahan, A

INTERNATIONAL SEARCH REPORT

Internat Application No
PCT/IB 03/02113

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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