(54) Title: SYSTEM AND TAPERED WAVEGUIDE FOR IMPROVING LIGHT COUPLING EFFICIENCY BETWEEN OPTICAL FIBERS AND INTEGRATED PLANAR WAVEGUIDES AND METHOD OF MANUFACTURING SAME

(57) Abstract: A tapered waveguide optical mode transformer (20) includes a tapered core formed on a planar substrate structure (16). To vertically taper the core (21), steps (22) are etched into the top surface of the core. The steps have depths and lengths along the optical axis of tapered waveguide that are selected to transform the optical mode characteristics of a desired optical fiber to the optical mode characteristics of a desired planar waveguide. The core can also be tapered horizontally to form a 2-D tapered waveguide. The tapered waveguide can be integrally included in planar lightwave circuits (PLCs) to reduce light coupling losses between optical fibers and the PLC waveguides.
before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments.

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SYSTEM AND TAPERED WAVEGUIDE FOR IMPROVING LIGHT COUPLING EFFICIENCY BETWEEN OPTICAL FIBERS AND INTEGRATED PLANAR WAVEGUIDES AND METHOD OF MANUFACTURING SAME

TECHNICAL FIELD

The present invention relates generally to planar lightwave circuits and, more particularly, to optical mode transformers for efficiently coupling optical fibers to planar integrated waveguides.

BACKGROUND

Planar Lightwave Circuits (PLCs) transmit and receive signals for both short distance data and long distance telecommunication systems. For optimal operation, the PLCs have functional optical components, such as waveguides. These waveguides should be small enough in size so that dense integration of waveguide optical devices, including sharp bends in the waveguides, is possible on a single chip.

High-index-contrast material systems work well for fabricating PLCs. Such material systems can be formed as a core layer of silicon, on a Silicon On Insulator (SOI) substrate, The core layer has a refractive index of about 3.5, and it is surrounded on both side by silica cladding with a refractive index of about 1.5. A waveguide formed in such a structure is called a channel waveguide. Channel waveguides offer better light confinement in smaller dimensions.

There are potentially many practical uses of high-index-contrast waveguide chips, especially in telecommunications, where there is a need for developing ways to route and process multi-wavelength optical signals transparently (i.e., without having to convert optical signals to electrical signals for processing, and then back again). One example of a PLC waveguide chip is a single-mode waveguide-based “mux” and “demux” for serializing and separating multi-wavelength optical signals in Dense Wavelength Division Multiplexing (DWDM) applications. For this kind of application and others, it is generally desirable to configure the waveguides so as to force single-mode propagation to avoid introduction of undesirable effects of differing propagation velocities of different optical modes.

One of the most difficult challenges facing high-index-contrast optical chips is efficiently coupling light into and out of the chip. Particularly difficult is the coupling of
light from a standard optical fiber or external source to a silicon waveguide on the chip. A large mismatch between the common optical fiber dimensions and that of the high-index-contrast waveguide, and their respective mode sizes, impairs light coupling from optical fibers to PLC waveguides.

A number of techniques have been utilized for optical coupling between waveguides and optical fibers, including prism couplers, grating couplers, tapered fibers and micro-lens mode transformers. Unfortunately, these techniques do not offer the combination of high coupling efficiency, wavelength independence, reliability, manufacturability, ruggedness, and robustness demanded for use in low-cost, high-volume telecommunications applications.

In view of the foregoing, there is a need for an improved coupling between optical fibers and planar waveguides formed on optical chips, such as PLCs.

SUMMARY

It is an advantage of the present invention to provide a tapered waveguide extension that significantly increases the light coupling efficiency between an optical fiber and a planar waveguide. The present invention also provides a relatively simple and reliable process for fabricating the tapered waveguide on a planar substrate structure so that it can be integrated with a PLC waveguide.

According to an embodiment of the invention, the tapered waveguide acts as an optical mode transformer between an optical fiber and a planar waveguide. The tapered waveguide includes a core formed on a planar substrate structure. The core is vertically tapered. To create the vertical taper, a plurality of steps is formed into the top surface of the core. The steps have depths and lengths along the optical axis of the tapered waveguide that are selected to transform the optical mode characteristics and size of the optical fiber to the optical mode characteristics and size of the planar waveguide.

Other aspects, features, embodiments, processes and advantages of the invention will be or will become apparent to one with skill in the art upon examination of the following figures and detailed description. It is intended that all such additional features, embodiments, processes and advantages be included within this description, be within the scope of the invention, and be protected by the accompanying claims.
BRIEF DESCRIPTION OF THE DRAWINGS

It is to be understood that the drawings are solely for purpose of illustration and do not define the limits of the invention. Furthermore, the components in the figures are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the invention. In the figures, like reference numerals designate corresponding parts throughout the different views.

FIG. 1 is a cross-sectional schematic of an exemplary starting material structure for fabricating a tapered planar waveguide.

FIG. 2 is a conceptual cross-sectional side view of a tapered waveguide in accordance with an exemplary embodiment of the present invention.

FIG. 3 is a conceptual top-down view of the tapered waveguide shown in FIG. 2.

FIG. 4 is a conceptual cross-sectional side view showing dimensional details of the waveguide steps shown in FIG. 2.

FIG. 5 is a conceptual perspective view of a waveguide step as it is being formed.

FIG. 6 is a conceptual cross-sectional side view of the tapered waveguide shown in FIG. 2 with a dielectric cladding layer deposited thereon, in accordance with another exemplary embodiment of the invention.

FIG. 7 is a conceptual cross-sectional side view of an optical system including the tapered waveguide of FIG. 6, in accordance with a further exemplary embodiment of the invention.

DETAILED DESCRIPTION

The following detailed description, which references to and incorporates the drawings, describes and illustrates one or more specific embodiments of the invention. These embodiments, offered not to limit but only to exemplify and teach the invention, are shown and described in sufficient detail to enable those skilled in the art to practice the invention. Thus, where appropriate to avoid obscuring the invention, the description may omit certain information known to those of skill in the art.

Turning now to the drawings, and in particular to FIG. 1, there is illustrated a cross-sectional schematic of an exemplary starting material structure 10 for fabricating a tapered planar waveguide. The preferred starting material 10 for fabricating 1-D and 2-D waveguides is a silicon-on-insulator (SOI) structure. The SOI starting material 10
includes a layer of crystalline silicon 12 grown on a planar substrate structure 13. The planar substrate structure includes a dielectric insulator layer 14 formed over a semiconductor substrate 16. The insulator layer 14 is a 2 μm thick layer of thermally grown SiO2 on a 6” silicon substrate 16. A 3 μm thick layer of crystalline silicon 12 is deposited on top of the SiO2 layer 14 to complete the structure.

The crystalline silicon layer 12, which makes up the core of the tapered waveguide 20 (See FIG. 2), has a refractive index of 3.47, the SiO2 layer 14 has a refractive index of 1.46, and the silicon substrate has a refractive index of 3.47.

FIG. 2 is a conceptual cross-sectional side view of a tapered waveguide 20 in accordance with an exemplary embodiment of the present invention. In FIG. 2, there is shown a 2-D tapered section 20 that acts as a classic adiabatic modal transformer that transforms the input fundamental mode shape (matched to the mode shape of an optical fiber) to that of a planar waveguide mode, making the light coupling more efficient than that with no taper at all.

The 2-D taper waveguide 20 includes a stepped crystalline silicon core 21 formed on a planar substrate structure 14,16. The core 21 is vertically and horizontally tapered (i.e., 2-D tapered). To create the vertical taper, a plurality of steps 22 are formed into the top surface of the core 21. The steps 22 have depths and lengths along the optical axis of the tapered waveguide 20 that are selected to transform the optical mode characteristics and physical dimensions of the optical fiber to the optical mode characteristics and physical dimensions of a planar waveguide formed on the same substrate with the tapered waveguide 20.

The tapered waveguide 20 includes a 1-D lateral flare that extends from the planar waveguide width w2 (See FIG. 3) at the tapered waveguide’s narrow end 26 to a width w1 (See FIG. 3) of 4 μm at the tapered waveguide’s wide end 25 to match the optical fiber, within a horizontal distance l of 250 μm. This lateral taper matches the mode field diameter (MFD) of a high numerical aperture (HNA) optical fiber to that of an integrated planar waveguide in the lateral direction.

Subsequent step etches are performed to create the stepped vertical taper along the core thickness in the 1-D flare area so that there is also a MFD match between the HNA fiber and the planar waveguide in the vertical direction. The vertical flare extends from the planar waveguide height h2 at the tapered waveguide’s narrow end 26 to a
height h1 of 3 μm at the tapered waveguide’s wide end 25 to match the optical fiber, within the horizontal distance l of 250 μm.

FIG. 4 is a conceptual cross-sectional side view showing dimensional details of the tapered waveguide steps 22 shown in FIG. 2. Each step is formed to have a predefined length L and a predefined H. In the example tapered waveguide 20 disclosed herein, each step is 0.167 μm deep (H), and has a length L of 25 μm. The total etch depth over the length l of the tapered waveguide 20 is 1.5 μm.

FIG. 5 is a conceptual perspective view of a tapered waveguide step 22 as it is being formed. The vertical steps 22 are formed using a novel process that relies on conventional photolithography and silicon etching techniques.

Before forming the vertical steps 22, the 1-D lateral taper is formed. This is an intermediate step toward the 2-D tapered waveguide 20. The 1-D lateral taper by itself has a coupling efficiency lower than that of the complete 2-D taper, but the 1-D taper requires less technological effort to form.

As illustrated by FIG. 3, the mask that initially defines the tapered waveguide 20 on the starting material 10 defines the lateral taper with width dimensions starting from w2 = 0.5 μm up to w1 = 3 μm.

To define sub-micron width waveguide geometries, a photolithography technique using P4110 i-line (365 nm) photoresist from AZ, Inc. and a MA6 mask aligner from Karl Suss, Inc. is employed. After masking the starting material 10, the waveguides are etched using a silicon dry etching system from Oxford, Inc. C4F8 (flow rate 90 sccm) + SF6 (flow rate 50 sccm) slow etch chemistry is utilized in the Oxford machine for etching the waveguide geometries. The average depth of the waveguides is ~1 μm.

The vertical steps are formed by first applying a protective layer over a step area of the core layer 12 extending from the waveguide’s wider end 25 location. As shown in FIG. 5, this is accomplished by covering the entire waveguide 12 with Shipley 1818 positive photoresist. Using an appropriate mask, the non-step portion 34 of the waveguide 12 is then exposed to UV light and the photoresist developed, leaving only 25 μm (the length of the first step) of protected area 36 covered by photoresist.

Dry etching, using the previous chemistry in the Oxford machine, is then performed to achieve a 0.167 μm step. This process is repeated, each time extending the protective photoresist layer another 25 μm from the wider end to define another step. The
process is repeated until the desired number of steps is formed.

After forming the 2-D tapered waveguide core 21, a dielectric cladding layer 30 (See FIG. 6) is e-beam evaporated onto the stepped core layer 21 to form the channel waveguide structure 40. The processed wafer is then diced into appropriate pieces and cleaned. The wide end 25 of the tapered waveguide 40 is then mechanically polished to a mirror finish. To further reduce light coupling loss, an anti-reflective (AR) coating is applied to the polished end using conventional techniques.

FIG. 6 is a conceptual cross-sectional side view of the tapered waveguide shown in FIG. 2 with a dielectric cladding layer 30 deposited thereon, in accordance with another exemplary embodiment of the invention. The dielectric cladding layer 30 is a 2 µm thick SiO2 top cladding layer that is e-beam evaporated onto the stepped core layer 21 to form the completed waveguide structure 40. The top cladding layer 30 has a refractive index of about 1.46.

In order to effectively couple light from optical fibers to high-index-contrast single mode planar waveguides, the tapered waveguides 20,40 can be formed as integral waveguide extensions of high-index-contrast single mode planar waveguides.

FIG. 7 is a conceptual cross-sectional side view of an optical system 70 including two of the tapered waveguides 40 integrally formed with and extending from a planar waveguide 42. The tapered waveguides 40 couple the planar waveguide 42 to high numerical aperture (HNA) optical fibers 50. The fibers 50 include a core 52 surrounded by cladding 54.

Although any single mode optical fiber can be coupled to the tapered waveguides disclosed herein, to further increase light coupling efficiency, a High Numerical Aperture (HNA) fiber may be used as the input fiber. A special splice program is used for coupling a regular single mode fiber SMF28 into the HNA fiber to further reduce light loss.

Each tapered waveguide 40 provides a lateral 1-D flare that extends from the planar waveguide width to a width of 4 µm within a horizontal distance of 250 µm. This lateral taper matches the mode field diameter (MFD) of high numerical aperture (HNA) optical fibers 50 to that of the planar waveguide 42 in the lateral direction. The stepped vertical flare extends from the planar waveguide height to 3 µm to match the MFD of the optical fiber 50, within the horizontal distance l of 250 µm.
Sample waveguides, some having a structure as illustrated in FIG. 6, were fabricated using the starting material illustrated in FIG. 1 and the method described herein. Testing was performed using an appropriate optical measurement setup that included a 1550 nm DFB laser, a 24 dBm amplifier, HNA input fiber, a microscope objective and a free space InGaAs detector to determine the waveguide propagation loss and the coupling loss between an HNA fiber and the sample waveguides. A comparison between at least three lengths of sample waveguides was used to find out the waveguide propagation loss value. Measurements of waveguide loss for 2-D, 1-D and no taper waveguide samples yielded the coupling efficiency of each taper type.

Experimental testing indicated a significant reduction in coupling loss between the HNA optical fiber and the stepped-taper waveguides, when compared to non-tapered waveguides. Actual test measurements demonstrated a coupling loss of about 2 dB for the 2-D stepped-taper waveguide. A 1-D stepped-taper waveguide was measured to have around 4 dB coupling loss. Without any taper, the waveguide coupling loss was measured to be larger than 8 dB.

The preceding detailed description has illustrated the principles of the invention using specific implementations of a tapered optical waveguide. However, the invention is not limited to these particular implementations. For example, the inventive principles disclosed herein can be implemented in many other types of substrate material structures, such as other semiconductor materials, such as InP, GaAs or the like, or polymer materials suitable for making waveguides, including polyimide, PMMA or the like.

Therefore, while one or more specific embodiments of the invention have been described, it will be apparent to those of ordinary skill in the art that many more embodiments are possible that are within the scope of this invention. Further, the foregoing detailed description and drawings are considered as illustrative only of the principles of the invention. Since other modifications and changes may be or become apparent to those skilled in the art, the invention is not limited the exact construction and operation shown and described, and accordingly, all suitable modifications and equivalents are deemed to fall within the scope of the invention.
CLAIMS

What is claimed:

1. A waveguide optical mode transformer, comprising:
   a core formed on a planar substrate structure; and
   a predetermined plurality of steps formed into the top surface of the core so as to
   vertically taper the core, each step having a predetermined height and a predetermined
   length.

2. The waveguide optical mode transformer of claim 1, wherein the core is
   horizontally tapered.

3. The waveguide optical mode transformer of claim 2, wherein the vertical
   and horizontal tapers narrow at the same end of the core.

4. The waveguide optical mode transformer of claim 1, further comprising a
   dielectric cladding layer formed over the core.

5. The waveguide optical mode transformer of claim 1, wherein the planar
   substrate structure includes a dielectric layer formed over a semiconductor substrate.

6. The waveguide optical mode transformer of claim 1, wherein the core is
   crystalline silicon.

7. The waveguide optical mode transformer of claim 1, further comprising
   dielectric layers formed under and over the core, wherein the dielectric layers each have
   a refractive index that is lower than the refractive index of the core.

8. An optical system, comprising:
   a planar waveguide; and
   a tapered waveguide extension formed at the end of the planar waveguide for
   coupling light between the planar waveguide and an optical fiber, the waveguide
extension having a predetermined plurality of steps formed into its top surface so as to vertically taper the waveguide extension, each of the steps having a predetermined height and a predetermined length.

9. The optical system of claim 8, wherein the tapered waveguide extension is horizontally tapered.

10. The optical system of claim 8, wherein the tapered waveguide extension includes a crystalline silicon core.

11. The optical system of claim 10, wherein the tapered waveguide extension includes a dielectric cladding layer formed over the core.

12. The optical system of claim 8, wherein the tapered waveguide extension includes a core layer between two dielectric layers, wherein the dielectric layers each have a refractive index that is lower than the refractive index of the core.

13. The optical system of claim 8, further comprising the optical fiber.

14. A method of manufacturing a tapered planar waveguide usable as an optical mode transformer between an optical fiber and a planar waveguide, comprising: providing a layer of core material for the tapered waveguide; and forming a predetermined plurality of steps into the top surface of the core so as to vertically taper the core layer, each step having a predetermined height and a predetermined length.

15. The method of claim 14, further comprising:
forming a horizontal taper in the core layer.

16. The method of claim 14, wherein the step of providing includes:
providing a starting material comprising a substrate, a dielectric layer formed on the substrate, and the layer of core material formed on the dielectric layer.
17. The method of claim 14, wherein the step of forming includes:

(a) defining the location of the wider end of the tapered waveguide on the core layer;

(b) applying a protective layer over a predetermined area of the core layer extending from the defined wider end location, the predetermined area defining the area one or more of the steps;

(c) etching the remaining unprotected area of the core layer to a predetermined depth defining the height of a step; and

(d) repeating steps (b) – (c) a predetermined number of times, each time extending the predetermined area farther from the defined wider end location to define the length of a new step, whereby forming the predetermined plurality of steps.

18. The method of claim 14, wherein the step of etching is performed using silicon dry etching.

19. The method of claim 14, further comprising polishing the wider end of the tapered waveguide.

20. The method of claim 14, further comprising applying an anti-reflective coating at the wider end of the tapered waveguide.
**INTERNATIONAL SEARCH REPORT**

A. **CLASSIFICATION OF SUBJECT MATTER**

G02B6/122  
G02B6/30  
G02B6/136

According to International Patent Classification (IPC) or to both national classification and IPC

B. **FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

G02B  
HO1S

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, INSPEC, WPI Data

C. **DOCUMENTS CONSIDERED TO BE RELEVANT**

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1 May 2003 (2003-05-01)  
paragraph ‘0027!’ - paragraph ‘0029!’  
paragraph ‘0042!’ - paragraph ‘0043!’  
paragraph ‘0052!’ - paragraph ‘0054!’  
figures 5,8 | 1-3,5,6,  
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figures 1-5 | 1,4,7,8,  
11,12,  
14,16,17 |

X Further special categories are listed in the continuation of box C.  
X Patent family members are listed in annex.

**Special categories of cited documents:**

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9 December 2005

Date of mailing of the international search report  
10/01/2006

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