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(54) **Voltage regulator circuit with oscillation suppression**

(57) A regulator circuit comprises a phase correcting circuit portion (40). The phase correcting circuit portion (40) comprises a current correcting transistor (M11) combined with an output transistor to form a current mirror circuit. An I-V converting resistor (R4) is connected to the current correcting transistor (M11) to convert a

current flowing through the phase correcting circuit portion (40) into a feedback voltage which is proportional to an output voltage and independent of an output capacitor (Co). A phase correcting capacitor (C1) and a resistor (R5) correct the error signal according to the feedback voltage.

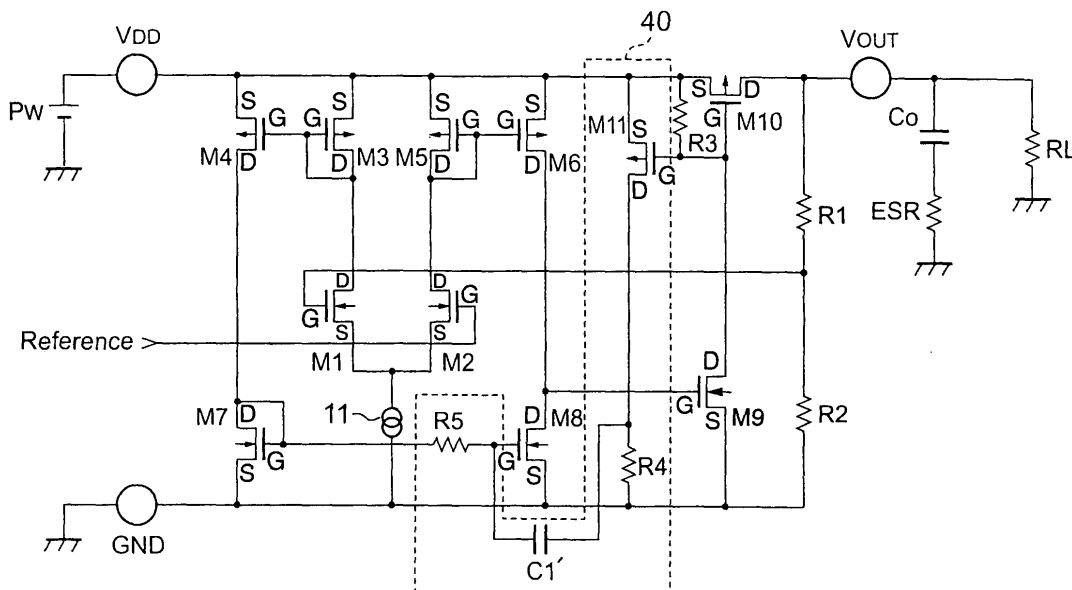


FIG. 4



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EUROPEAN SEARCH REPORT

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A	* page 6, line 14 - page 7, line 13; figure 1 *	3-5,7		

The present search report has been drawn up for all claims				
Place of search				
Date of completion of the search				
Examiner				
Munich			11 May 2005	Vaño Gea, J
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document				

3

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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EP 04 25 1107

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