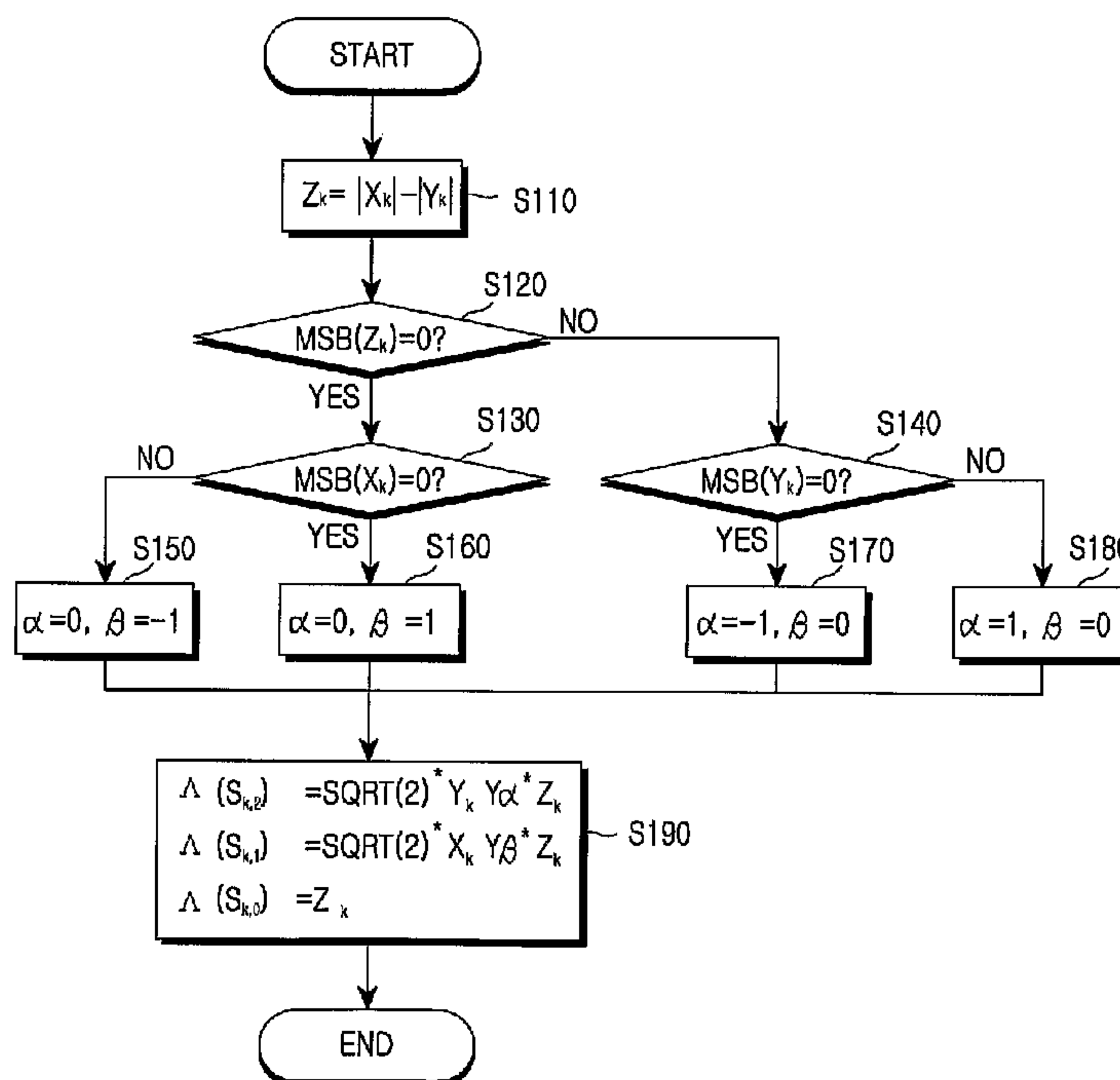




(86) Date de dépôt PCT/PCT Filing Date: 2002/08/13
 (87) Date publication PCT/PCT Publication Date: 2003/02/27
 (45) Date de délivrance/Issue Date: 2006/04/18
 (85) Entrée phase nationale/National Entry: 2003/04/08
 (86) N° demande PCT/PCT Application No.: KR 2002/001546
 (87) N° publication PCT/PCT Publication No.: 2003/017609
 (30) Priorité/Priority: 2001/08/14 (2001-48921) KR

(51) Cl.Int./Int.Cl. *H04L 27/22* (2006.01),
H04L 25/06 (2006.01)
 (72) Inventeurs/Inventors:
HA, SANG-HYUCK, KR;
KIM, MIN-GOO, KR
 (73) Propriétaire/Owner:
SAMSUNG ELECTRONICS CO., LTD., KR
 (74) Agent: MARKS & CLERK

(54) Titre : APPAREIL ET PROCEDE DE DEMODULATION DANS UN SYSTEME DE COMMUNICATION FAISANT APPEL A UNE MODULATION PAR DEPLACEMENT DE PHASE (PSK) A 8 ETATS
 (54) Title: DEMODULATION APPARATUS AND METHOD IN A COMMUNICATION SYSTEM EMPLOYING 8-ARY PSK MODULATION



(57) **Abrégé/Abstract:**

An 8-Ary PSK demodulation apparatus for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision value $A(S_{k,0})$, $A(S_{k,1})$, and $A(S_{k,2})$. A calculator calculates Z_k by subtracting a level $|Y|$ of the quadrature-phase signal component Y_k from a level $|X_k|$ of the in-phase signal component X_k . A first selector selects the Z_k , according to an MSB of the quadrature-phase signal component Y_k . A second selector selects the Z_k or the reverse Z_k according to an MSB of the in-phase signal component X_k . A third selector selects an output of the second selector or a value "0" according to an MSB of the Z_k .

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

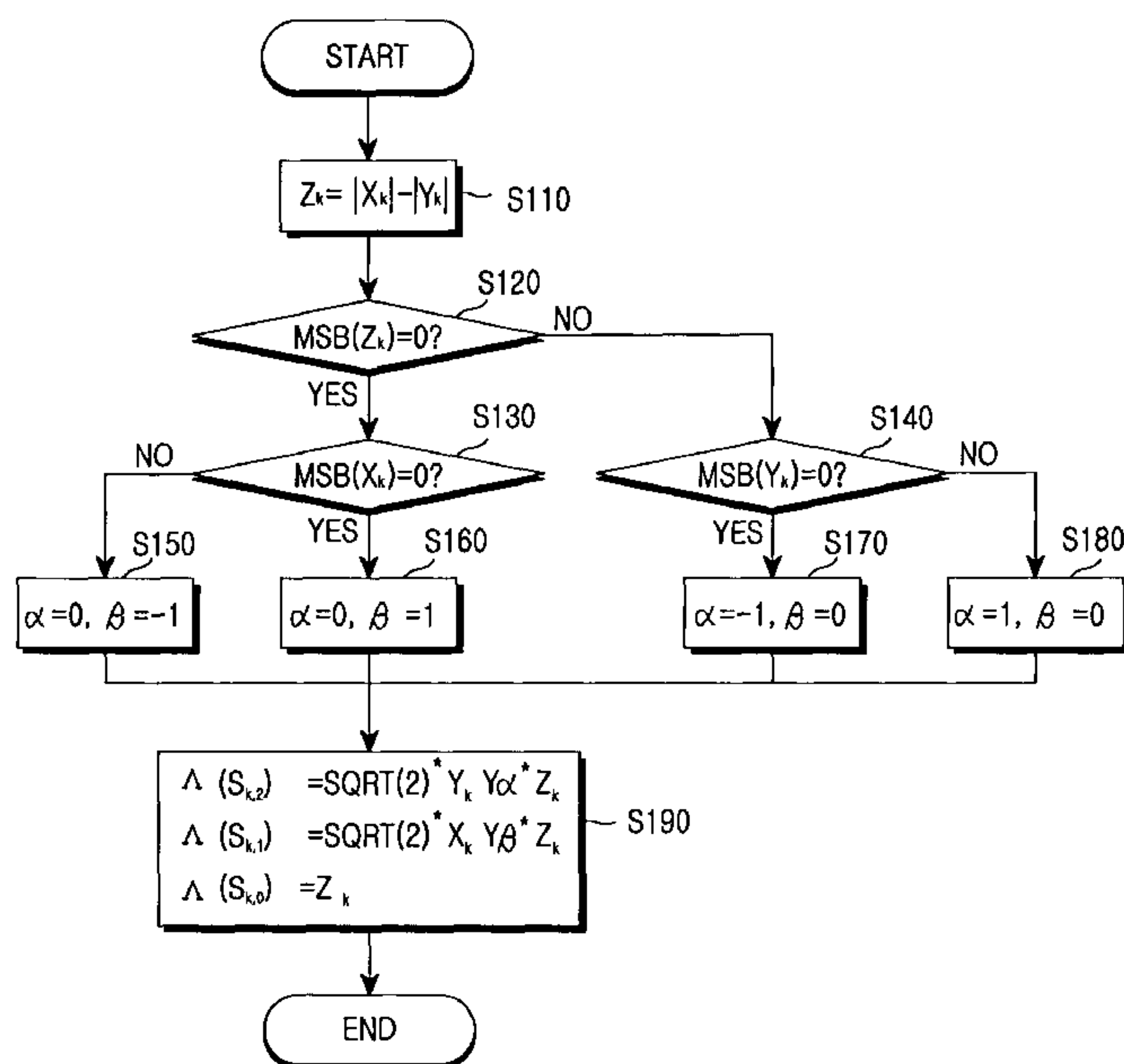
(19) World Intellectual Property Organization
International Bureau(43) International Publication Date
27 February 2003 (27.02.2003)

PCT

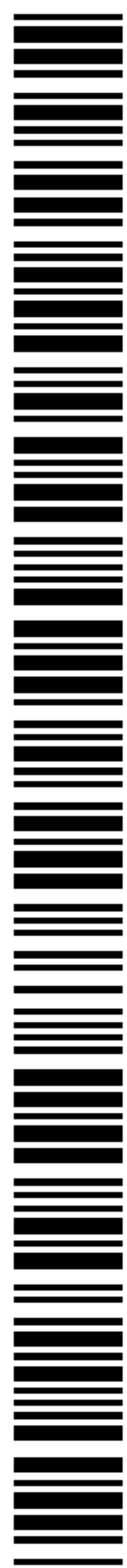
(10) International Publication Number
WO 03/017609 A1

- (51) International Patent Classification⁷: H04L 27/22 (72) Inventors: HA, Sang-Hyuck; 488, Kokbanjong-dong, Kwonsong-gu, Suwon-shi, 441-400 Kyonggi-do (KR). KIM, Min-Goo; Woosung APT. #822-406, Young-tong-dong, Paltal-gu,, Suwon-shi 442-470 (KR).
- (21) International Application Number: PCT/KR02/01546
- (22) International Filing Date: 13 August 2002 (13.08.2002) (74) Agent: LEE, Keon-Joo; Mihwa Bldg. 110-2, Myongryun-dong 4-Ga, Chongro-gu, 110-524 Seoul (KR).
- (25) Filing Language: English
- (26) Publication Language: English (81) Designated States (national): AU, BR, CA, CN, IN, JP, RU.
- (30) Priority Data: 2001-48921 14 August 2001 (14.08.2001) KR Published: — with international search report
- (71) Applicant: SAMSUNG ELECTRONICS CO., LTD. [KR/KR]; 416, Maetan-dong, Paldal-ku, Suwon, Kyungki-do 442-370 (KR). For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: DEMODULATION APPARATUS AND METHOD IN A COMMUNICATION SYSTEM EMPLOYING 8-ARY PSK MODULATION



(57) Abstract: An 8-Ary PSK demodulation apparatus for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(S_{k,0})$, $\Lambda(S_{k,1})$, and $\Lambda(S_{k,2})$. A calculator calculates Z_k by subtracting a level $|Y_k|$ of the quadrature-phase signal component Y_k from a level $|X_k|$ of the in-phase signal component X_k . A first selector selects the Z_k , according to an MSB of the quadrature-phase signal component Y_k . A second selector selects the Z_k or the reverse Z_k according to an MSB of the in-phase signal component X_k . A third selector selects an output of the second selector or a value "0" according to an MSB of the Z_k .



WO 03/017609 A1

- 1 -

**DEMODULATION APPARATUS AND METHOD IN A
COMMUNICATION SYSTEM EMPLOYING 8-ARY PSK MODULATION**

BACKGROUND OF THE INVENTION

5

1. Field of the Invention

The present invention relates generally to a demodulation apparatus and method in a digital communication system employing multi-level modulation, and in particular, to a demodulation apparatus and method for calculating soft decision values needed as inputs to a channel decoder in a demodulator for a digital communication system employing 8-ary PSK(Phase Shift Keying) modulation.

10

2. Description of the Related Art

In a digital communication system employing 8-ary PSK modulation, a kind of multi-level modulations, to increase spectral efficiency, a signal encoded by a channel encoder is transmitted after being modulated. A demodulator then demodulates the signal transmitted and provides the demodulated signal to a channel decoder for decoding. The channel decoder performs soft decision decoding in order to correct errors. To do so, the demodulator must have a mapping algorithm for generating soft decision values (or soft values) corresponding to output bits of the channel encoder from a 2-dimensional signal comprised of an in-phase signal component and a quadrature-phase signal component.

15

The mapping algorithm is classified into a simple metric procedure proposed by Nokia and a dual minimum metric procedure proposed by Motorola. Both algorithms calculate LLR (Log Likelihood Ratio) for the output bits and use the calculated LLR as an input soft decision value of the channel decoder.

25

The simple metric procedure, a mapping algorithm given by modifying a complex LLR calculation formula into a simple approximate formula, has a simple LLR calculation formula, but LLR distortion caused by the use of the approximate formula leads to performance degradation. The dual minimum metric procedure, a mapping algorithm of calculating LLR with a more accurate approximate formula and using the calculated LLR as an input soft decision value of the channel decoder, can make up for performance degradation of the simple metric procedure to some extent. However, compared with the simple metric

30

35

- 2 -

procedure, this procedure needs increased calculations, thus causing a considerable increase in hardware complexity.

SUMMARY OF THE INVENTION

5

It is, therefore, an object of the present invention to provide an apparatus and method for simplifying obtaining input soft values of a channel decoder, calculated by the dual minimum metric procedure, without a mapping table or complex processing needed to obtain a minimum distance value with a received signal in a demodulator for a digital communication system employing 8-ary PSK modulation.

10

It is another object of the present invention to provide an apparatus and method for calculating a soft decision value by a simple conditional formula in a digital communication system employing 8-ary PSK modulation.

15

To achieve the above and other objects, there is provided an 8-ary PSK demodulation apparatus for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(s_{k,0})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,2})$ for the input signal $R_k(X_k, Y_k)$ by a soft decision means. The apparatus comprises a calculator for calculating a soft value Z_k by subtracting a level $|Y_k|$ of the quadrature-phase signal component Y_k from a level $|X_k|$ of the in-phase signal component X_k of the received signal $R_k(X_k, Y_k)$, and outputting the Z_k as a first soft decision value; a first selector for receiving the Z_k from the calculator and an inverted value $-Z_k$ of the Z_k , and selecting the Z_k or the $-Z_k$ according to a most significant bit (MSB) of the quadrature-phase signal component Y_k ; a second selector for receiving the Z_k from the calculator and the $-Z_k$, and selecting the Z_k or the $-Z_k$ according to an MSB of the in-phase signal component X_k ; a third selector for receiving an output of the second selector and a value "0", and selecting the output of the second selector or the value "0" according to an MSB of the function Z_k ; a first adder for adding a value calculated by multiplying the quadrature-phase signal component Y_k by $\sqrt{2}$ to an output of the third selector, and outputting the result value as a third soft decision value; a fourth selector for receiving an output of the second selector and a value "0", and selecting the output of the second selector or the value "0" according to the MSB of the function Z_k ; and a second adder for adding a value calculated by multiplying the in-phase signal component X_k by $\sqrt{2}$ to an

20

25

30

35

- 3 -

output of the fourth selector, and outputting the result value as a second soft decision value

To achieve the above and other objects, there is provided an 8-ary PSK demodulation method for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(s_{k,0})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,2})$ for the input signal $R_k(X_k, Y_k)$ by a soft decision means. The method comprises the steps of: (a) calculating a soft value Z_k of a first demodulated symbol by subtracting a level $|Y_k|$ of the quadrature-phase signal component Y_k from a level $|X_k|$ of the in-phase signal component X_k of the received signal $R_k(X_k, Y_k)$, (b) setting a first variable α to "0" if the soft value Z_k has a positive value, setting the first variable α to "-1" if the Z_k has a negative value and the quadrature-phase component Y_k has a positive value, and setting the first variable α to "1" if the Z_k has a negative value and the quadrature-phase component Y_k has a negative value; (c) determining a soft value of a third demodulated symbol by calculating $\sqrt{2} Y_k + \alpha * Z_k$ using the quadrature-phase component Y_k , the soft value Z_k and the first variable α ; (d) setting a second variable β to "0" if the soft value Z_k has a negative value, setting the second variable β to "-1" if the Z_k has a positive value and the in-phase component X_k has a negative value, and setting the second variable β to "1" if the Z_k has a positive value and the in-phase component X_k has a positive value; (e) determining a soft value of a second demodulated symbol by calculating $\sqrt{2} X_k + \beta * Z_k$ using the in-phase component X_k , the soft value Z_k and the second variable β .

BRIEF DESCRIPTION OF THE DRAWINGS

The above and other objects, features and advantages of the present invention will become more apparent from the following detailed description when taken in conjunction with the accompanying drawings in which:

FIG. 1 illustrates a signal constellation with mapping points according to 8-ary PSK;

FIG. 2 illustrates a procedure for calculating soft decision values in a digital communication system employing 8-ary PSK according to an embodiment of the present invention;

FIG. 3 illustrates block diagram of a calculator for determining soft decision values of a demodulated symbol according to an embodiment of the

- 4 -

present invention;

FIG. 4 illustrates a logic circuit of a soft decision value calculator for use in a digital communication system employing 8-ary PSK; and

5 FIG. 5 illustrates a signal constellation having mapping points according to the 8-ary PSK, for explanation of calculations.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

10 A preferred embodiment of the present invention will be described herein below with reference to the accompanying drawings. In the following description, well-known functions or constructions are not described in detail since they would obscure the invention in unnecessary detail.

15 The present invention provides a method for calculating multi-dimensional soft decision values needed as inputs to a channel decoder from a 2-dimensional received signal, using the dual minimum metric procedure.

20 In a transmitter, a modulator divides an output bit stream of a channel encoder into m-bit signal sequences, and maps the signal sequences to corresponding signal points among M ($=2^m$) signal points according to a Gray coding rule. This can be represented by

Equation (1)

$$s_{k,m-1}s_{k,m-2}\cdots s_{k,0} \xrightarrow{f} I_k, Q_k$$

25 In Equation (1), $s_{k,i}$ ($i=0,1,\dots,m-1$) indicates an i^{th} bit in a signal sequence mapped to a k^{th} symbol, and I_k and Q_k indicate an in-phase (I) signal component and a quadrature-phase (Q) signal component of the k^{th} symbol, respectively. For 8-ary PSK, $m=3$ and a corresponding signal constellation are illustrated in FIG. 1. As illustrated, the signal constellation includes 8 ($=2^3$) mapping points, each point having a 45° phase difference with the mapping points adjacent thereto.

30

35 As illustrated in FIG. 1, a symbol is mapped to an in-phase signal component I_k and a quadrature-phase signal component Q_k , and transmitted to a receiver through transmission media. Upon receiving the in-phase signal component and the quadrature-phase signal component, the receiver demodulates

- 5 -

the received signal components at a symbol demodulator. A received signal corresponding to the transmitted signal comprised of the in-phase signal component I_k and the quadrature-phase signal component Q_k can be expressed in a complex number by Equation (2) below, taking a transmission gain and noises into consideration.

Equation (2)

$$R_k \equiv X_k + jY_k = g_k(I_k + jQ_k) + (\eta_k^I + j\eta_k^Q)$$

In Equation (2), X_k and Y_k indicate an in-phase signal component and a quadrature-phase signal component of a 2-dimensional received signal mapped to a k^{th} symbol, respectively. Further, g_k is a complex coefficient indicating gains of the transmitter, the transmission media, and the receiver. In addition, η_k^I and η_k^Q are Gaussian noises with an average 0 and a divergence σ_n^2 , and they are statistically independent of each other.

The symbol demodulator of the receiver calculates LLR using a received signal R_k of Equation (2). The LLR corresponding to an i^{th} bit $s_{k,i}$ ($i=0,1,\dots,m-1$) in the output sequence of the channel encoder in the transmitter can be calculated by Equation (3), and the calculated LLR is provided to the channel decoder in the receiver as a soft decision value.

Equation (3)

$$\Lambda(S_{k,i}) = K \log \frac{\Pr\{S_{k,i} = 0 | X_k, Y_k\}}{\Pr\{S_{k,i} = 1 | X_k, Y_k\}} \quad i = 0, 1, \dots, m-1$$

In Equation (3), $\Lambda(s_{k,i})$ is an LLR or a soft decision value corresponding to $s_{k,i}$, k is a constant, and $\Pr\{A|B\}$ indicates a conditional probability defined as a probability that an event A will occur when an event B occurs. However, Equation (3) is non-linear, thus accompanying relatively many calculations. Therefore, it is necessary to approximate Equation (3), for actual realization. In the case of a Gaussian noise channel with $g_k=1$ in Equation (2), Equation (3) can be written as follows.

- 6 -

Equation (4)

$$\Lambda(s_{k,i}) = K \log \frac{\sum_{z_k} \exp\left\{-1/\sigma_\eta^2 |R_k - z_k(s_{k,i} = 0)|^2\right\}}{\sum_{z_k} \exp\left\{-1/\sigma_\eta^2 |R_k - z_k(s_{k,i} = 1)|^2\right\}}$$

5 However, Equation (4) is also non-linear. Therefore, Equation (4) can be approximated by the dual minimum metric procedure proposed by Motorola, as follows.

Equation (5)

$$\begin{aligned} 10 \quad \Lambda(x_{k,i}) &\approx K \log \frac{\exp\left\{-1/\sigma_\eta^2 \min |R_k - z_k(s_{k,i} = 0)|^2\right\}}{\exp\left\{-1/\sigma_\eta^2 \min |R_k - z_k(s_{k,i} = 1)|^2\right\}} \\ &= K' \left[\min |R_k - z_k(s_{k,i} = 1)|^2 - \min |R_k - z_k(s_{k,i} = 0)|^2 \right] \end{aligned}$$

15 In Equation (5), $K' = (1/\sigma_\eta^2)K$, and $z_k(s_{k,i}=0)$ and $z_k(s_{k,i}=1)$ indicate actual values of $I_k + jQ_k$ for $s_{k,i}=0$ and $s_{k,i}=1$, respectively. In order to calculate Equation (5), it is necessary to determine $z_k(s_{k,i}=0)$ and $z_k(s_{k,i}=1)$ for minimizing $|R_k - z_k(s_{k,i} = 0)|^2$ and $|R_k - z_k(s_{k,i} = 1)|^2$ for a 2-dimensional received signal R_k .

20 Taking into consideration $n_{k,i}$ indicating an i^{th} bit value of a reverse mapping sequence for a signal point nearest to R_k and $\bar{n}_{k,i}$ indicating a negation for $n_{k,i}$, Equation (5) approximated by the dual minimum metric procedure can be rewritten as

Equation (6)

$$\Lambda(s_{k,i}) = K'(2n_{k,i} - 1) \left[|R_k - z_k(s_{k,i} = n_{k,i})|^2 - \min |R_k - z_k(s_{k,i} = \bar{n}_{k,i})|^2 \right]$$

25

That is, Equation (6) can be calculated by determining whether an i^{th} bit value $n_{k,i}$ of a reverse mapping sequence for a signal point at the shortest distance

- 7 -

from R_k is "0" or "1" and determining the minimum $\bar{n}_{k,i}$ value for an i^{th} bit value of the reverse mapping sequence. The value calculated by Equation (6) becomes a soft decision value for the i^{th} bit value of the reverse mapping sequence. As the soft decision value becomes larger in a positive or a negative value, the information provided to a channel decoder becomes more correct.

The signal point at the shortest distance from R_k is determined by ranges of an in-phase signal component value and a quadrature-phase signal component value of R_k . Therefore, a first term in the brackets of Equation (6) can be written as

Equation (7)

$$\left| R_k - z_k(s_{k,i} = n_{k,i}) \right|^2 = (X_k - U_k)^2 + (Y_k - V_k)^2$$

In Equation (7), U_k and V_k denote an in-phase signal component and a quadrature-phase signal component of a signal point mapped by $n_k = \{n_{k,m-1}, \dots, n_{k,i}, \dots, n_{k,1}, n_{k,0}\}$, respectively.

Further, a second term in the brackets of Equation (6) can be written as

Equation (8)

$$\min \left| R_k - z_k(s_{k,i} = \bar{n}_{k,i}) \right|^2 = (X_k - U_{k,i})^2 + (Y_k - V_{k,i})^2$$

In Equation (8), $U_{k,i}$ and $V_{k,i}$ denote an in-phase signal component and a quadrature-phase signal component of a signal point mapped by a reverse mapping sequence $m_k = \{m_{k,m-1}, \dots, m_{k,i} (= \bar{n}_{k,i}), \dots, m_{k,1}, m_{k,0}\}$ of z_k minimizing

$\left| R_k - z_k(s_{k,i} = \bar{n}_{k,i}) \right|^2$, respectively. Equation (6) is rewritten as Equation (9) by Equation (7) and Equation (8).

Equation (9)

$$\Lambda(s_{k,i}) = K'(2n_{k,i} - 1) \left[\left\{ (X_k - U_k)^2 + (Y_k - V_k)^2 \right\} - \left\{ (X_k - U_{k,i})^2 + (Y_k - V_{k,i})^2 \right\} \right]$$

- 8 -

$$= K'(2n_{k,i} - 1) \left[(U_k + U_{k,i} - 2X_k)(U_k - U_{k,i}) + (V_k + V_{k,i} - 2Y_k)(V_k - V_{k,i}) \right]$$

From Equation (9), m soft decision values needed as inputs to a channel decoder supporting m -level modulation can be calculated.

5

Herein, a process of calculating input soft decision values to the channel decoder by a demodulator in a data communication system employing 8-ary PSK by Equation (9) will be described.

10 First, Table 1 is used to calculate $\{n_{k,2}, n_{k,1}, n_{k,0}\}$, U_k and V_k from two signal components X_k and Y_k of an 8-ary PSK-modulated received signal R_k . Table 1 illustrates $\{(n_{k,2}, n_{k,1}, n_{k,0}), U_k, \text{ and } V_k$ for the case where a received signal R_k appears in each of 8 regions centered on the signal points in FIG. 1. For the sake of convenience, 4 boundary values, i.e., result values at $X_k=0$, $Y_k=0$, $Y_k=X_k$,
15 $Y_k=-X_k$ are omitted from Table 1.

Table 1

Condition of Y_k	Condition of Y_k/X_k	$\{n_{k,2}, n_{k,1}, n_{k,0}\}$	U_k	V_k
$Y_k > 0$	$Y_k/X_k > 1$	$\{0, 0, 1\}$	$\sin(\pi/8)$	$\cos(\pi/8)$
	$0 < Y_k/X_k < 1$	$\{0, 0, 0\}$	$\cos(\pi/8)$	$\sin(\pi/8)$
	$-1 < Y_k/X_k < 0$	$\{0, 1, 0\}$	$-\cos(\pi/8)$	$\sin(\pi/8)$
	$Y_k/X_k < -1$	$\{0, 1, 1\}$	$-\sin(\pi/8)$	$\cos(\pi/8)$
$Y_k < 0$	$Y_k/X_k > 1$	$\{1, 1, 1\}$	$-\sin(\pi/8)$	$-\cos(\pi/8)$
	$0 < Y_k/X_k < 1$	$\{1, 1, 0\}$	$-\cos(\pi/8)$	$-\sin(\pi/8)$
	$-1 < Y_k/X_k < 0$	$\{1, 0, 0\}$	$\cos(\pi/8)$	$-\sin(\pi/8)$
	$Y_k/X_k < -1$	$\{1, 0, 1\}$	$\sin(\pi/8)$	$-\cos(\pi/8)$

Further, Table 2 illustrates a sequence $\{m_{k,2}, m_{k,1}, m_{k,0}\}$ minimizing
20 $|R_k - z_k(s_{k,i} = \bar{n}_{k,i})|^2$ calculated for i (where $i \in \{0, 1, 2\}$), in terms of a function of $\{n_{k,2}, n_{k,1}, n_{k,0}\}$, and also illustrates in-phase and quadrature-phase signal components $U_{k,i}$ and $V_{k,i}$ of the corresponding z_k .

Table 2

i	$\{m_{k,2}, m_{k,1}, m_{k,0}\}$	$U_{k,i}$	$V_{k,i}$
2	$\{\bar{n}_{k,2}, n_{k,1}, 0\}$	$U_{k,2}$	$V_{k,2}$

1	$\{n_{k,2}, \bar{n}_{k,1}, 1\}$	$U_{k,1}$	$V_{k,1}$
0	$\{n_{k,2}, n_{k,1}, \bar{n}_{k,0}\}$	$U_{k,0}$	$V_{k,0}$

Table 3 illustrates $V_{k,i}$ and $U_{k,i}$ corresponding to $\{m_{k,2}, m_{k,1}, m_{k,0}\}$ determined from Table 2, for all combinations of $\{n_{k,2}, n_{k,1}, n_{k,0}\}$.

5 Table 3

$\{n_{k,2}, n_{k,1}, n_{k,0}\}$	$U_{k,2}$	$U_{k,1}$	$U_{k,0}$	$V_{k,2}$	$V_{k,1}$	$V_{k,0}$
$\{0, 0, 1\}$	$\cos(\pi/8)$	$-\sin(\pi/8)$	$\cos(\pi/8)$	$-\sin(\pi/8)$	$\cos(\pi/8)$	$\sin(\pi/8)$
$\{0, 0, 0\}$	$\cos(\pi/8)$	$-\sin(\pi/8)$	$\sin(\pi/8)$	$-\sin(\pi/8)$	$\cos(\pi/8)$	$\cos(\pi/8)$
$\{0, 1, 0\}$	$-\cos(\pi/8)$	$\sin(\pi/8)$	$-\sin(\pi/8)$	$-\sin(\pi/8)$	$\cos(\pi/8)$	$\cos(\pi/8)$
$\{0, 1, 1\}$	$-\cos(\pi/8)$	$\sin(\pi/8)$	$-\cos(\pi/8)$	$-\sin(\pi/8)$	$\cos(\pi/8)$	$\sin(\pi/8)$
$\{1, 1, 1\}$	$-\cos(\pi/8)$	$\sin(\pi/8)$	$-\cos(\pi/8)$	$\sin(\pi/8)$	$-\cos(\pi/8)$	$-\sin(\pi/8)$
$\{1, 1, 0\}$	$-\cos(\pi/8)$	$\sin(\pi/8)$	$-\sin(\pi/8)$	$\sin(\pi/8)$	$-\cos(\pi/8)$	$-\cos(\pi/8)$
$\{1, 0, 0\}$	$\cos(\pi/8)$	$-\sin(\pi/8)$	$\sin(\pi/8)$	$\sin(\pi/8)$	$-\cos(\pi/8)$	$-\cos(\pi/8)$
$\{1, 0, 1\}$	$\cos(\pi/8)$	$-\sin(\pi/8)$	$\cos(\pi/8)$	$\sin(\pi/8)$	$-\cos(\pi/8)$	$-\sin(\pi/8)$

Table 4 illustrates results given by down-scaling, in a ratio of $K'(\sqrt{(2+\sqrt{2})} - \sqrt{(2-\sqrt{2})})$, soft decision values obtained by substituting $V_{k,i}$ and $U_{k,i}$ of Table 3 into Equation (9), i.e., illustrates the results normalized by $K'(\sqrt{(2+\sqrt{2})} - \sqrt{(2-\sqrt{2})})$. That is, when a received signal R_k is applied, an LLR satisfying a corresponding condition can be determined as a soft decision value by Table 4. If the channel decoder used in the system is not a max-logMAP (logarithmic maximum a posteriori) decoder, a process of up-scaling the LLR of Table 4 in a reverse ratio of the down-scale ratio must be added.

15 Table 4

Condition of Y_k	Condition of Y_k/X_k	$\Lambda(s_{k,2})$	$\Lambda(s_{k,1})$	$\Lambda(s_{k,0})$
$Y_k > 0$	$Y_k/X_k > 1$	$\sqrt{2}Y_k + (-X_k + Y_k)$	$\sqrt{2}X_k$	$X_k - Y_k$
	$0 < Y_k/X_k < 1$	$\sqrt{2}Y_k$	$\sqrt{2}X_k + (X_k - Y_k)$	$X_k - Y_k$
	$-1 < Y_k/X_k < 0$	$\sqrt{2}Y_k$	$\sqrt{2}X_k + (X_k + Y_k)$	$-X_k - Y_k$
	$Y_k/X_k < -1$	$\sqrt{2}Y_k + (X_k + Y_k)$	$\sqrt{2}X_k$	$-X_k - Y_k$
$Y_k < 0$	$Y_k/X_k > 1$	$\sqrt{2}Y_k + (-X_k + Y_k)$	$\sqrt{2}X_k$	$-X_k + Y_k$
	$0 < Y_k/X_k < 1$	$\sqrt{2}Y_k$	$\sqrt{2}X_k + (X_k - Y_k)$	$-X_k + Y_k$
	$-1 < Y_k/X_k < 0$	$\sqrt{2}Y_k$	$\sqrt{2}X_k + (X_k + Y_k)$	$X_k + Y_k$
	$Y_k/X_k < -1$	$\sqrt{2}Y_k + (X_k + Y_k)$	$\sqrt{2}X_k$	$X_k + Y_k$

However, when performing 8-ary PSK soft decision demodulation using Table 4, the demodulator should first perform a condition determining operation, including a dividing operation, on the two components of a received signal. Thereafter, the demodulator selects a formula corresponding to the result of the condition determining operation among the formulas designated according to the conditions, and substitutes the two components of the received signal into the selected formula, thereby calculating soft decision values. To this end, the demodulator requires an operator for performing the dividing operation and a memory for storing different formulas according to the condition.

In order to exclude the dividing operation and remove the memory, it is necessary to modify condition determining formulas and derive soft decision value calculation formulas that can be commonly applied even to the different conditions. To this end, the condition determining formulas shown in Table 4 can be expressed as shown in Table 5, using a new function Z_k defined as $|X_k| - |Y_k|$. In Table 5, the dividing operations are excluded and the soft decision values at the 4 boundary values, which were omitted from Table 4 for convenience, are taken into consideration.

Table 5

Condition of Y_k	Condition of X_k	Condition of Z_k	$\Lambda(s_{k,2})$	$\Lambda(s_{k,1})$	$\Lambda(s_{k,0})$
$Y_k \geq 0$	$X_k \geq 0$	$Z_k \geq 0$	$\sqrt{2}Y_k$	$\sqrt{2}X_k + (X_k - Y_k)$	$X_k - Y_k$
		$Z_k < 0$	$\sqrt{2}Y_k - (X_k - Y_k)$	$\sqrt{2}X_k$	$X_k - Y_k$
	$X_k < 0$	$Z_k \geq 0$	$\sqrt{2}Y_k$	$\sqrt{2}X_k - (-X_k - Y_k)$	$-X_k - Y_k$
		$Z_k < 0$	$\sqrt{2}Y_k - (-X_k - Y_k)$	$\sqrt{2}X_k$	$-X_k - Y_k$
$Y_k < 0$	$X_k \geq 0$	$Z_k \geq 0$	$\sqrt{2}Y_k$	$\sqrt{2}X_k + (X_k + Y_k)$	$X_k + Y_k$
		$Z_k < 0$	$\sqrt{2}Y_k + (X_k + Y_k)$	$\sqrt{2}X_k$	$X_k + Y_k$
	$X_k < 0$	$Z_k \geq 0$	$\sqrt{2}Y_k$	$\sqrt{2}X_k - (-X_k + Y_k)$	$-X_k + Y_k$
		$Z_k < 0$	$\sqrt{2}Y_k + (-X_k + Y_k)$	$\sqrt{2}X_k$	$-X_k + Y_k$

In hardware realization, Table 5 can be simplified into Table 6 on condition that a sign of X_k , Y_k , and Z_k can be expressed by their MSB (Most Significant Bit), or sign bit. In Table 6, MSB(x) denotes an MSB of a given value x.

Table 6

- 11 -

MSB(Y_k)	MSB(X_k)	MSB(Z_k)	$\Lambda(s_{k,2})$	$\Lambda(s_{k,1})$	$\Lambda(s_{k,0})$
0	0	0	$\sqrt{2}Y_k$	$\sqrt{2X_k+Z_k}$	Z_k
		1	$\sqrt{2Y_k-Z_k}$	$\sqrt{2X_k}$	
	1	0	$\sqrt{2}Y_k$	$\sqrt{2X_k-Z_k}$	
		1	$\sqrt{2Y_k-Z_k}$	$\sqrt{2X_k}$	
1	0	0	$\sqrt{2}Y_k$	$\sqrt{2X_k+Z_k}$	
		1	$\sqrt{2Y_k+Z_k}$	$\sqrt{2X_k}$	
	1	0	$\sqrt{2}Y_k$	$\sqrt{2X_k-Z_k}$	
		1	$\sqrt{2Y_k+Z_k}$	$\sqrt{2X_k}$	

From Table 6, soft decision values $\Lambda(s_{k,2})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,0})$ for each i are expressed as

5 Equation (10)

$$\Lambda(s_{k,2}) = \sqrt{2}Y_k + \alpha \cdot Z_k, \text{ where } \alpha = \begin{cases} 0 & \text{if } MSB(Z_k) = 0 \\ -1 & \text{if } MSB(Z_k) = 1 \text{ and } MSB(Y_k) = 0 \\ 1 & \text{if } MSB(Z_k) = 1 \text{ and } MSB(Y_k) = 1 \end{cases}$$

In Equation (10), a parameter α is 0 for $MSB(Z_k)=0$, -1 for $MSB(Z_k)=1$ and $MSB(Y_k)=0$, and 1 for $MSB(Z_k)=1$ and $MSB(Y_k)=1$.

10

Equation (11)

$$\Lambda(s_{k,1}) = \sqrt{2}X_k + \beta \cdot Z_k, \text{ where } \beta = \begin{cases} 0 & \text{if } MSB(Z_k) = 1 \\ -1 & \text{if } MSB(Z_k) = 0 \text{ and } MSB(X_k) = 1 \\ 1 & \text{if } MSB(Z_k) = 0 \text{ and } MSB(X_k) = 0 \end{cases}$$

In Equation (11), a parameter β is 0 for $MSB(Z_k)=1$, -1 for $MSB(Z_k)=0$ and $MSB(X_k)=1$, and 1 for $MSB(Z_k)=0$ and $MSB(X_k)=0$.

15

Equation (12)

$$\Lambda(s_{k,0}) = Z_k$$

20 That is, in the digital communication system employing 8-ary PSK, it is possible to actually calculate 3 soft decision values, which are outputs of the demodulator for one received signal or inputs to the channel decoder, using the

- 12 -

dual minimum metric procedure of Equation (4), through the simple conditional formulas of Equation (10) to Equation (12). This process is illustrated in FIG. 2.

FIG. 2 illustrates a procedure for calculating soft decision values in a digital communication system employing 8-ary PSK according to an embodiment of the present invention. Referring to FIG. 2, in step S110, a symbol demodulator calculates $Z_k = |X_k| - |Y_k|$ to define the condition determining formulas shown in Table 4 as a new function. The symbol demodulator analyzes an MSB of the Z_k in step S120, in order to determine α and β according to the MSB of the Z_k in Equation (1) to Equation (12). As a result of the analysis in step S120, if the MSB of the Z_k is "0", the symbol demodulator proceeds to step S130, and otherwise, proceeds to step S140. In step S130, the symbol demodulator analyzes an MSB of X_k . As a result of the analysis in step S130, if the MSB of the X_k is "1", the symbol demodulator sets the parameter α to "0" and the parameter β to "-1" in step S150. If the MSB of the X_k is "0", the symbol demodulator sets the parameter α to "0" and the parameter β to "1" in step S160.

As a result of the analysis in step S120, if the MSB of the Z_k is "1", the symbol demodulator analyzes an MSB of Y_k in step S140. As a result of the analysis in step S140, if the MSB of the Y_k is "0", the symbol demodulator sets the parameter α to "-1" and the parameter β to "0" in step S170. If the MSB of the Y_k is "1", the symbol demodulator sets the parameter α to "1" and the parameter β to "0" in step S180. Thereafter, in step S190, the symbol demodulator calculates soft decision values by substituting the parameters α and β determined in the proceeding steps and the received signal into Equation (10) to Equation (12). In this way, the symbol demodulation is performed.

Summarizing, the process of calculating soft decision values by the dual minimum metric procedure includes a first step of determining the first parameter α and the second parameter β by analyzing a 2-dimensional received signal comprised of an in-phase signal component and a quadrature-phase signal component, and a second step of calculating soft decision values using the 2-dimensional received signal and the first parameter α and the second parameter β determined in the first step. The determined soft decision values of a demodulated symbol are provided to a channel decoder.

FIG. 3 illustrates a calculator for determining soft decision values of a

- 13 -

demodulated symbol according to an embodiment of the present invention. Referring to FIG. 3, the calculator for determining soft decision values by the dual minimum metric procedure in a digital communication system includes a received signal analyzer 10 and a soft decision value output unit 20. The received signal analyzer 10 determines first and second parameters α and β by analyzing a received signal comprised of an in-phase signal component X_k and a quadrature-phase signal component Y_k . The soft decision value output unit 20 then calculates soft decision values $\Lambda(s_{k,2})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,0})$ required for soft decision decoding, using the received signal and the determined parameters α and β .

10

A logic circuit of the calculator for calculating soft decision values in accordance with Equation (10) to Equation (12) is illustrated in FIG. 4. In particular, FIG. 4 illustrates a soft decision value calculator for use in a digital communication system employing 8-ary PSK. The logic circuit of FIG. 4 is included in a demodulator of the digital communication system employing 8-ary PSK, and calculates soft decision values using Equation (10) to Equation (12). Herein, the 2-dimensional received signal R_k , in-phase signal component X_k , quadrature-phase signal component Y_k , variable Z_k , parameter α , and parameter β are all real numbers, and digital values with a sign bit. In FIG. 4, a calculator 105, an inverter 115, a first MSB extractor 155, a first selector 110, a third MSB extractor 165 and a third selector 120 constitute a structure for determining the first parameter α . Further, the calculator 105, the inverter 115, a second MSB extractor 160, a second selector 135, the third MSB extractor 165 and a fourth selector 140 constitutes a structure for determining the second parameter β .

25

Referring to FIG. 4, the calculator 105 calculates $Z_k = |X_k| - |Y_k|$ using an in-phase signal component X_k and a quadrature-phase signal component Y_k of a 2-dimensional received signal R_k mapped to a k^{th} symbol. The inverter 115 inverts a sign of the Z_k by calculating the Z_k from the calculator 105 by “-1. The first MSB extractor 155 extracts an MSB of the Y_k received, and provides the extracted MSB to the first selector 110 as a first select signal. The second MSB extractor 160 extracts an MSB of the X_k received, and provides the extracted MSB to the second selector 135 as a second select signal. The third MSB extractor 165 extracts an MSB of the Z_k received from the calculator 105, and provides the extracted MSB to the third selector 120 as a third select signal. In addition, the Y_k is multiplied by $\sqrt{2}$ at a first multiplier 130, and the X_k is also multiplied by $\sqrt{2}$ at a second multiplier 150.

35

- 14 -

The first selector 110 receives the Z_k from the calculator 105 and the “ $-Z_k$ ” from the inverter 115, and selects one of the inputs according to the first select signal from the first MSB extractor 155. The third selector 120 then receives an output of the first selector 110 and a bit “0”, and selects one of the inputs according to the third select signal from the third MSB extractor 165. An output of the third selector 120 is added to an output value $\sqrt{2}Y_k$ of the first multiplier 130 by a first adder 125, generating a third soft decision value $\Lambda(s_{k,2})$ of the received signal R_k mapped to a k^{th} symbol.

In addition, the second selector 135 receives the Z_k from the calculator 105 and the “ $-Z_k$ ” from the inverter 115, and selects one of the inputs according to the second select signal from the second MSB extractor 160. The fourth selector 140 then receives an output of the second selector 135 and a bit “0”, and selects one of the inputs according to the third select signal from the third MSB extractor 165. An output of the fourth selector 140 is added to an output value $\sqrt{2}X_k$ of the second multiplier 150 by a second adder 145, thus generating a second soft decision value $\Lambda(s_{k,1})$ of the received signal R_k mapped to the k^{th} symbol.

Meanwhile, the Z_k output from the calculator 105 becomes a first soft decision value $\Lambda(s_{k,0})$ of the received signal R_k mapped to the k^{th} symbol.

According to the foregoing description, a conventional soft decision value calculator using the dual minimum metric procedure realized by Equation (5) needs ten or more squaring operations and comparison operations. However, the novel calculator of FIG. 4 realized using Equation (10) to Equation (12) is comprised of 3 adders, 3 multipliers, and 4 multiplexers, contributing to a remarkable reduction in operation time and complexity of the calculator. Table 7 below illustrates a comparison made between Equation (5) and Equations (10) to (12) in terms of the type and number of operations, for $i \in \{0, 1, 2\}$.

Table 7

Equation (4)		Equations (10) to (12)	
Operation	No of Operations	Operation	No of Operations
Addition	$3 \times 8 + 3 = 27$	Addition	3
Squaring	$2 \times 8 = 16$	Multiplication	3

- 15 -

Comparison	$3 \times 2 \times 3 = 18$	Multiplexing	4
------------	----------------------------	--------------	---

In sum, the present invention derives Table 6 to Table 11 from Equation (6) to Equation (8) and the process of Table 1 to Table 5, in order to reduce a time delay and complexity, which may occur when Equation (4), the known dual minimum metric procedure, or Equation (5), obtained by simplifying the dual minimum metric procedure, is actually realized using the 16-ary QAM. Further, the present invention provides Equation (9) and Equation (10), new formulas used to realize the dual minimum metric procedure in the 16-ary QAM. In addition, the present invention provides a hardware device realized based on Equation (9) and Equation (10).

Now, a comparison will be made between a conventional method of calculating a soft decision value $\Lambda(s_{k,2})$ using Equation (5) and a new method of calculating the soft decision value $\Lambda(s_{k,2})$ using Equation (10). FIG. 5 illustrates a signal constellation having mapping points according to the 8-ary PSK, for explanation of calculations. Referring to FIG. 5, a 2-dimensional received signal R_k comprised of an in-phase signal component X_k and a quadrature-phase signal component Y_k has a coordinate value represented by "x." Herein, it will be assumed that $X_k = -0.6$ and $Y_k = -0.1$.

First, a conventional process of calculating a soft decision value $\Lambda(s_{k,2})$ using Equation (5) will be described.

The square of each distance between a received signal R_k and 4 mapping points with $s_{k,2} = 1$ (i.e., 4 mapping points under an x-axis in FIG. 5) is first calculated to determine the shortest distance.

$$\text{The square of a distance from a mapping point "110"} = \{-0.6 - \cos(9\pi/8)\}^2 + \{-0.1 - \sin(9\pi/8)\}^2 = 0.185$$

$$\text{The square of a distance from a mapping point "111"} = \{-0.6 - \cos(11\pi/8)\}^2 + \{-0.1 - \sin(11\pi/8)\}^2 = 0.726$$

$$\text{The square of a distance from a mapping point "101"} = \{-0.6 - \cos(13\pi/8)\}^2 + \{-0.1 - \sin(13\pi/8)\}^2 = 1.644$$

- 16 -

The square of a distance from a mapping point "100" = $\{-0.6-\cos(15\pi/8)\}^2 + \{-0.1-\sin(15\pi/8)\}^2 = 2.402$

5 Therefore, the minimum value (or the shortest distance from the received signal R_k) $|R_k - z_k(s_{k,2}=1)|^2$ is 0.185.

Then, the square of each distance between the received signal R_k and 4 mapping points with $S_{k,2}=0$ (i.e., 4 mapping points over the x -axis in FIG. 5) is calculated to determine the shortest distance.

10

The square of a distance from a mapping point "000" = $\{-0.6-\cos(\pi/8)\}^2 + \{-0.1-\sin(\pi/8)\}^2 = 2.555$

15 The square of a distance from a mapping point "001" = $\{-0.6-\cos(3\pi/8)\}^2 + \{-0.1-\sin(3\pi/8)\}^2 = 2.014$

The square of a distance from a mapping point "011" = $\{-0.6-\cos(5\pi/8)\}^2 + \{-0.1-\sin(5\pi/8)\}^2 = 1.096$

20 The square of a distance from a mapping point "010" = $\{-0.6-\cos(7\pi/8)\}^2 + \{-0.1-\sin(7\pi/8)\}^2 = 0.338$

Therefore, the minimum distance of $|R_k - z_k(s_{k,2}=1)|^2$ is 0.338.

25 If the above results are substituted into Equation (5), then the soft decision value becomes

$$\begin{aligned} \Lambda(s_{k,2}) &= K' \left[\min |R_k - z_k(s_{k,2}=1)|^2 - \min |R_k - z_k(s_{k,2}=0)|^2 \right] \\ &= K' \times (0.185 - 0.338) \\ &= -0.153 \times K' \end{aligned}$$

30

Next, a new process of calculating a soft decision value $\Lambda(s_{k,2})$ using Equation (10) will be described.

35 Z_k and α are first calculated.

- 17 -

$$Z_k = |X_k| - |Y_k| = |-0.6| - |-0.1| = 0.5$$

From this, since $Z_k \geq 0$, i.e., $\text{MSB}(Z_k) = 0$, $\alpha = 0$.

5 If the above results are substituted into Equation (10), then the soft decision value becomes

$$\Lambda(s_{k,2}) = \sqrt{2}Y_k + \alpha \cdot Z_k = \sqrt{2} \times (-0.1) + 0 \times 0.5 = -0.141$$

10 Here, the reason that the result of Equation (5) is different from the result of Equation (10) is because a soft decision value calculated by Equation (9) was normalized by $K'(\sqrt{(2+\sqrt{2})} - \sqrt{(2-\sqrt{2})})$. In the case of a turbo decoder using a max-logMAP core (currently, both L3QS and 1xTREME use max-logMAP core), normalizing all LLR values (or soft values) using the same coefficient never
15 affects performance.

If a coefficient is actually multiplied to calculate a non-normalized value, then

$$20 \quad -0.141 \times K'(\sqrt{(2+\sqrt{2})} - \sqrt{(2-\sqrt{2})}) = -0.141 \times 1.082 \times K' = -0.153 \times K'$$

It is noted that the calculated non-normalized value is identical to the result of Equation (5).

25 Summarizing, in order to reduce a time delay and complexity caused by the use of the dual minimum metric procedure of Equation (5), the present invention draws the mapping tables of Table 4 to Table 6 through the process of Equation (6) to Equation (9) and Table 1 to Table 3. Further, the present invention substitutes the mapping tables into Equation (10) to Equation (12), the dual
30 minimum metric procedure realizing formulas. In addition, the present invention provides a logic circuit of a calculator for calculating 8-ary PSK soft decision values, realized by Equation (10) to Equation (12).

As described above, in deriving a soft decision value needed as an input

- 18 -

to a channel decoder by the dual minimum metric procedure, the novel demodulator for a digital communication system employing 8-ary PSK modulation enables simple and rapid calculations, contributing to a remarkable reduction in an operation time and complexity of the demodulator that calculates
5 soft decision values.

While the invention has been shown and described with reference to a certain preferred embodiment thereof, it will be understood by those skilled in the art that various changes in form and details may be made therein without
10 departing from the spirit and scope of the invention as defined by the appended claims.

WHAT IS CLAIMED IS:

1. An 8-ary PSK (Phase Shift Keying) demodulation apparatus for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(s_{k,0})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,2})$ for the input signal $R_k(X_k, Y_k)$ by a soft decision means, comprising:

a received signal analyzer for calculating a function Z_k of the input signal $R_k(X_k, Y_k)$ according to an equation $Z_k = |X_k| - |Y_k|$, and determining a first parameter α and a second parameter β by the input signal; and

a soft decision value output unit for calculating the soft decision values for the input signal $R_k(X_k, Y_k)$, using the first parameter α , the second parameter β , and the received signal $R_k(X_k, Y_k)$, according to

$$\Lambda(s_{k,2}) = \sqrt{2}Y_k + \alpha \cdot Z_k, \text{ where } \alpha = \begin{cases} 0 & \text{if } Z_k \geq 0 \\ -1 & \text{if } Z_k < 0 \text{ and } Y_k \geq 0 \\ 1 & \text{if } Z_k < 0 \text{ and } Y_k < 0 \end{cases}$$

$$\Lambda(s_{k,1}) = \sqrt{2}X_k + \beta \cdot Z_k, \text{ where } \beta = \begin{cases} 0 & \text{if } Z_k < 0 \\ -1 & \text{if } Z_k \geq 0 \text{ and } X_k < 0 \\ 1 & \text{if } Z_k \geq 0 \text{ and } X_k \geq 0 \end{cases}$$

$$\Lambda(s_{k,0}) = Z_k$$

where $\Lambda(s_{k,i})$ indicates a soft decision value corresponding to $s_{k,i}$ ($i=0,1,2$), and $s_{k,i}$ indicates an i^{th} bit in a coded signal sequence mapped to a k^{th} symbol.

2. An 8-ary PSK (Phase Shift Keying) demodulation method for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(s_{k,0})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,2})$ for the input signal $R_k(X_k, Y_k)$ by a soft decision means, comprising the steps of:

calculating a soft value Z_k of the input signal $R_k(X_k, Y_k)$ according to equation $Z_k = |X_k| - |Y_k|$, and determining a first parameter α and a second parameter β by the input signal; and

- 20 -

calculating the soft decision values for the input signal $R_k(X_k, Y_k)$, using the first parameter α , the second parameter β , and the received signal $R_k(X_k, Y_k)$, according to

$$\Lambda(s_{k,2}) = \sqrt{2}Y_k + \alpha \cdot Z_k, \text{ where } \alpha = \begin{cases} 0 & \text{if } MSB(Z_k) = 0 \\ -1 & \text{if } MSB(Z_k) = 1 \text{ and } MSB(Y_k) = 0 \\ 1 & \text{if } MSB(Z_k) = 1 \text{ and } MSB(Y_k) = 1 \end{cases}$$

$$5 \quad \Lambda(s_{k,1}) = \sqrt{2}X_k + \beta \cdot Z_k, \text{ where } \beta = \begin{cases} 0 & \text{if } MSB(Z_k) = 1 \\ -1 & \text{if } MSB(Z_k) = 0 \text{ and } MSB(X_k) = 1 \\ 1 & \text{if } MSB(Z_k) = 0 \text{ and } MSB(X_k) = 0 \end{cases}$$

$$\Lambda(s_{k,0}) = Z_k$$

where $\Lambda(s_{k,i})$ indicates a soft decision value corresponding to $s_{k,i}$ ($i=0,1,2$), and $s_{k,i}$ indicates an i^{th} bit in a coded signal sequence mapped to a k^{th} symbol.

10 3. An 8-ary PSK (Phase Shift Keying) demodulation apparatus for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(s_{k,0})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,2})$ for the input signal $R_k(X_k, Y_k)$ by a soft decision means, comprising:

15 a calculator for calculating a soft value Z_k by subtracting a level $|Y_k|$ of the quadrature-phase signal component Y_k from a level $|X_k|$ of the in-phase signal component X_k of the received signal $R_k(X_k, Y_k)$, and outputting the Z_k as a first soft decision value;

20 a first selector for receiving the Z_k from the calculator and an inverted value $-Z_k$ of the Z_k , and selecting the Z_k or the $-Z_k$ according to a most significant bit (MSB) of the quadrature-phase signal component Y_k ;

 a second selector for receiving the Z_k from the calculator and the $-Z_k$, and selecting the Z_k or the $-Z_k$ according to an MSB of the in-phase signal component X_k ;

25 a third selector for receiving an output of the second selector and a value "0", and selecting the output of the second selector or the value "0" according to an MSB of the Z_k ;

30 a first adder for adding a value calculated by multiplying the quadrature-phase signal component Y_k by $\sqrt{2}$ to an output of the third selector, and outputting a result value as a third soft decision value;

- 21 -

a fourth selector for receiving an output of the second selector and a value “0”, and selecting the output of the second selector or the value “0” according to the MSB of the Z_k ; and

5 a second adder for adding a value calculated by multiplying the in-phase signal component X_k by $\sqrt{2}$ to an output of the fourth selector, and outputting a result value as a second soft decision value.

10 4. An 8-ary PSK (Phase Shift Keying) demodulation method for receiving an input signal $R_k(X_k, Y_k)$ comprised of a k^{th} quadrature-phase component Y_k and a k^{th} in-phase component X_k , and for generating soft decision values $\Lambda(s_{k,0})$, $\Lambda(s_{k,1})$, and $\Lambda(s_{k,2})$ for the input signal $R_k(X_k, Y_k)$ by a soft decision means, comprising the steps of:

15 (a) calculating a soft value Z_k of a first demodulated symbol by subtracting a level $|Y_k|$ of the quadrature-phase signal component Y_k from a level $|X_k|$ of the in-phase signal component X_k of the received signal $R_k(X_k, Y_k)$;

20 (b) setting a first variable α to “0” if the soft value Z_k has a positive value, setting the first variable α to “-1” if the Z_k has a negative value and the quadrature-phase component Y_k has a positive value, and setting the first variable α to “1” if the Z_k has a negative value and the quadrature-phase component Y_k has a negative value;

(c) determining a soft value of a third demodulated symbol by calculating $\sqrt{2} Y_k + \alpha * Z_k$ using the quadrature-phase component Y_k , the soft value Z_k and the first variable α ;

25 (d) setting a second variable β to “0” if the soft value Z_k has a negative value, setting the second variable β to “-1” if the Z_k has a positive value and the in-phase component X_k has a negative value, and setting the second variable β to “1” if the Z_k has a positive value and the in-phase component X_k has a positive value; and

30 (e) determining a soft value of a second demodulated symbol by calculating $\sqrt{2} X_k + \beta * Z_k$ using the in-phase component X_k , the soft value Z_k and the second variable β .

1/5

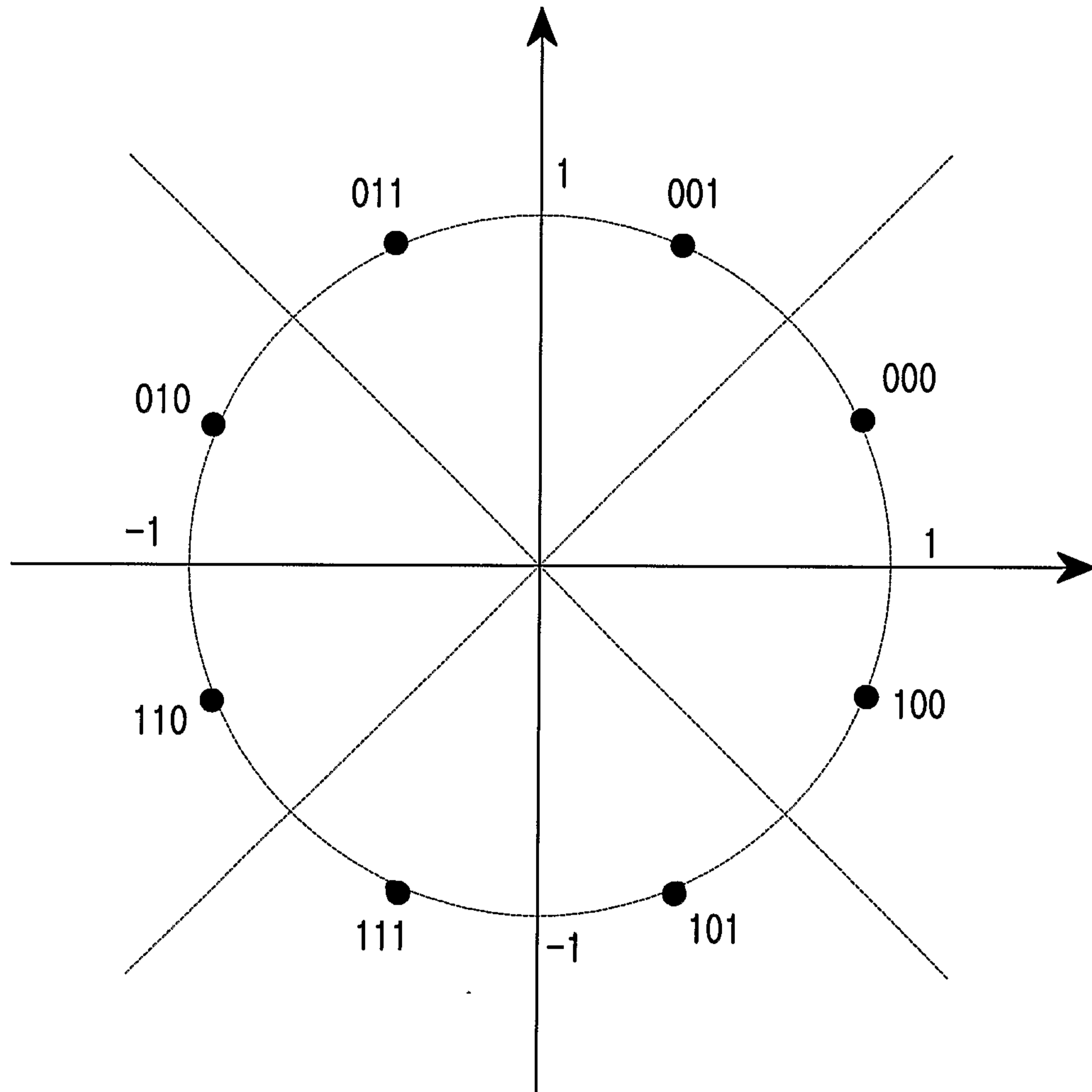


FIG. 1

2/5

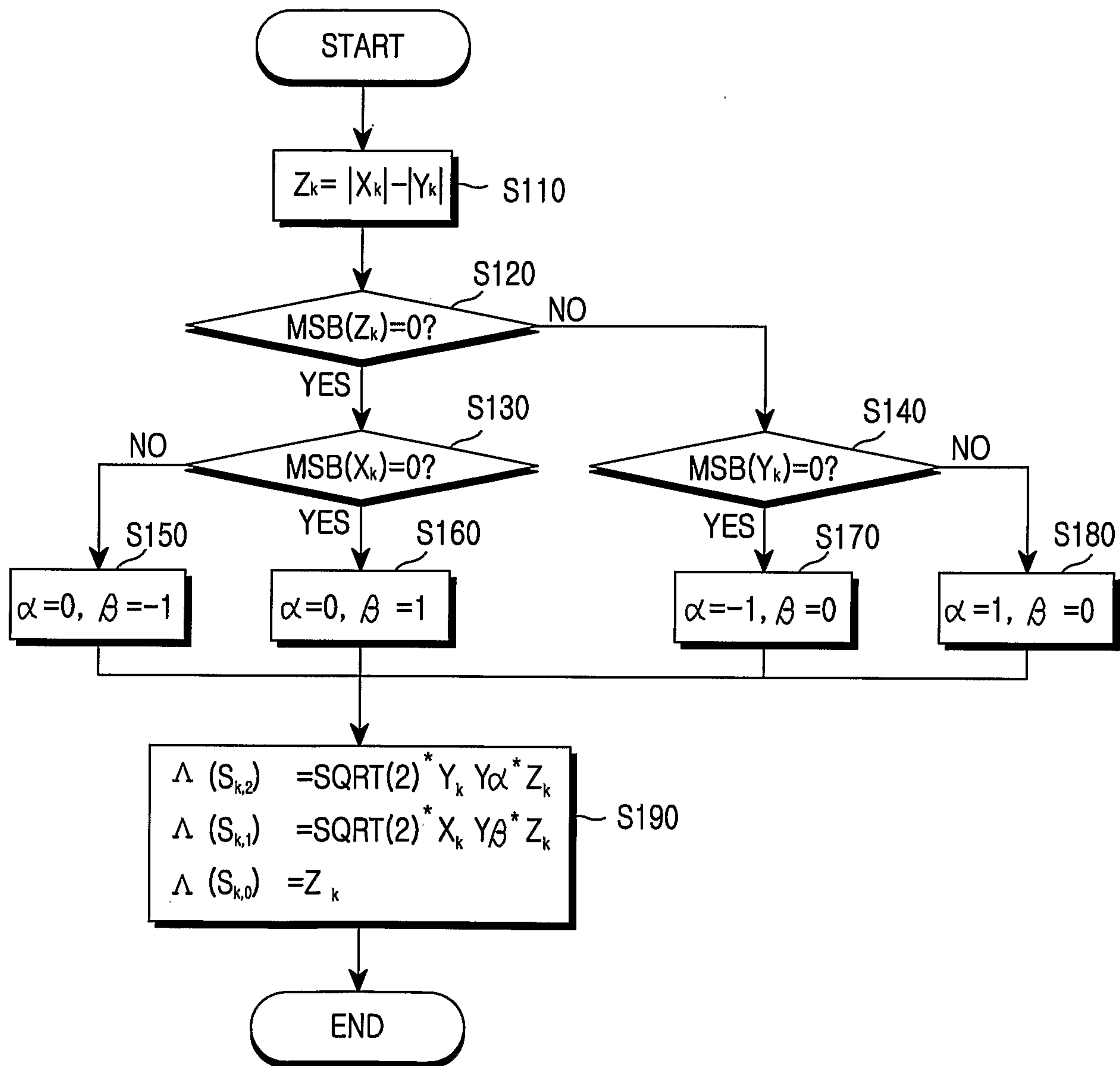


FIG. 2

3/5

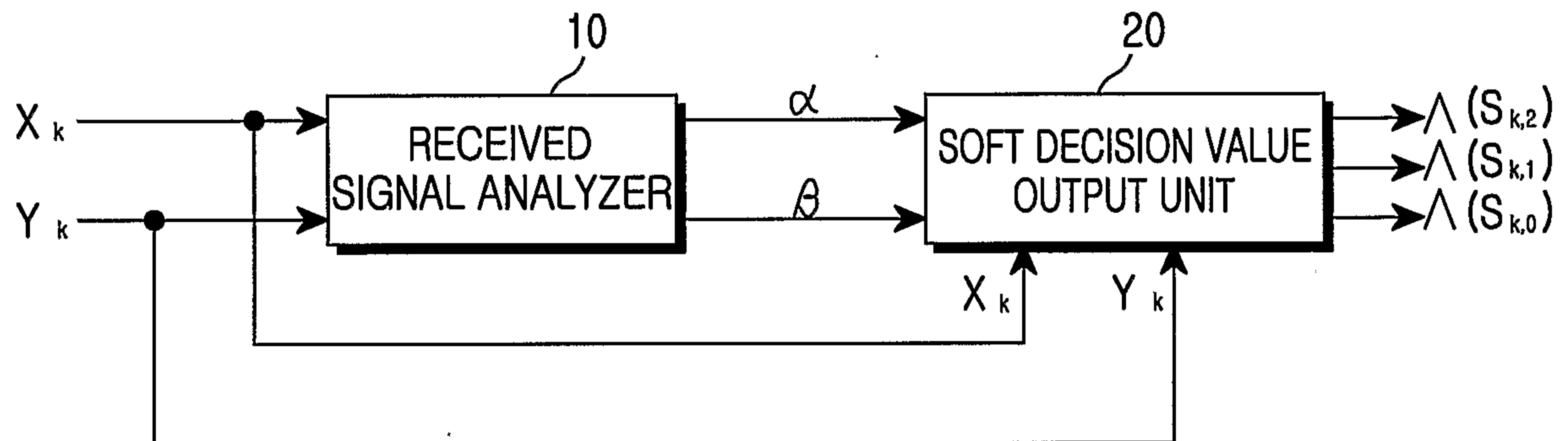


FIG. 3

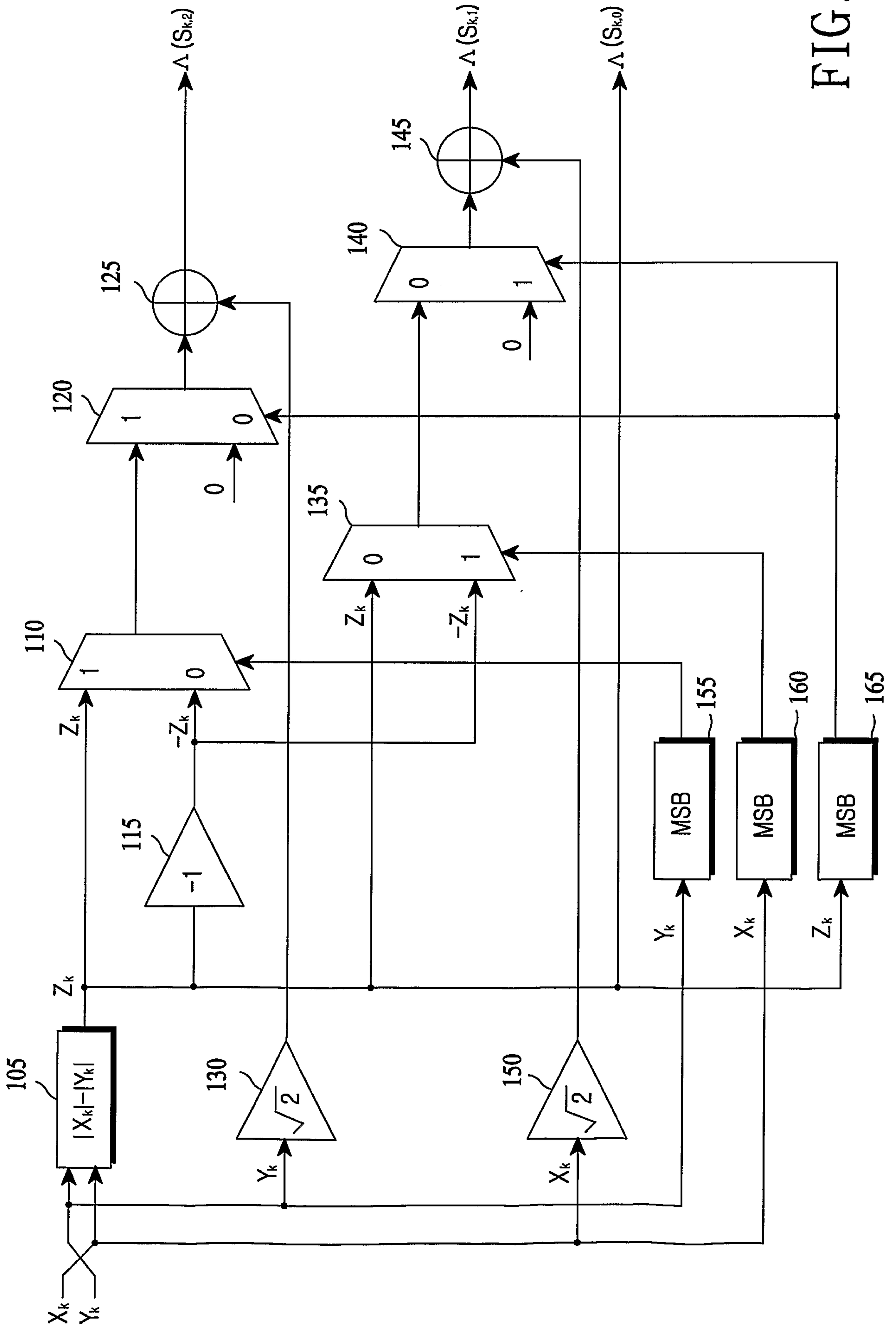


FIG. 4

5/5

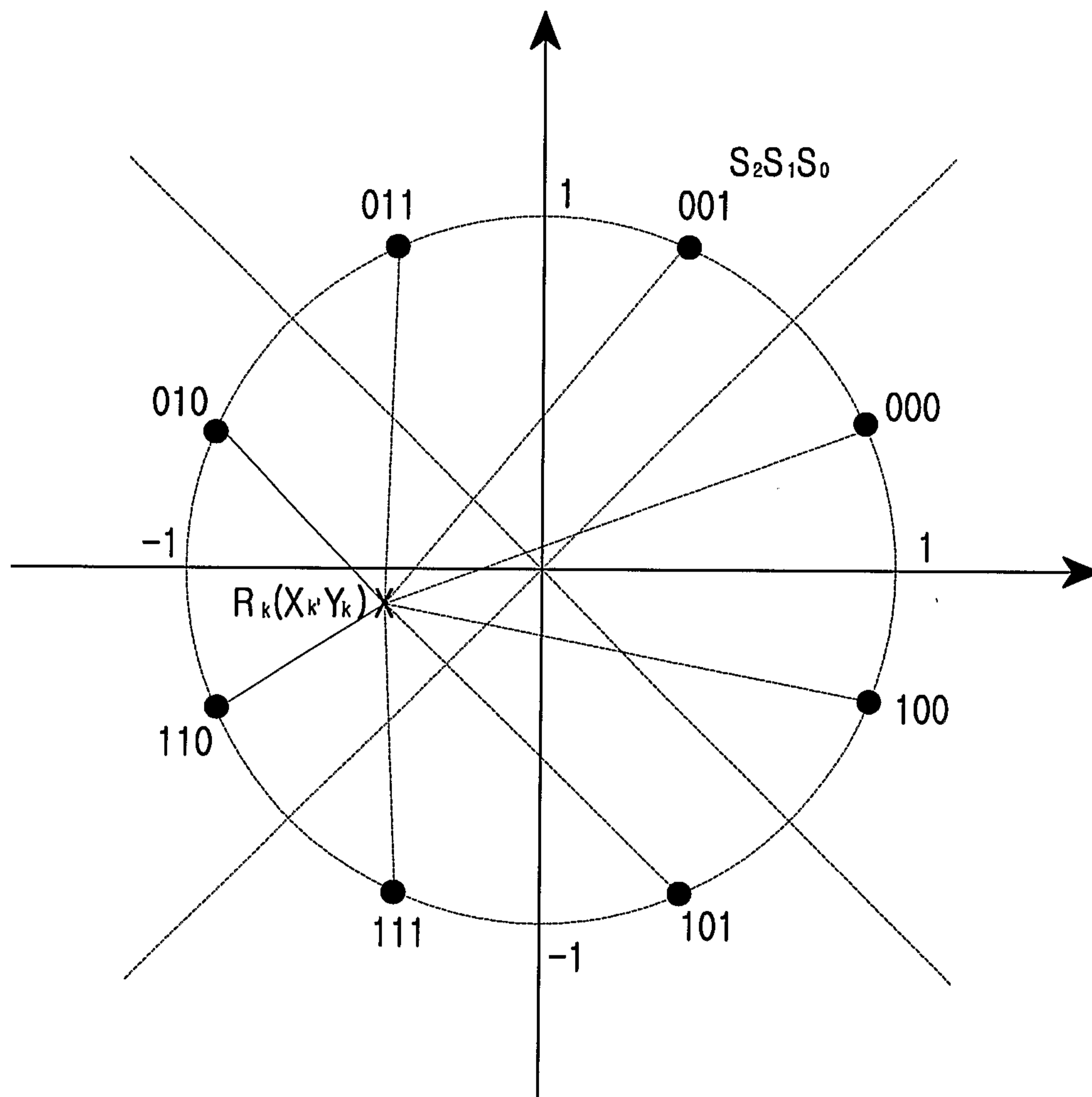


FIG. 5

