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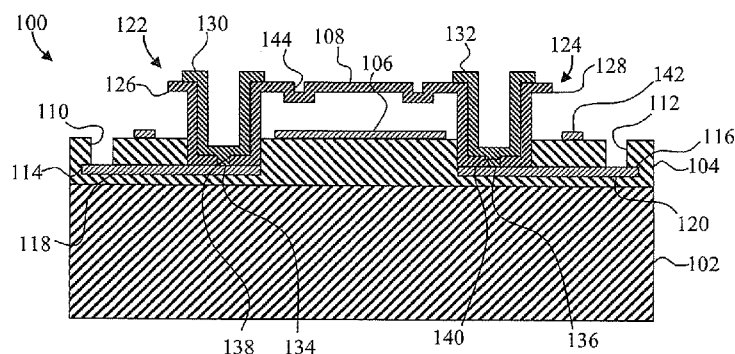


FIG. 1

(57) Abstract: In one embodiment, a method of forming a semiconductor device includes providing a substrate, forming a sacrificial layer above the substrate layer, forming a first trench in the sacrificial layer, forming a first sidewall layer with a thickness of less than about 50 nm on a first sidewall of the first trench using atomic layer deposition (ALD), and removing the sacrificial layer.

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According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) B81C G01J H01L B81B		
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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	YONEOKA S ET AL: "ALD-metal uncooled bolometer", IEEE 24TH INTERNATIONAL CONFERENCE ON MICRO ELECTRO MECHANICAL SYSTEMS (MEMS 2011), IEEE, US, 23 January 2011 (2011-01-23), pages 676-679, XP031982502, DOI: 10.1109/MEMSYS.2011.5734515 ISBN: 978-1-4244-9632-7 figures 1-3,6	1-20
A	----- US 2010/171190 A1 (LIGER MATTHIEU [US]) 8 July 2010 (2010-07-08) figures 1-3,5-9 -----	1-20
☐ Further documents are listed in the continuation of Box C. ☒ See patent family annex.		
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