

CORRECTED VERSION

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
30 July 2009 (30.07.2009)

PCT

(10) International Publication Number
WO 2009/092152 A8

(51) International Patent Classification:
G11C 16/06 (2006.01)

(21) International Application Number:
PCT/CA2008/002155

(22) International Filing Date:
15 December 2008 (15.12.2008)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
61/022,656 22 January 2008 (22.01.2008) US
12/286,959 3 October 2008 (03.10.2008) US

(71) Applicant (for all designated States except US): **MO-SAID TECHNOLOGIES INCORPORATED**
[CA/CA]; 11 Hines Road, Suite 203, Ottawa, Ontario K2K 2X1 (CA).

(72) Inventor; and

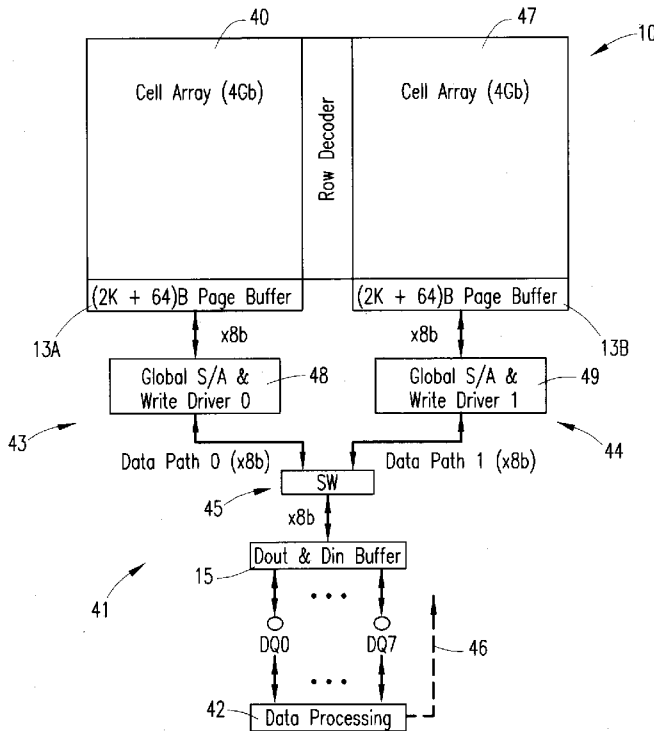
(75) Inventor/Applicant (for US only): **KIM, Jin-Ki**
[CA/CA]; 46 Ironside Court, Kanata, Ontario K2K 3H6 (CA).

(74) Agents: **HAMMOND, Daniel** et al.; Mosaid Technologies Incorporated, 11 Hines Road, Suite 203, Kanata, Ontario K2K 2X1 (CA).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

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(54) Title: NAND FLASH MEMORY ACCESS WITH RELAXED TIMING CONSTRAINTS



(57) Abstract: Timing constraints on data transfers during access of a NAND flash memory can be relaxed by providing a plurality of data paths that couple the NAND flash memory to a buffer that provides external access to the memory. The buffer defines a bit width associated with the external access, and each of the data paths accommodates that bit width.

FIG. 4

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(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

— *of inventorship (Rule 4.17(iv))*

Published:

— *with international search report (Art. 21(3))*

(48) Date of publication of this corrected version:

8 October 2009

(15) Information about Correction:

see Notice of 8 October 2009

Declarations under Rule 4.17:

— *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))*