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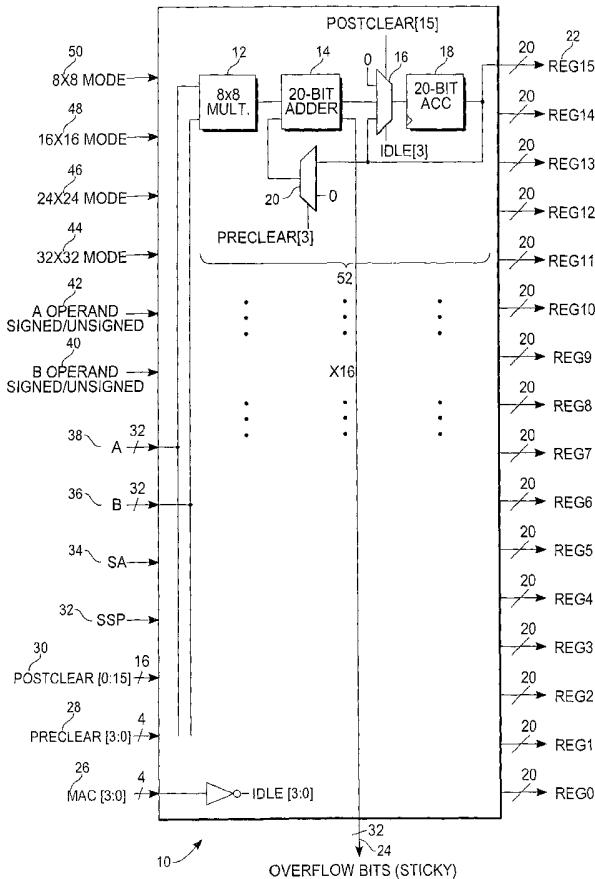
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(54) Title: METHOD AND SYSTEM FOR PERFORMING OPERATIONS ON DATA AND TRANSFERRING DATA



(57) Abstract: Methods and systems for performing operations on data, such as parallel integer multiply accumulate operations on packed data and SIMD operations, and transferring data, are provided. A multiple accumulate unit ("MAC") (10) performs operations on packed integer data and allows results to be individually retrieved from registers (22) and the corresponding accumulator (18) cleared after the read cycle. SIMD instructions, including parallel absolute value and parallel conditional move instructions, as well as a method and circuit for saturating results of operations are provided. Scatter/gather mode for DMA transfer of data provides a table of buffer descriptors which may be used in automatic buffer switching and a table of packet descriptors describing location and size of incoming data, thus decoding packet sizes from buffer sizes.

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Declarations under Rule 4.17:

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- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations*

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/24667

<p>A. CLASSIFICATION OF SUBJECT MATTER</p> <p>IPC(7) : G06F 7/48</p> <p>US CL : 708/523,513</p> <p>According to International Patent Classification (IPC) or to both national classification and IPC</p>																	
<p>B. FIELDS SEARCHED</p> <p>Minimum documentation searched (classification system followed by classification symbols)</p> <p>U.S. : 708/523,513,501</p> <p>Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched</p> <p>Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)</p>																	
<p>C. DOCUMENTS CONSIDERED TO BE RELEVANT</p> <table border="1"> <thead> <tr> <th>Category *</th> <th>Citation of document, with indication, where appropriate, of the relevant passages</th> <th>Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td>A</td> <td>US 2003/0097391 A (SAULSBURY) 22 May 2003, see the entire document</td> <td>1-22</td> </tr> <tr> <td>A</td> <td>US 6,385,634 A (PELEG et al) 7 May 2002, see the entire document</td> <td>1-22</td> </tr> <tr> <td>A</td> <td>US 6,282,556 A (CHEHRAZI et al) 28 August 2001, see the entire document</td> <td>1-22</td> </tr> <tr> <td>A</td> <td>US 6,230,180 A (MOHAMED) 8 May 2001, see the entire document</td> <td>1-22</td> </tr> </tbody> </table>			Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	A	US 2003/0097391 A (SAULSBURY) 22 May 2003, see the entire document	1-22	A	US 6,385,634 A (PELEG et al) 7 May 2002, see the entire document	1-22	A	US 6,282,556 A (CHEHRAZI et al) 28 August 2001, see the entire document	1-22	A	US 6,230,180 A (MOHAMED) 8 May 2001, see the entire document	1-22
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<p>Name and mailing address of the ISA/US</p> <p>Mail Stop PCT, Attn: ISA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450</p> <p>Facsimile No. (703) 305-3230</p>		<p>Authorized officer</p> <p>David H. Malzahn</p> <p>Telephone No. (703) 305-3900</p>															

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Box IV TEXT OF THE ABSTRACT (Continuation of Item 5 of the first sheet)

The technical features mentioned in the abstract do not include a reference sign between parentheses (PCT Rule 8.1(d)).

NEW ABSTRACT

Methods and systems for performing operations on data, such as parallel integer multiply accumulate operations on packed data and SIMD operations, and transferring data, are provided. A multiply accumulate unit ("MAC") (10) performs operations on packed integer data and allows results to be individually retrieved from registers (22) and the corresponding accumulator (18) cleared after the read cycle. SIMD instructions, including parallel absolute value and parallel conditional move instructions, as well as a method and circuit for saturating results of operations are provided. Scatter/gather mode for DMA transfer of data provides a table of buffer descriptors which may be used in automatic buffer switching and a table of packet descriptors describing location and size of incoming data, thus decoding packet sizes from buffer sizes.