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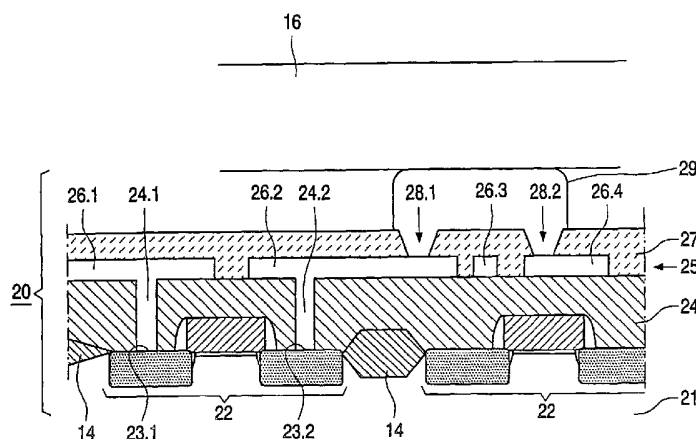
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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: INTEGRATED CIRCUIT DEVICE WITH BUMP BRIDGES AND METHOD FOR MAKING THE SAME



(57) Abstract: Text Integrated circuit device (20) comprising a silicon substrate (21), integrated devices (22) with contacts (23.1, 23.2), an isolating layer (24) at least partially covering the integrated devices (22) and comprising conducting areas (24.1, 24.2) which establish a conductive path to the contacts (23.1, 23.2) of the integrated devices (22). A metallization level (25) with metal lines (26.1, 26.2, 26.3, 26.4) is provided which connect to one of the contacts (23.2). The metal lines (26.1, 26.2, 26.3, 26.4) are situated above the isolating layer (24). A passivation layer (27) - situated above the metallization level (25) - comprises at least two contact areas (28.1, 28.2) for partially exposing at least two of the metal lines (26.2, 26.4). A bump bridge (29) comprising a conductive, low-resistance material, is situated on the passivation layer (27). The bump bridge (29) has a high aspect ratio and provides for a conductive connection between at least two of the metal lines (26.2, 26.4). It crosses another metal line (26.3) that is situated within the metallization level (25), without making contact to this metal line (26.3), and a substantial part of the bump bridge (29) is supported by the passivation layer (27).



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## A. CLASSIFICATION OF SUBJECT MATTER

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According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ, INSPEC

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 182 629 A (NOWAK MATTHEW M ET AL) 26 January 1993 (1993-01-26) column 2, line 40 -column 3, line 16 column 6, line 4 -column 9, line 10 claims 1-15 figures 1,3-6 ---	1-5,8-19
Y	US 5 903 058 A (AKRAM SALMAN) 11 May 1999 (1999-05-11) column 2, line 23 - line 62 column 5, line 44 -column 7, line 8 column 8, line 32 - line 65 claims 1,2,6,7,11,12,14,17,19,23-25 claims 29,32-34,38,39 figures 1E,1H,2A-2E,3A-4E,8A-8J --- -/--	1-19



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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A	US 5 710 068 A (HILL DARRELL G) 20 January 1998 (1998-01-20)  column 1, line 48 -column 2, line 22 column 5, line 15 - line 21 figures 2,4J-4M ----	1-3, 8-13,15, 17-19
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A	EP 0 291 242 A (ADVANCED MICRO DEVICES INC) 17 November 1988 (1988-11-17) column 4, line 5 -column 9, line 52 claims 1,6 figures 1-9 -----	1,11-13

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